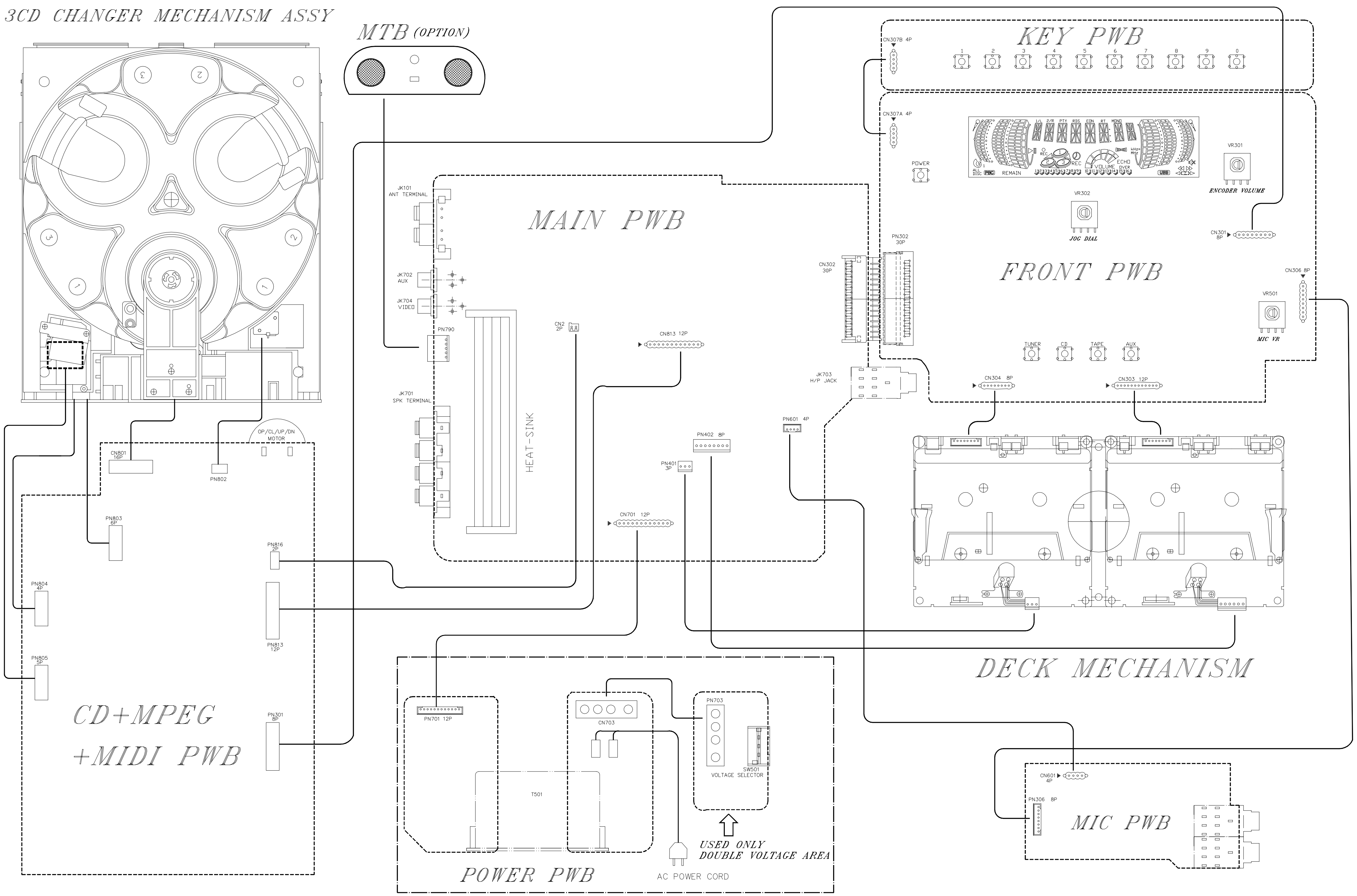
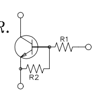
WIRING DIAGRAM



*KEC TR.

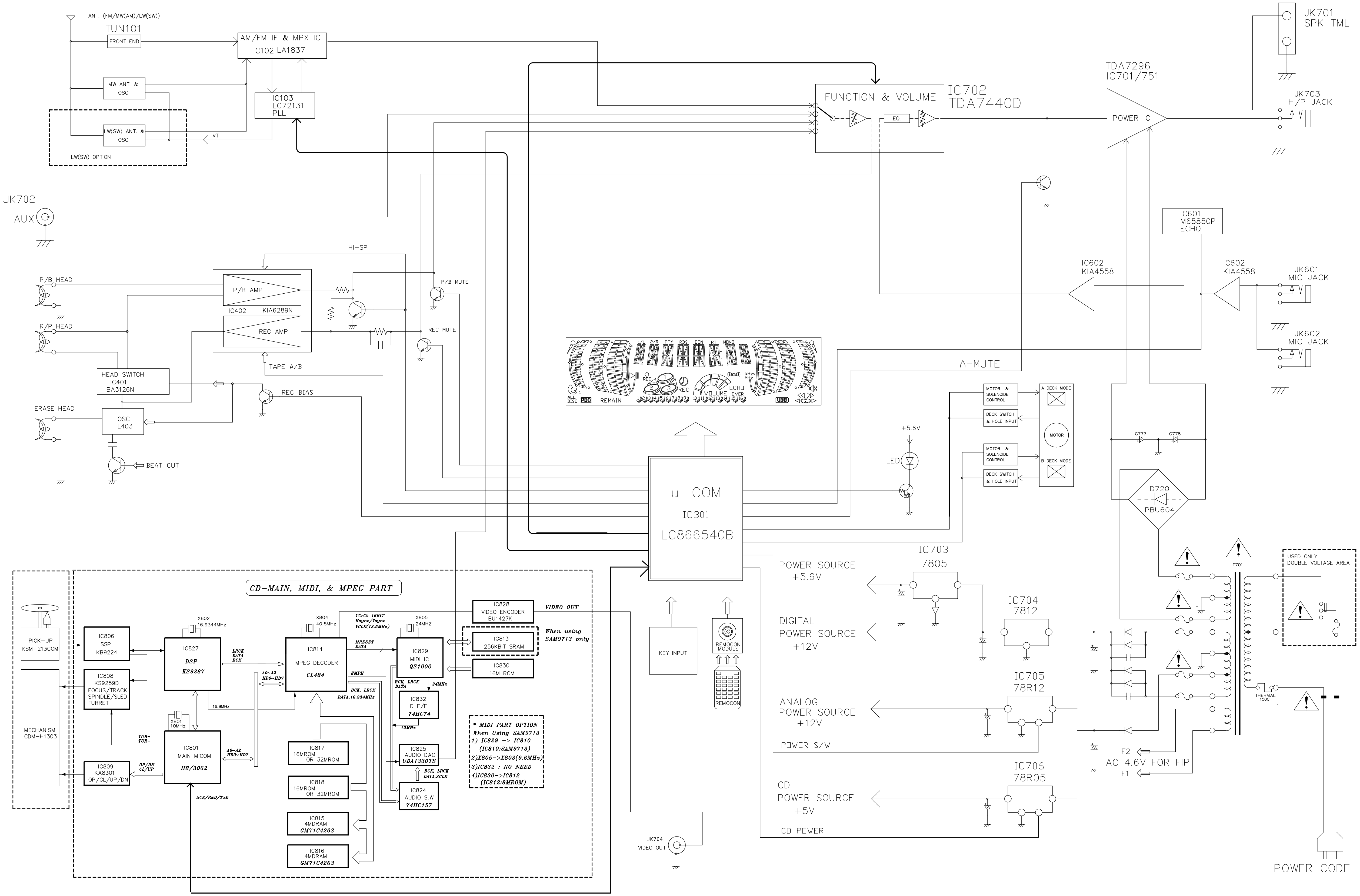


NAME	R1	R2
C102M	10K	10K
C103M	22K	22K
C104M	47K	47K
C110M	4.7K	

NAME	R1	R2
C111M	10K	

NOTES : **Resistance values are indicted in ohms unless otherwise specified (K=1,000, M=1,000,000).**
Capacitance values are shown in microfarads unless otherwise (P=MICRO-MICRO FARADS).
Schematic diagram for this model are subject to change for improvement without prior notice.

BLOCK DIAGRAM



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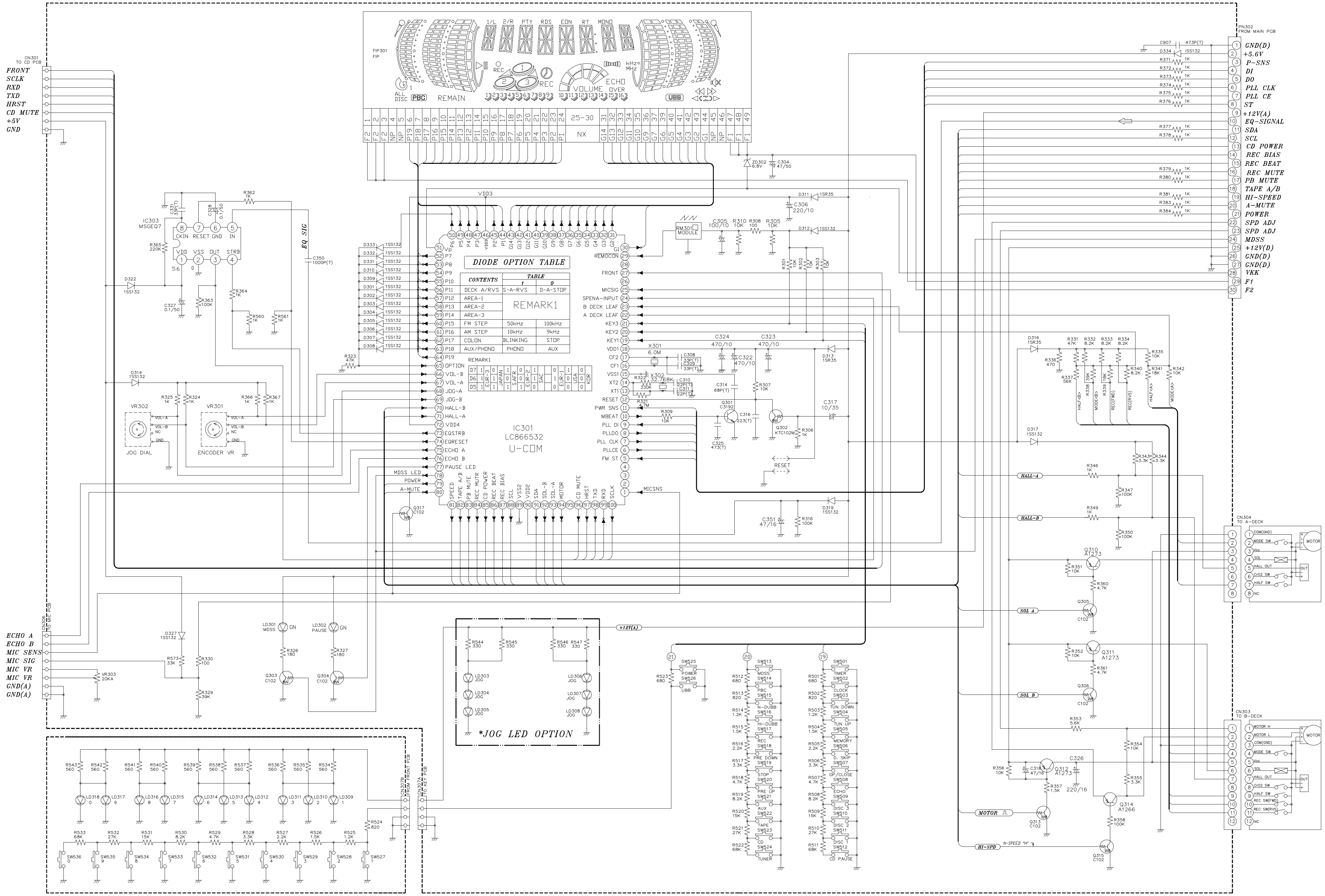
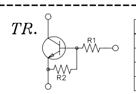
*KEC TR.

NAME	R1	R2	NAME	R1	R2
C102M	10K	10K	C111M	10K	
C103M	22K	22K			
C104M	47K	47K			
C110M	4.7K				

• FRONT



*KEC TR.



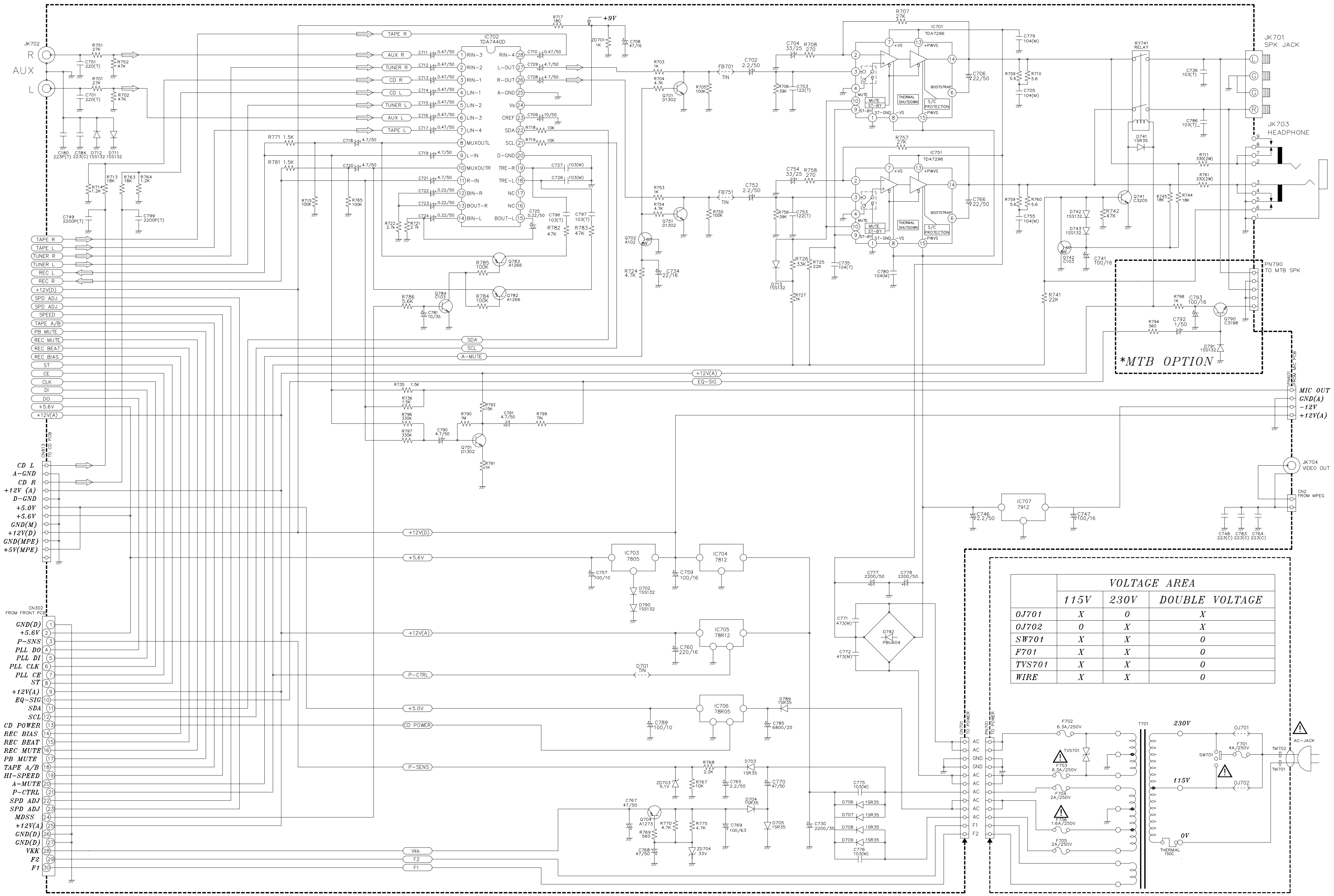
NAME	R1	R2
C102M	10K	10K
C103M	22K	22K
C104M	47K	47K
C110M	4.7K	

NAME	R1	R2
C111M	10K	

NOTES : **Resistance values are indicted in ohms unless otherwise specified (K=1,000, M=1,000,000).**
Capacitance values are shown in microfarads unless otherwise (P=MICRO-MICRO FARADS).
Schematic diagram for this model are subject to change for improvement without prior notice.

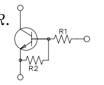
SCHEMATIC DIAGRAMS

• MAIN



	VOLTAGE AREA		
	115V	230V	DOUBLE VOLTAGE
0J701	X	0	X
0J702	0	X	X
SW701	X	X	0
F701	X	X	0
TVS701	X	X	0
WIRE	X	X	0

*KEC TR.



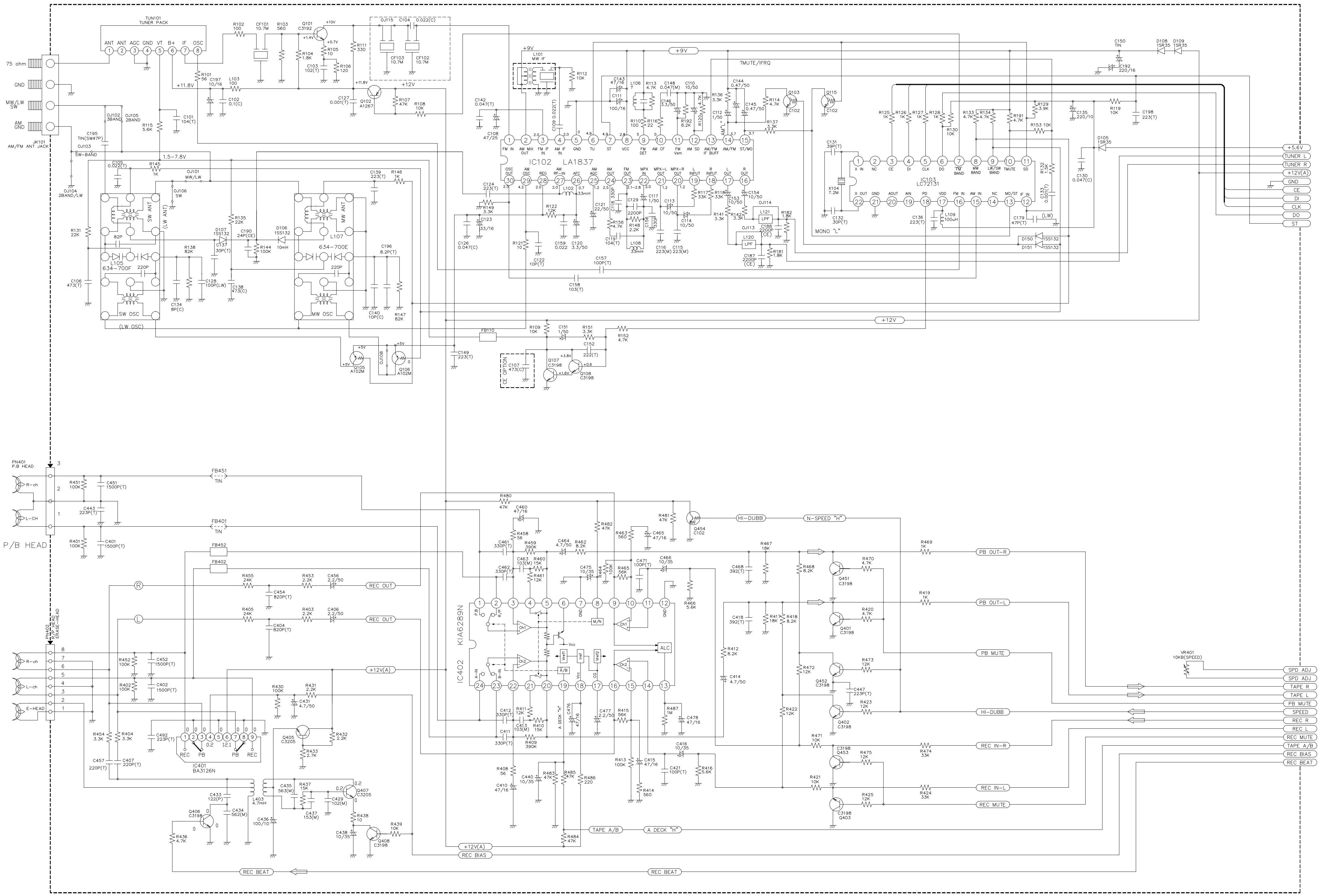
NAME	R1	R2
C102M	10K	10K
C103M	22K	22K
C104M	47K	47K
C110M	4.7K	

NAME	R1	R2
C111M	10K	

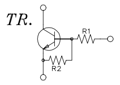
NOTES :

1. **Resistance values are indicted in ohms unless otherwise specified (K=1,000, M=1,000,000).**
2. **Capacitance values are shown in microfarads unless otherwise (P=MICRO-MICRO FARADS).**
3. **Schematic diagram for this model are subject to change for improvement without prior notice.**

• TUNER & DECK



*KEC TR.

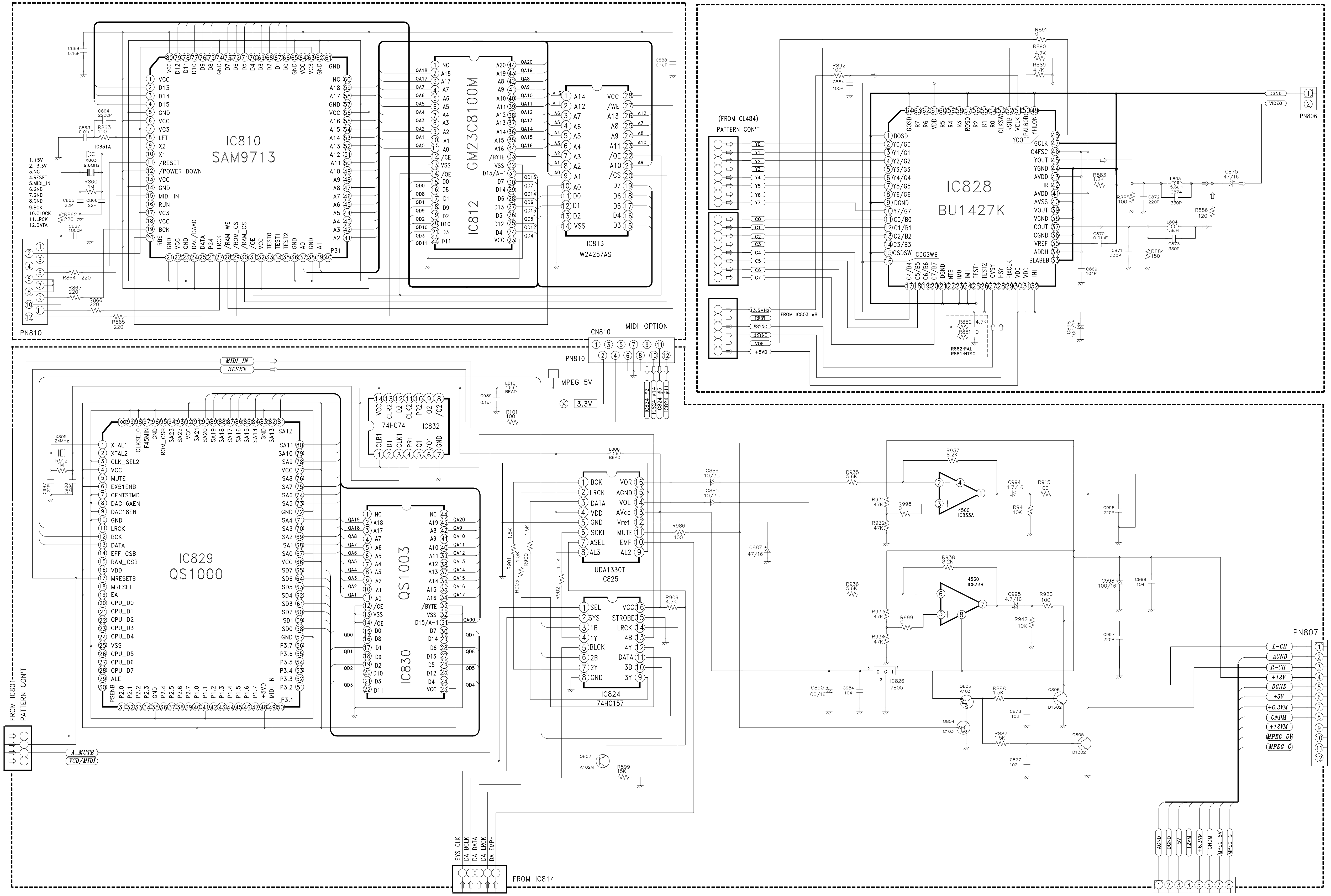


NAME	R1	R2
C102M	10K	10K
C103M	22K	22K
C104M	47K	47K
C110M	4.7K	

NAME	R1	R2
C111M	10K	

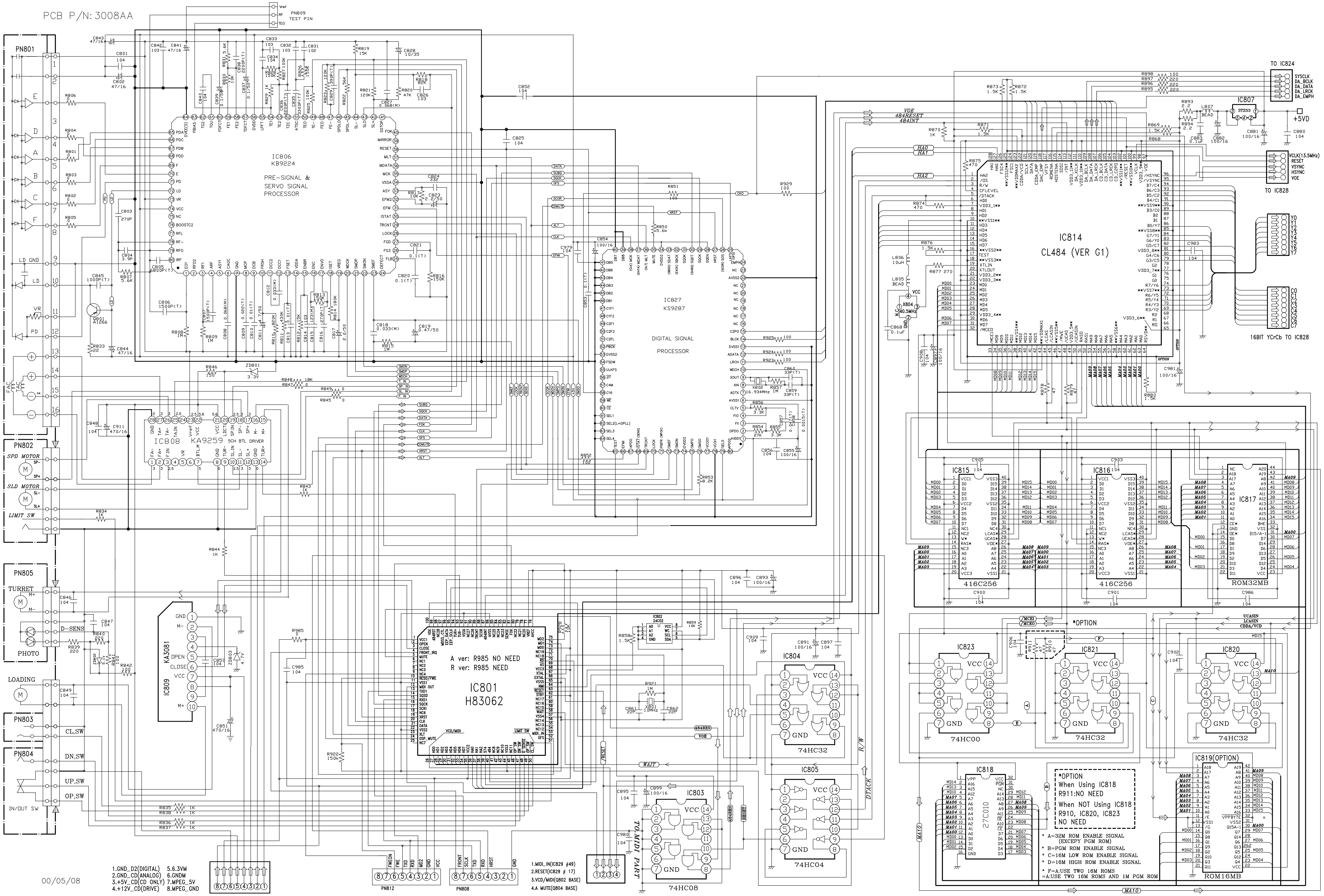
NOTES : **Resistance values are indicted in ohms unless otherwise specified (K=1,000, M=1,000,000).**
Capacitance values are shown in microfarads unless otherwise (P=MICRO-MICRO FARADS).
Schematic diagram for this model are subject to change for improvement without prior notice.

• MIDI



IC810
SAM9713
IC812
GM23C8100M
IC813
W24257AS
IC828
BU1427K
IC829
QS1000
IC830
QS1003
IC832
74HC74
UDA1330T
IC825
IC824
74HC157
IC826
7805
PN810
CN810
MIDI_OPTION
PN806
PN807
(FROM CL484)
PATTERN CON'T
FROM IC814
MPEG 5V
3.3V
1.+5V
2. 3.3V
3.NC
4.RESET
5.MIDI_IN
6.GND
7.GND
8.GND
9.BCK
10.CLOCK
11.LRCK
12.DATA

• CD/MPEG


PCB P/N: 3008AA
IC806
KB9224
PRE-SIGNAL &
SERVO SIGNAL
PROCESSOR
IC827
KS9287
DIGITAL SIGNAL
PROCESSOR
IC814
CL484 (VER G1)
IC808
KA9259
IC801
H83062
A ver: R985 NO NEED
R ver: R985 NEED
IC809
KA3081
IC815
416C256
IC816
416C256
IC817
ROM32MB
IC823
74HC00
IC821
74HC32
IC820
74HC32
IC804
74HC32
IC805
74HC04
IC803
74HC08
IC818
27C010
IC819(OPTION)
ROM16MB
*OPTION
When Using IC818
R911:NO NEED
When NOT Using IC818
R910, IC820, IC823
NO NEED
* A=32M ROM ENABLE SIGNAL (EXCEPT PGM ROM)
* B=PGM ROM ENABLE SIGNAL
* C=16M LOW ROM ENABLE SIGNAL
* D=16M HIGH ROM ENABLE SIGNAL
* F=A:USE TWO 16M ROMS
=A:USE TWO 16M ROMS AND 1M PGM ROM
TO IC824
TO IC828
16BIT YCrCb TO IC828
1.GND_D2(DIGITAL) 5.6.3VM
2.GND_CD(ANALOG) 6.GNDM
3.+5V_CD(CD ONLY) 7.MPEG_5V
4.+12V_CD(DRIVE) 8.MPEG_GND
1.MIDI_IN(IC829 #49)
2.RESET(IC829 # 17)
3.VCD/MIDI(Q802 BASE)
4.A MUTE(Q804 BASE)
TO MIDI PART
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ADJUSTMENTS

This set has been aligned at the factory and normally will not require further adjustment. As a result, it is not recommended that any attempt is made to modificate any circuit. If any parts are replaced or if anyone tampers with the adjustment, realignment may be necessary.

IMPORTANT

1. Check Power-source voltage.
2. Set the function switch to band being aligned.
3. Turn volume control to minimum unless otherwise noted.
4. Connect low side of signal source and output indicator to chassis ground unless otherwise specified.
5. Keep the signal input as low as possible to avoid AGC and AC action.

TAPE DECK ADJUSTMENT

1. AZIMUTH ADJUSTMENT

Deck Mode	Test Tape	Test Point	Adjustment	Adjust for
A Deck Playback	MTT-114	Speaker Out	Azimuth Screw	Maximum
B Deck Playback	MTT-114	Speaker Out	Azimuth Screw	Maximum

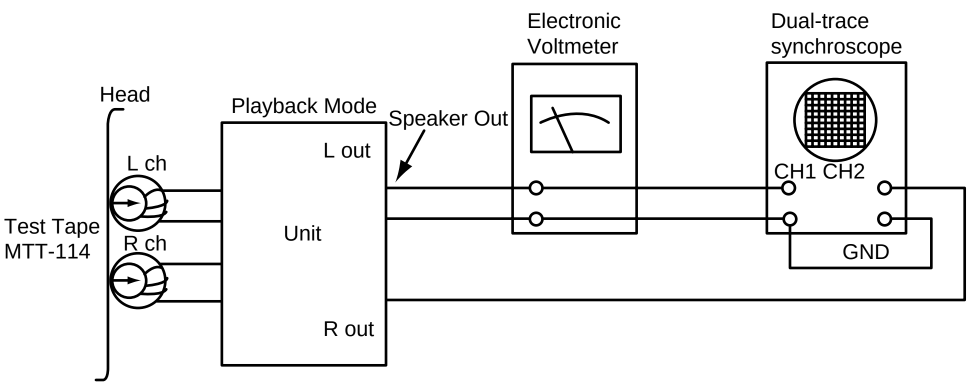

Figure 2. Azimuth Adjustment Connection Diagram

2. MOTOR SPEED ADJUSTMENT

Deck Mode	Test Tape	Test Point	Adjustment	Adjust for	Remark
Normal Speed	MTT-111	Speaker Out	VR401	3kHz ± 1%	A Deck
HI-Speed	MTT-111	Speaker Out	more than 5.4kHz		HI-Speed Dubbing Mode

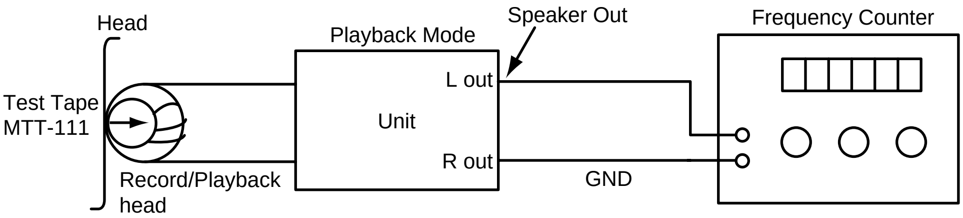

Figure 3. Motor Speed Adjustment Connection Diagram

3. RECORD BIAS ADJUSTMENT

Deck Mode	Test Tape	Test Point	Adjustment	Adjust for
Rec/Pause	MTT-5511	PN402	L403	90kHz±5kHz

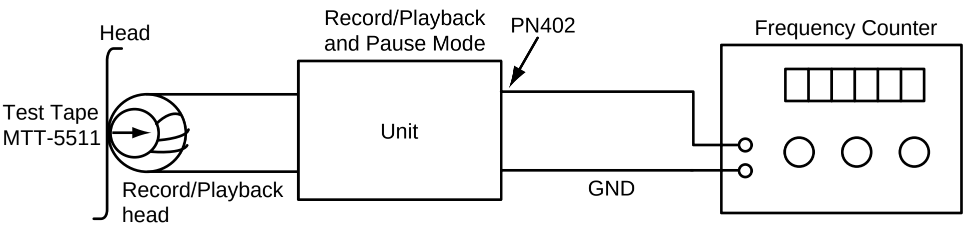

Figure 4. Record Bias Adjustment Connection Diagram