

GPIO Table Please note GPIO state that affects consumption

GPIO #	Signal	I/O	Function	Note(description)
GPIO_A6	I2S OUT MCK	I	HD RESET LOW	
GPIO_B5	I2S OUT SD	I	HD RESET LOW	BOOT STRAP CONFIGURATION
GPIO_C6	I2S OUT BCK	I	HD RESET LOW	
GPIO_AB25	PWM0	I	HD RESET LOW	Boot Strap for TVMICO
GPIO_AB24	PWM1	I	HD RESET LOW	
GPIO_P5	SPDIF	O	DIGITAL AUDIO OUT PUT	
GPIO_Y8	DVD POWER	O	DVD POWER CONTROL	0:Turn On 1:Turn Off
GPIO_AB7	DVD IR	O	DVD IR REMOTE	simulate code to control DVD
GPIO_AA9	DVD TXD	O	communications	simulate uart
GPIO_E6	DVD RXD	I	DVD communications	simulate UART
GPIO_B2	SPI_WEN	O	SPI FLASH WRITE ENABLE	0:Enable(default) 1:Disable
GPIO_C4	LED	O	CONTROL LED STATE(PM6)	0:greed 1:red
GPIO_AB10	ANT POWER	I	control ANT POWER	0:Low Off 1:HIGT ON
GPIO_N4	VGA_HSYNC	ADC	Detect VGA input	
GPIO_N5	VGA_VSYNC	ADC	Detect VGA input	NOTE SET adc INTERFACE
GPIO_H5	AMP STB	O	Control amp STB1	0:Low PLAY 1:HIGT STB
GPIO_T4	SEL_DVD/YBPBR	O	Control 5V330	0:Low YBPBR 1:HIGT DVD
GPIO_AC11	CI CARD POWER	O	CI CARD POWER CONTROL	0:Low Off 1:HIGT ON
GPIO_C5	SYSTEM POWER	O	System PWR ON	0:Turn Off(STB) 1:Turn On
GPIO_K4	MUTE AMP	O	CONTROL AMP MUTE	0:Low MUTE 1:HIGT PLAY
GPIO_E4	LCD POWER	O	CONTROL LCD POWER	0:Turn Off(STB) 1:Turn On
GPIO_F4	BACK LIGHT CONTROL	O	LCD backlight on/off control	0:Turn ON 1:Turn OFF
GPIO_B4	SCART FS	I	0-2V TV MODE,5-8V ->16:9,9.5-12V->4:3	
GPIO_Y6	SCART FB	I	0.7V ->AV,1-3V->RGB	
GPIO_AD2	SIF switch	O	control sif switch at different	0:BG/DK/I/L 1:L'
GPIO_AC4	RF_AGC switch	O	RF AGC control	At the DVB Please set high , ATV set low for RF AGC

Sheet Index

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- PAGE 02 - :system power
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- PAGE 04 - MEMORY
- PAGE 05 - Tuner & Demodulator
- PAGE 06 - PCMCIA
- PAGE 07 - HDMI Input
- PAGE 08 - SIGNAL INTERFACE
- PAGE 09 - Audio AMP

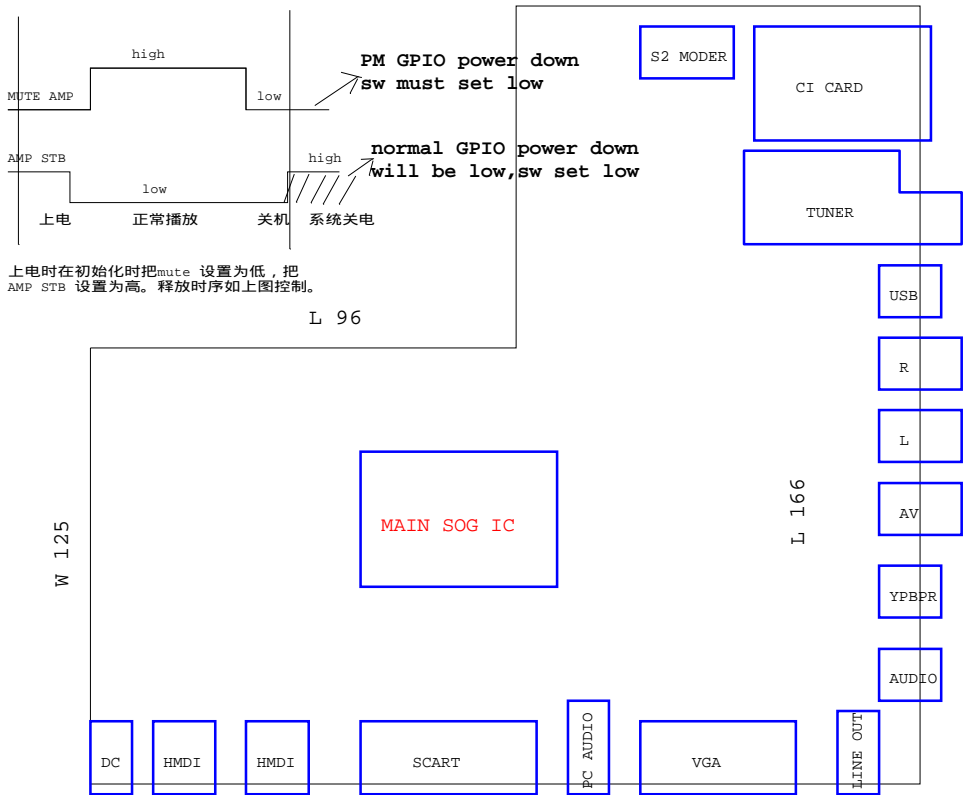
Notes:(unless otherwise stated)

- 1.所有利用的电阻必须满足功率要求。
- 2.耦合电容必须要使用X7R材质，不得用Y5V。靠近在IC附近的退耦电容使用X7R材质。滤波网络上也使用X7R或NPO。远离发热器件可以使用Y5V

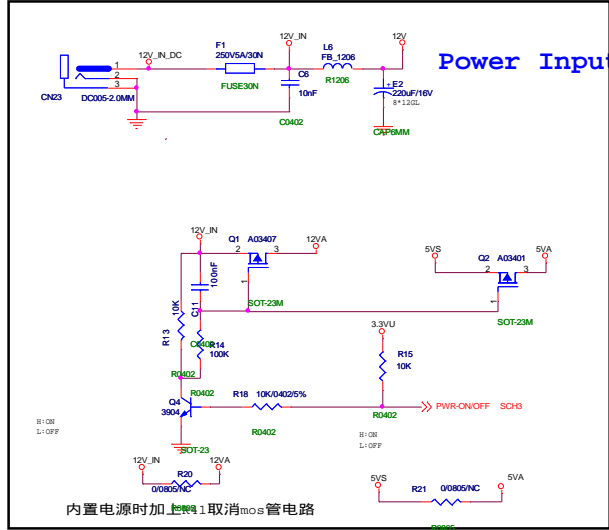
L Shape DVB drive Board

Revision History

Revision	Description	Author	Data
A	Initial Release	THE GROUP	2010 5 15
B	1. LED的灯 改为C4脚。PM6控制，方便软件控制。 2. LINE OUT 改为射随输出。 3. 更改CORE 电压DC-DC的布局，输入加电解C30 改为：E9 100uf/16V TM 5*11 4. DDR供电的LDO过热，换封装TO-263，二极管要散开，和DC-DC要分开，避免过热。 5. SART 的FS脚的分压电阻。要和306的保持一样。 6. R255的网络有错误，当要兼容QINJIA的tuner时要把它接到第8脚。 7. C140改为0805/Y5V/X5R/10V 8. 增加电阻R495 -> 22K 9. R435改为0402 10. E27 改为6MM。 11. U6改为1084 ADJ TO-263		

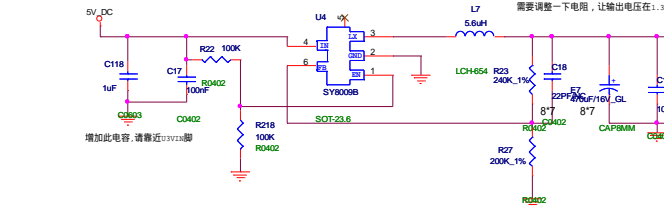
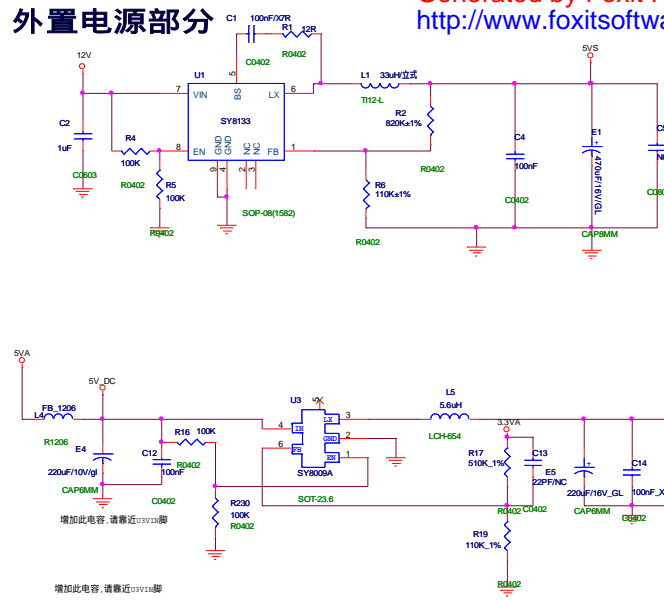


外置电源输入及控制电路

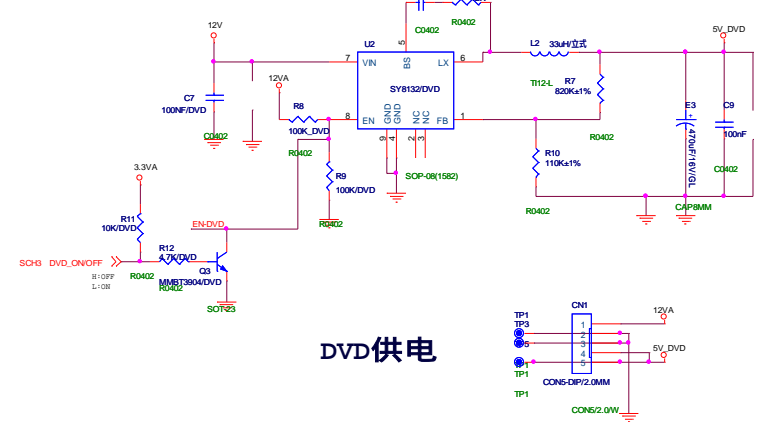


MST Power supply

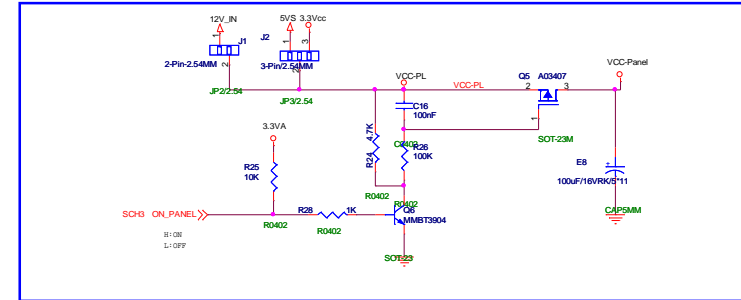
外置电源部分



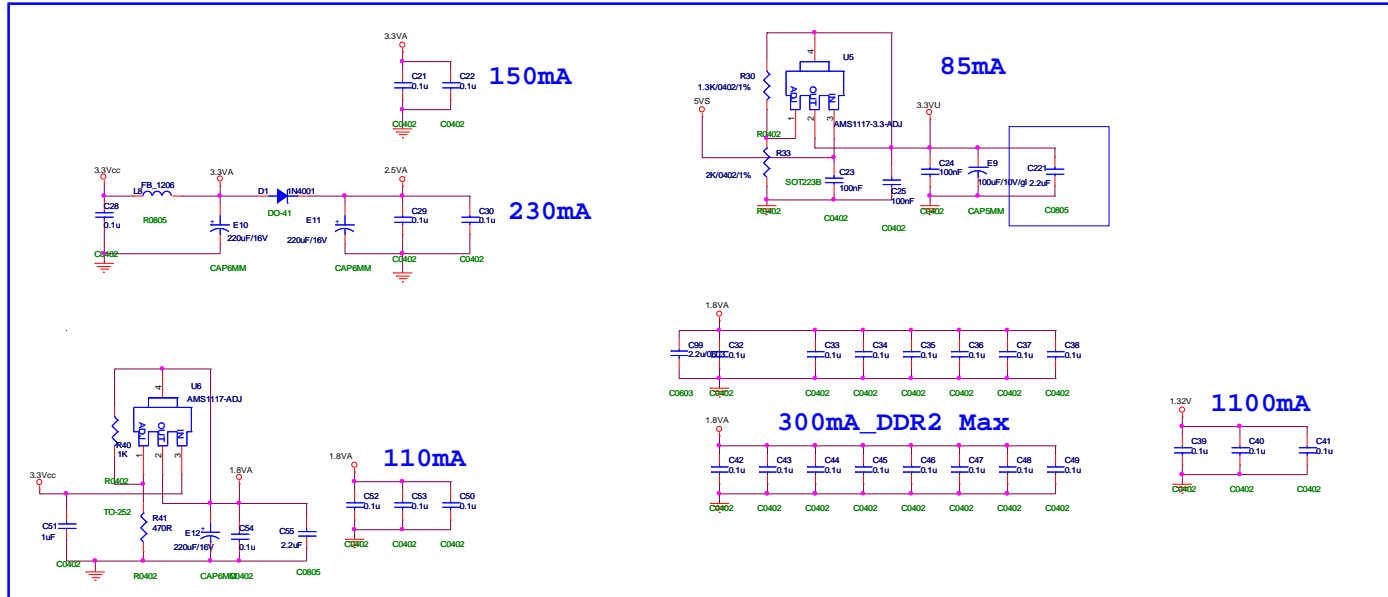
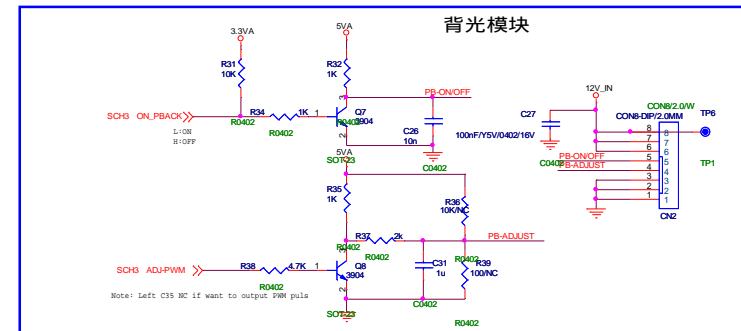
DVD Power Interface

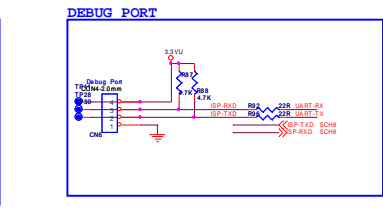
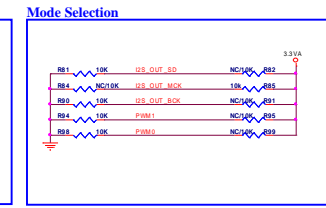
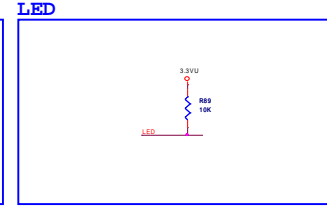
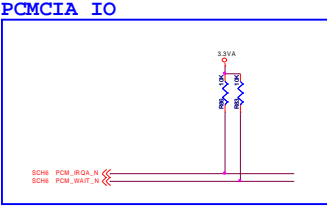
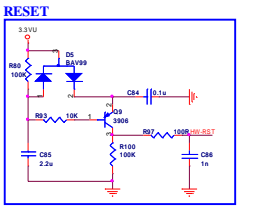
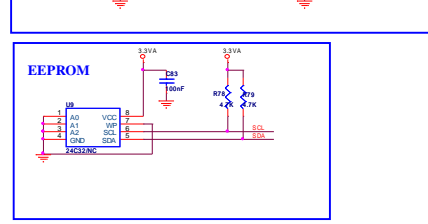
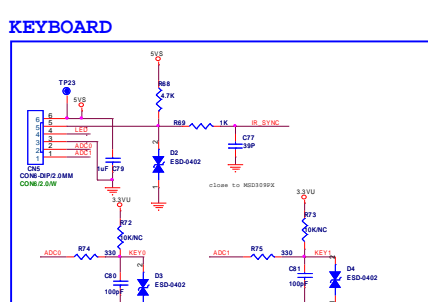
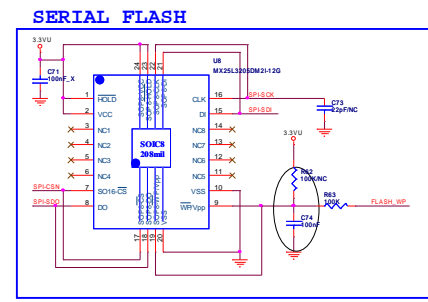
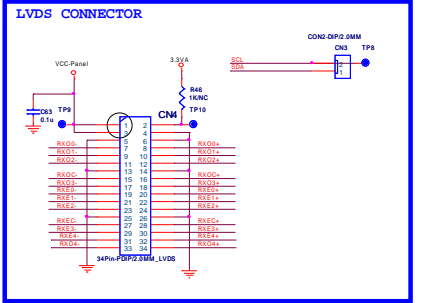
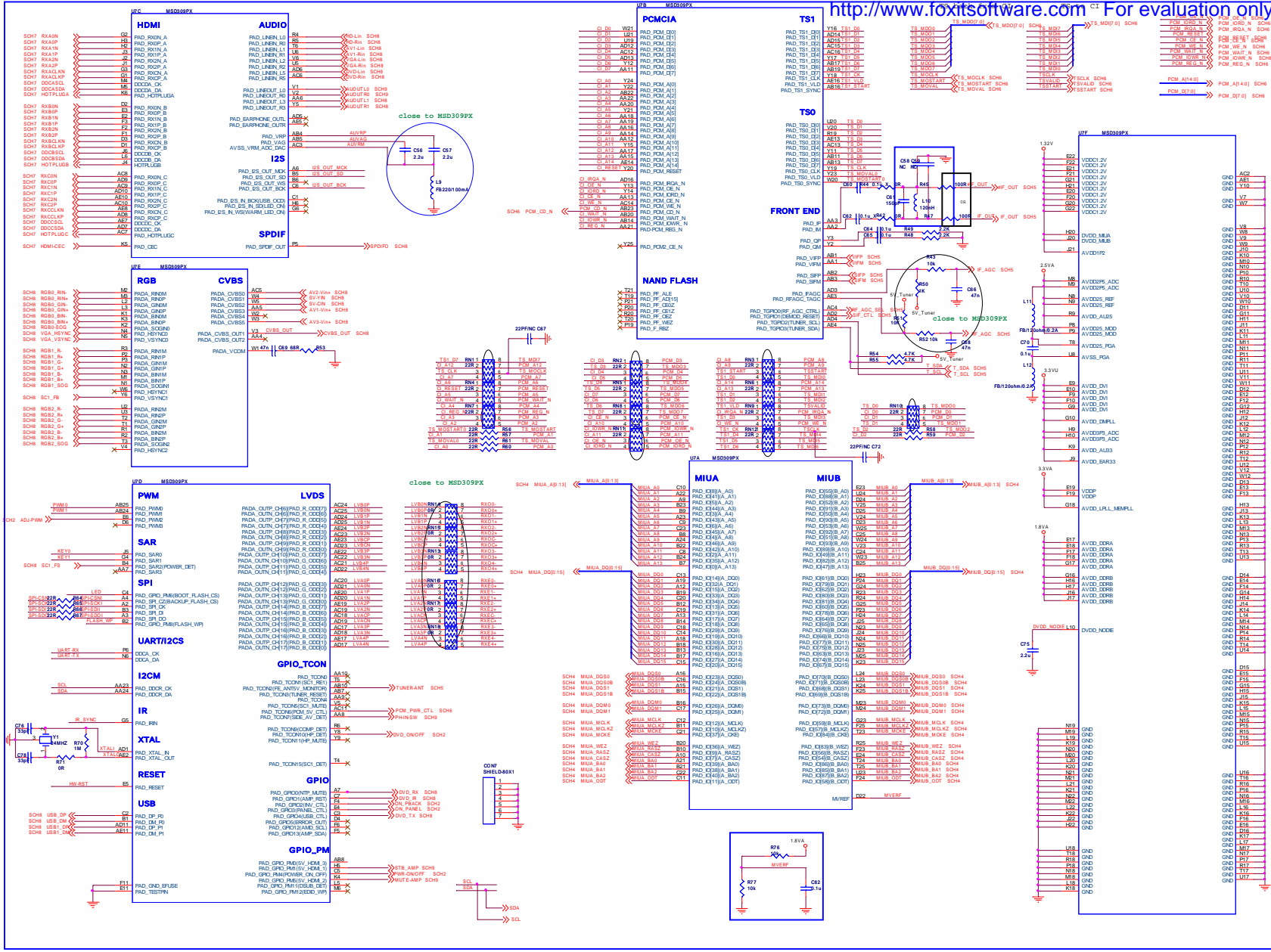


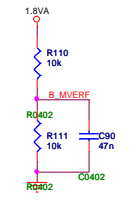
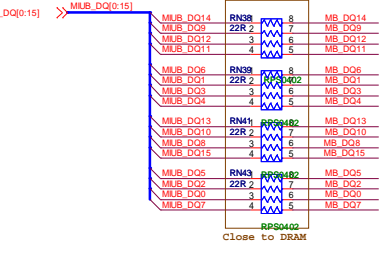
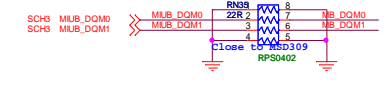
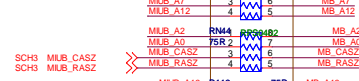
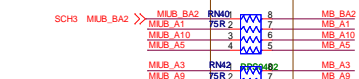
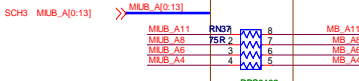
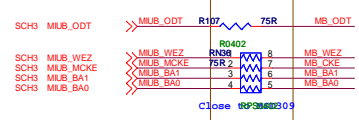
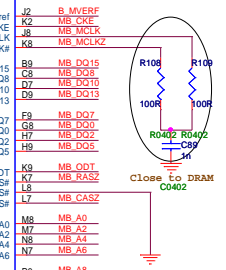
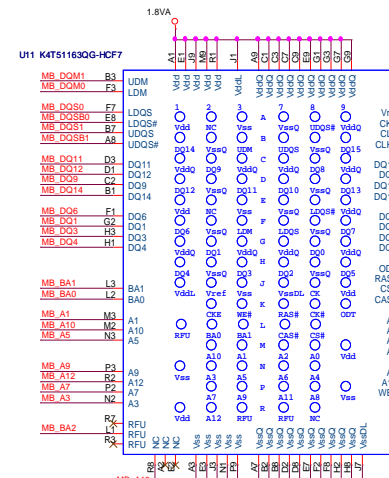
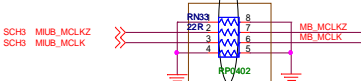
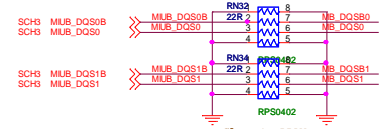
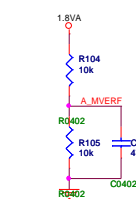
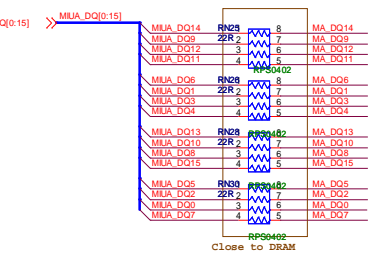
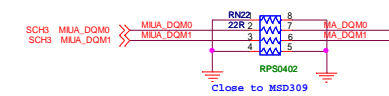
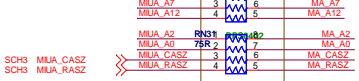
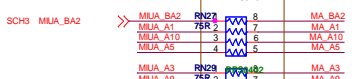
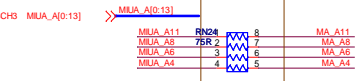
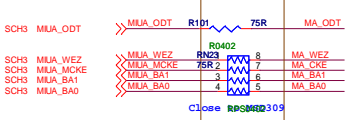
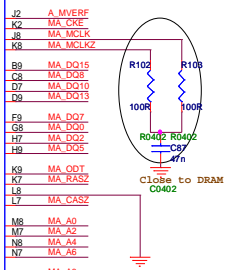
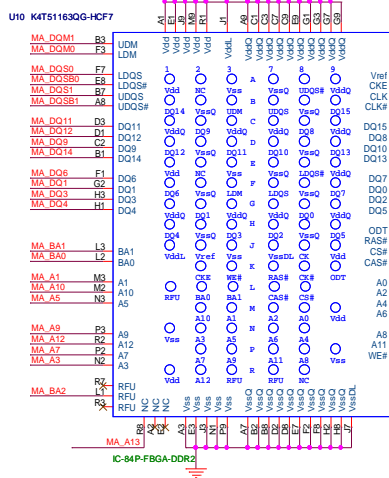
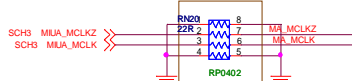
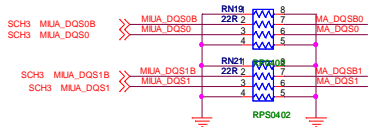
PANEL POWER



Inverter Interface





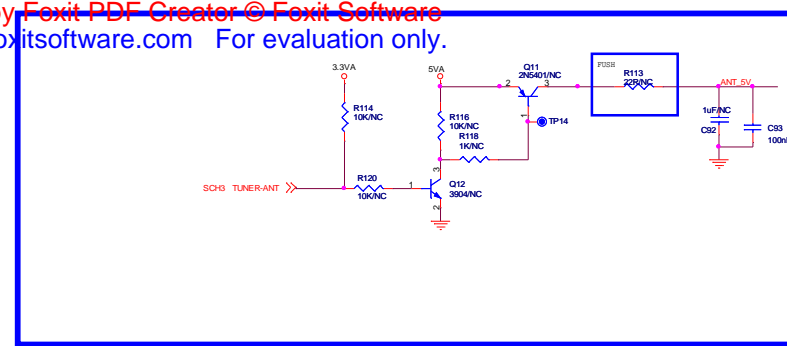
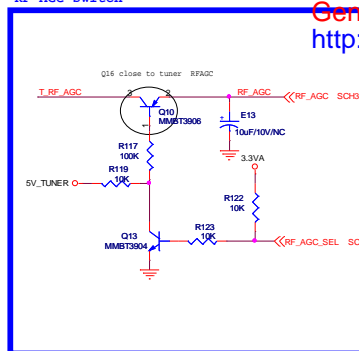
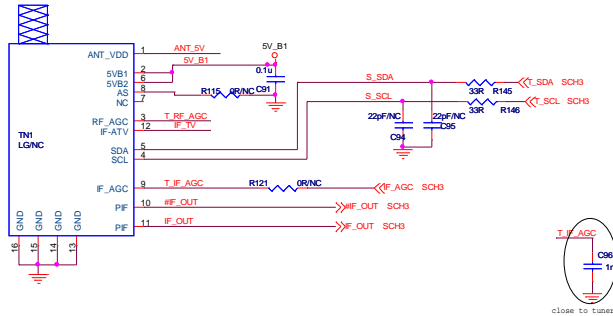


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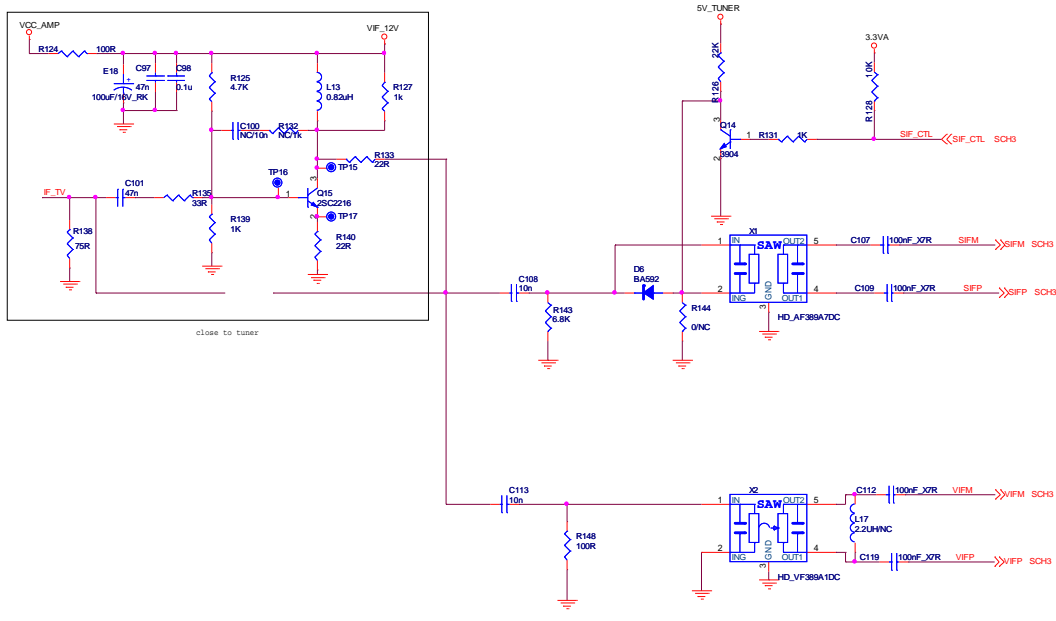
RF AGC Switch

馈电部分

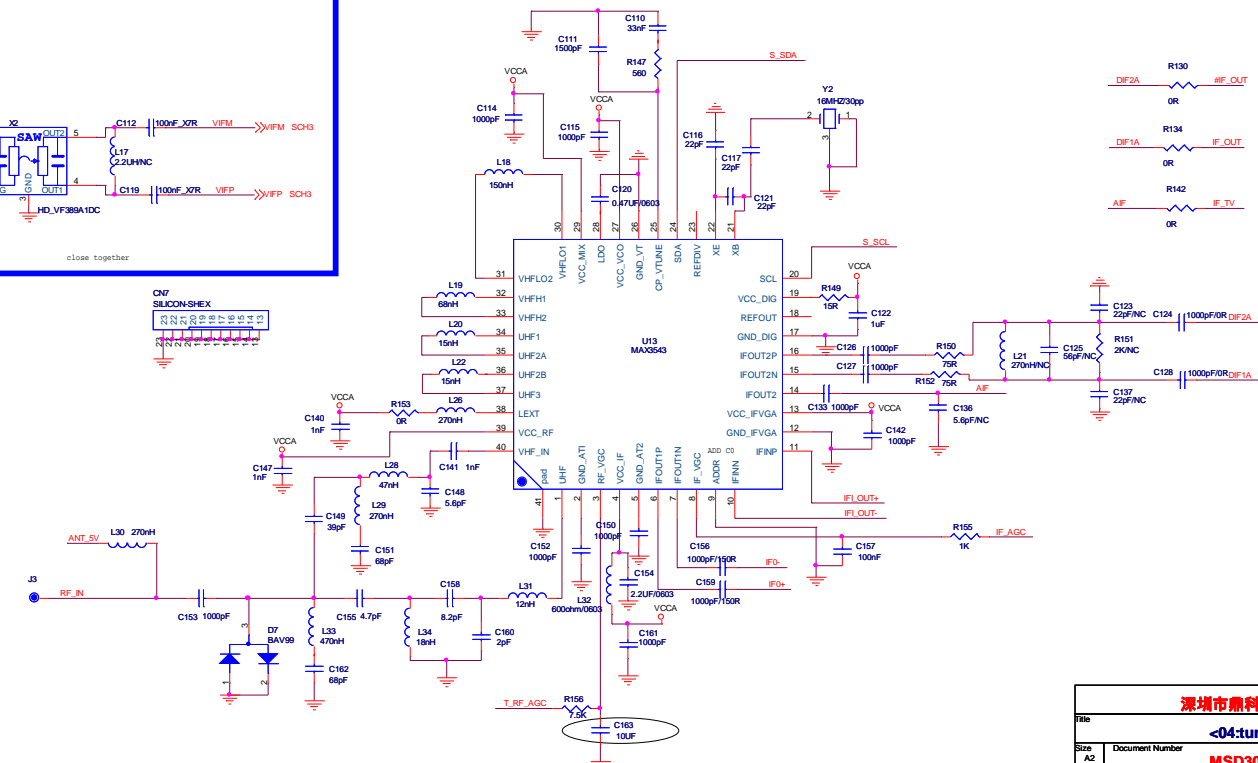
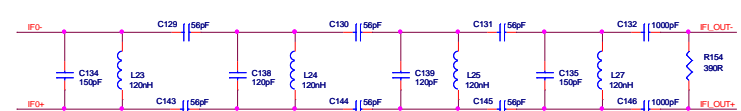
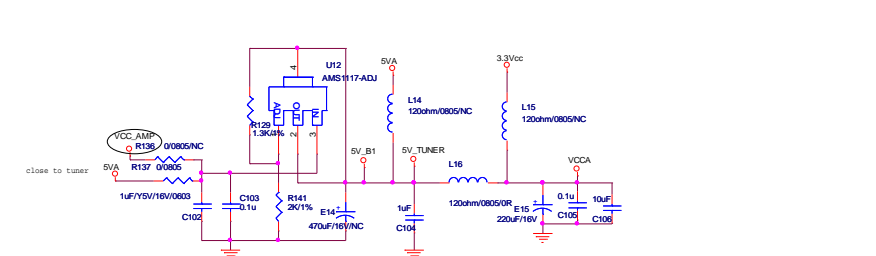
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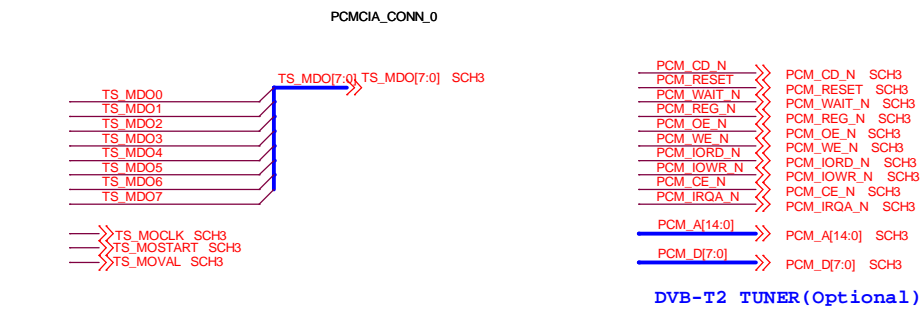
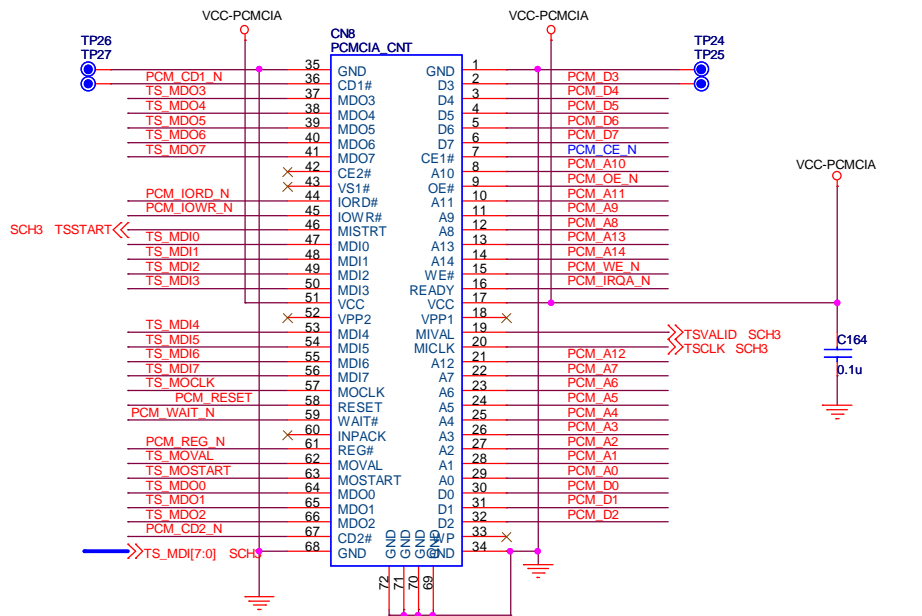


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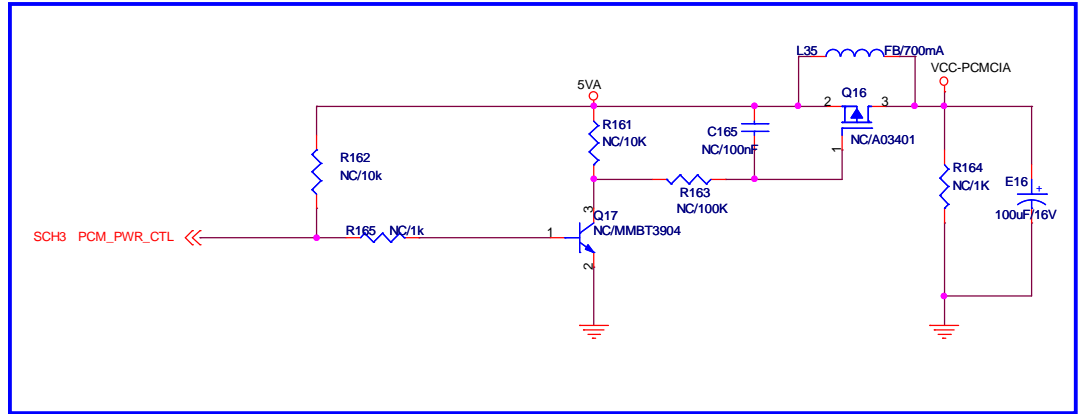


TUNER POWER





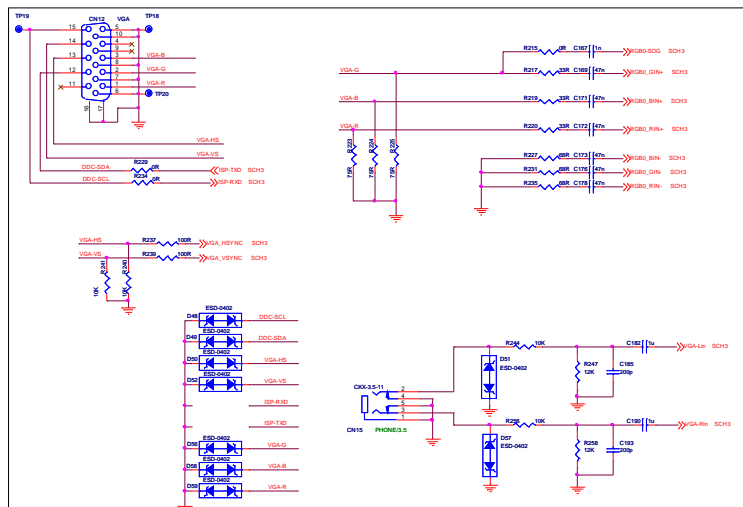
PWR CTRL



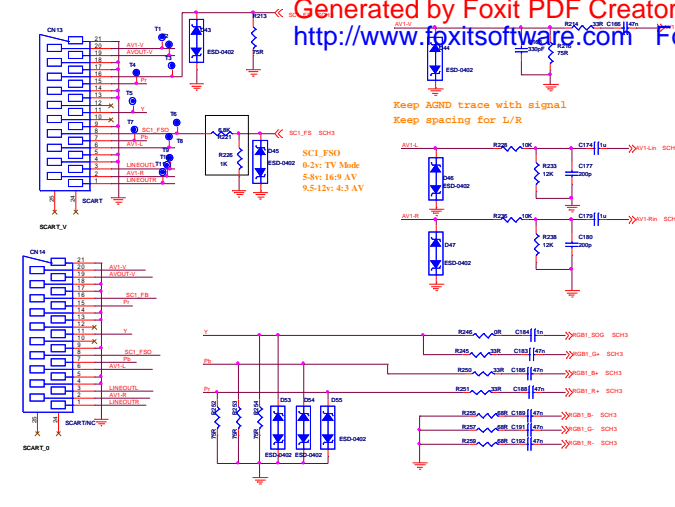
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VGA INPUT

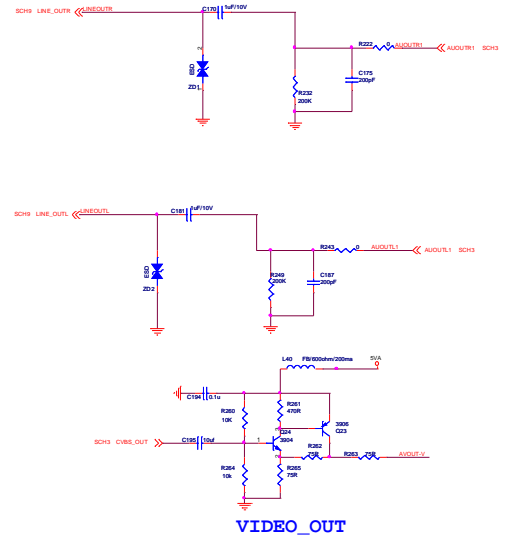


FULL SCART

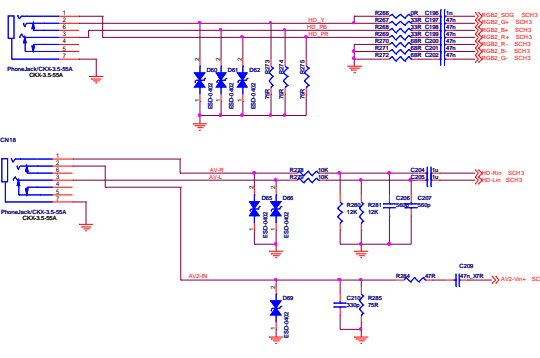


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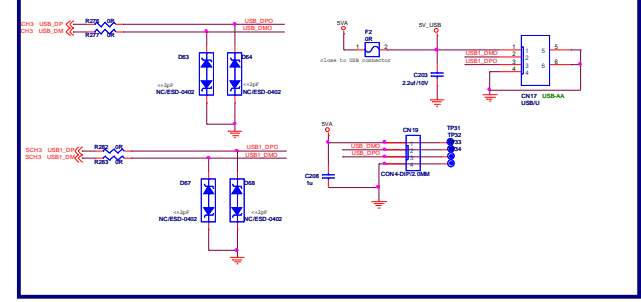
Keep AGND trace with signal
 Keep spacing for L/R



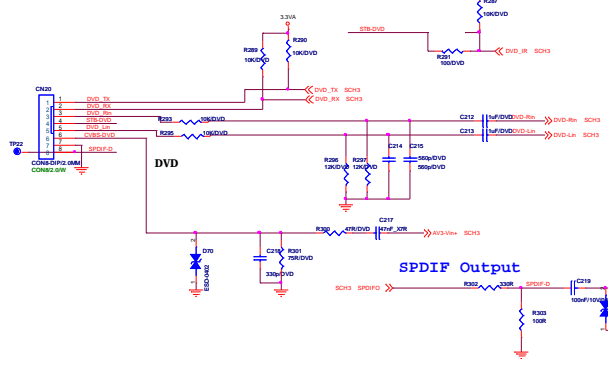
PHOSE_HD



USB



DVD



SPDIF Output

