

HSD311

ESS ES6178F + ALI SERVO + HOP1200

Vibratto ES6178F

LCEx#	FUNCTION
1	IO EXPAND
2	IO EXPAND
3	ROM/FLASH

AUXx	FUNCTION
0	I2C DATA/VFD DATA
1	I2C CLOCK
2	FLASH VPP CONTROL

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- 3 MEMORY
- 4 DVD DRIVE INTERFACE
- 5 AUDIO & VIDEO
- 6 PREAMP
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GROUND

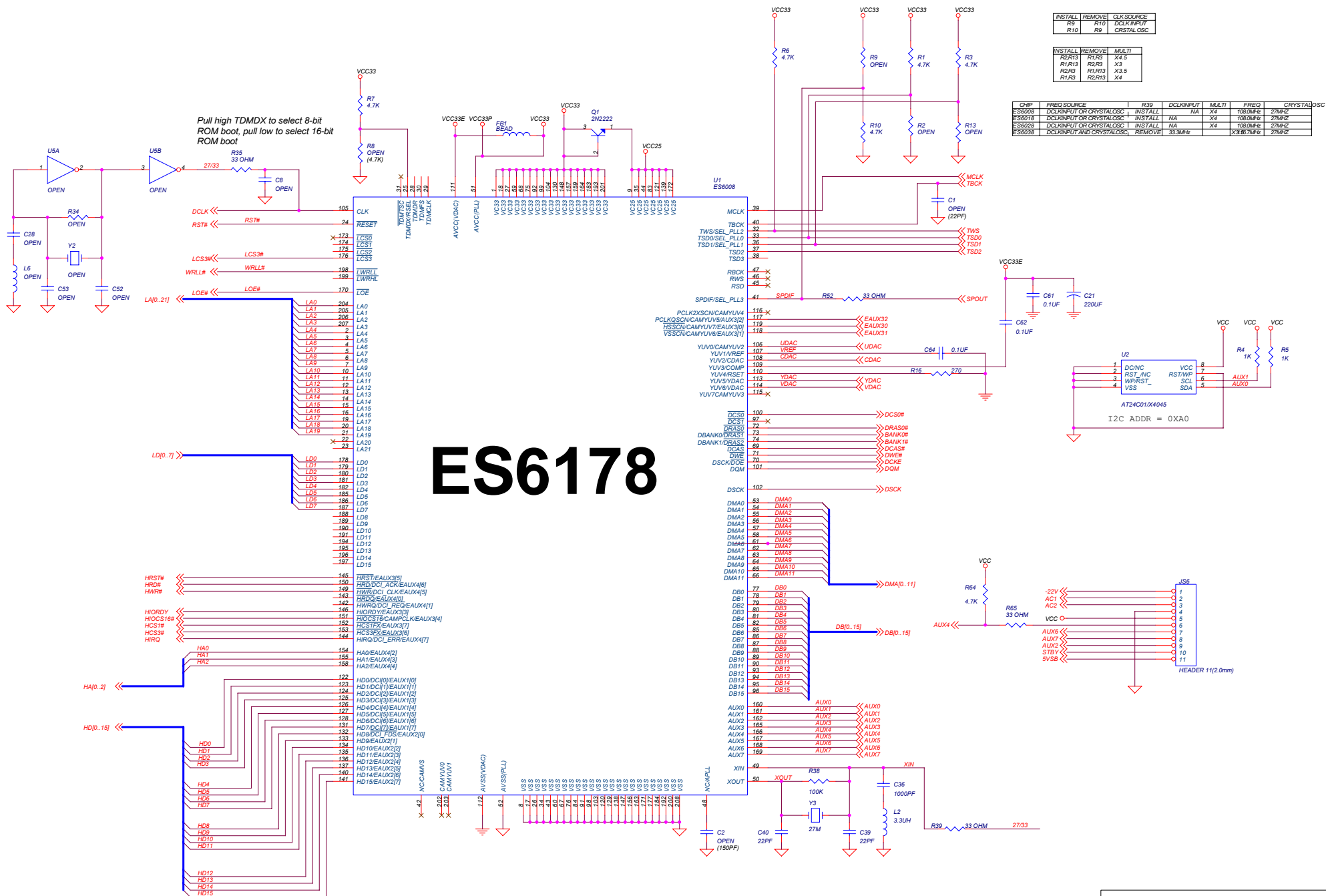
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GND	DIGITAL GROUND
GNDV	VIDEO GROUND

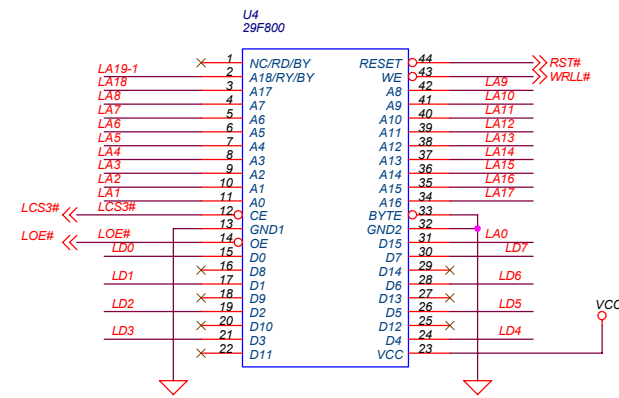
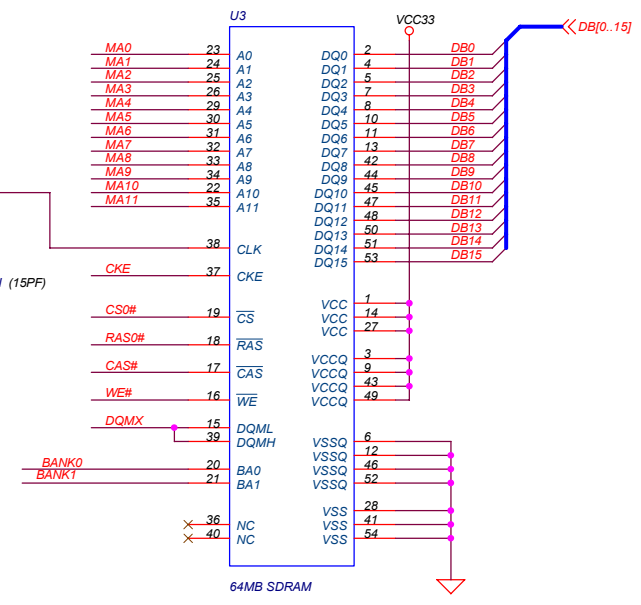
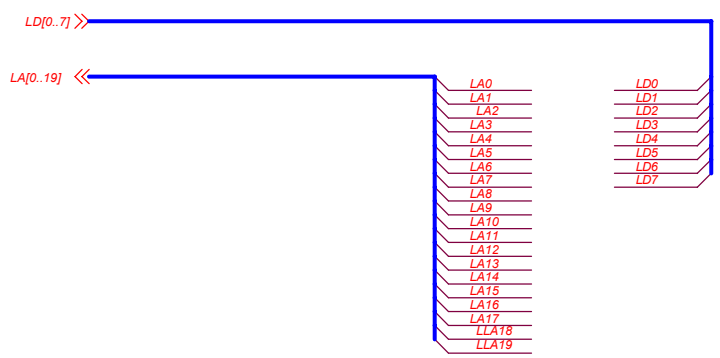
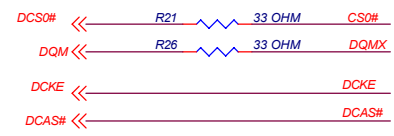
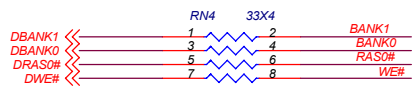
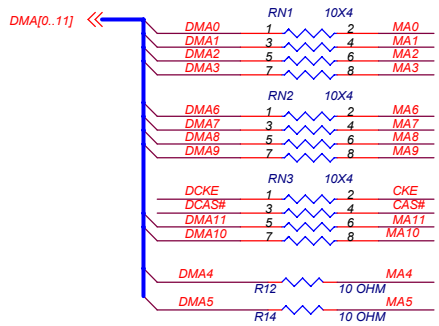
POWER

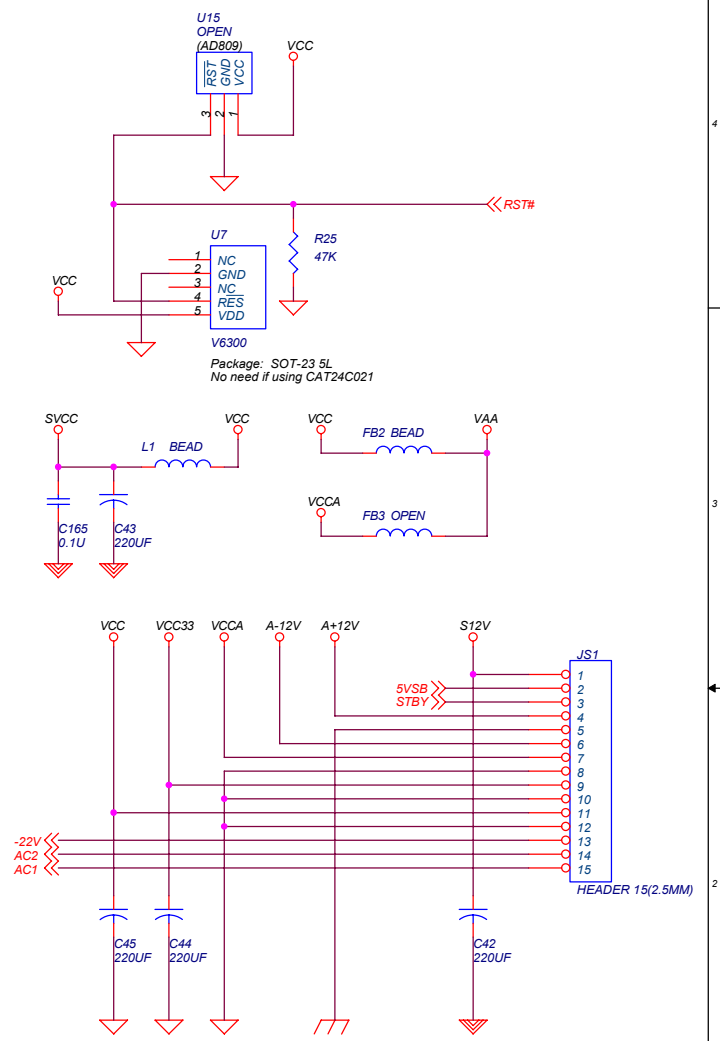
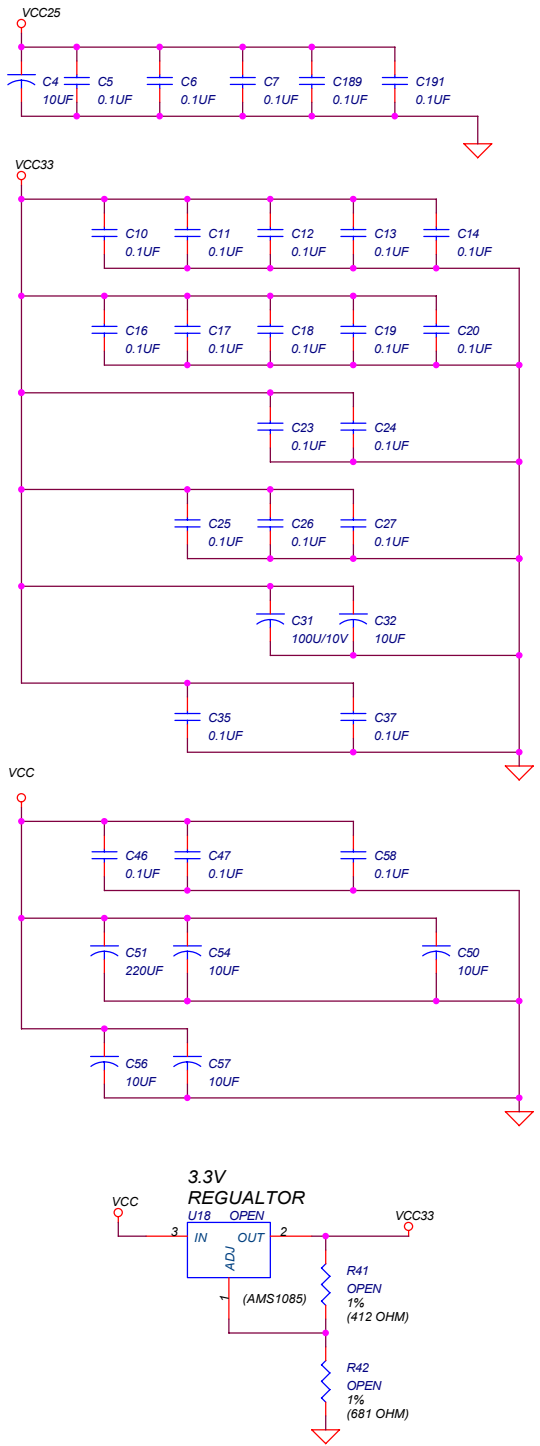
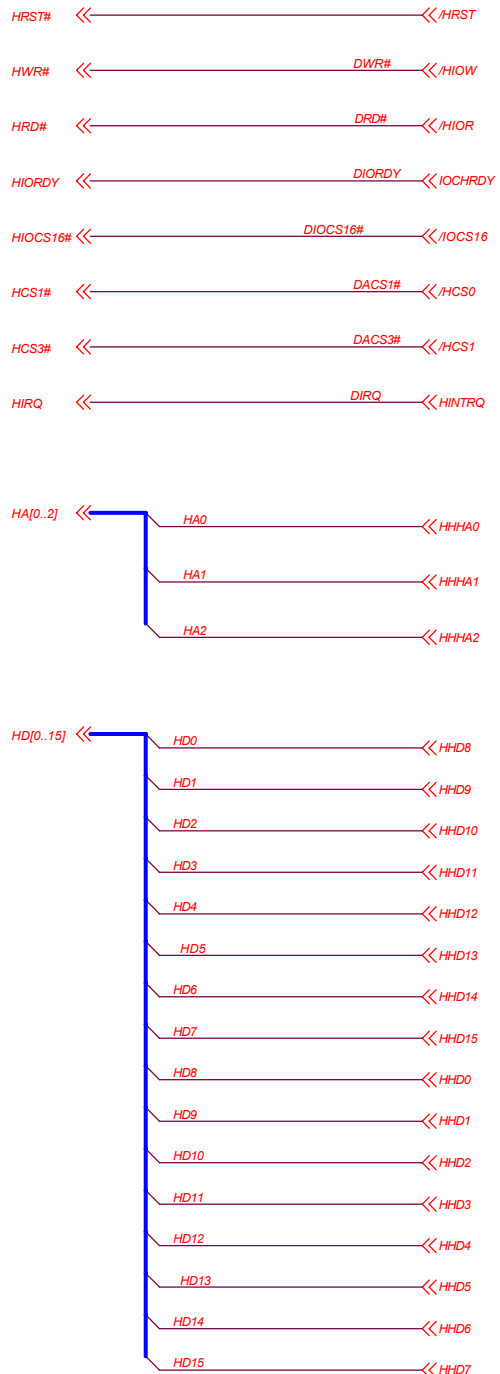
NAME	TYPE	DEVICE
VCC	DIGITAL 5V	SUPPLY
VCC3	DIGITAL 3.6V	ES6178 SDRAM

THE INFORMATION HAS BEEN CHECKED AND IS BELIEVED TO BE RELIABLE. HOWEVER, NO RESPONSIBILITY IS ASSUMED FOR INNACCURACIES. CIRCUIT DIAGRAMS ARE PROVIDED AS A MEANS OF ILLUSTRATING TYPICAL APPLICATIONS; CONSEQUENTLY COMPLETE INFORMATION FOR CONSTRUCTION PURPOSES IS NOT NECESSARILY GIVEN. ESS RESERVES THE RIGHT TO MAKE CHANGES AT ANY TIME IN ORDER TO IMPROVE THE DESIGN.

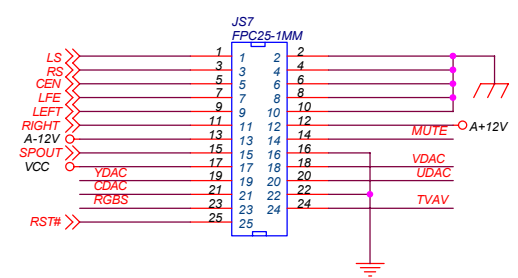
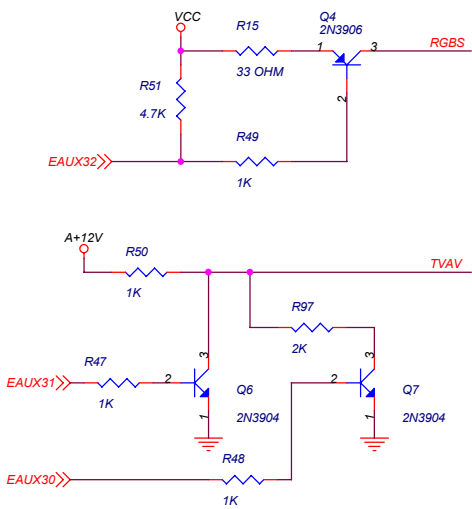
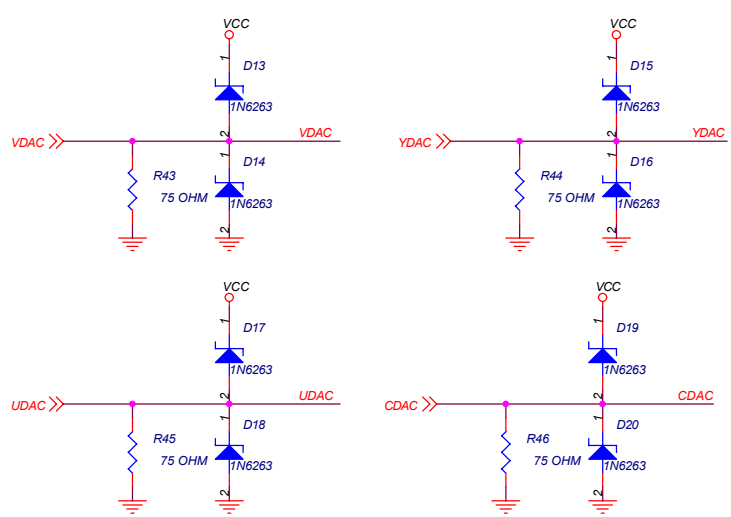
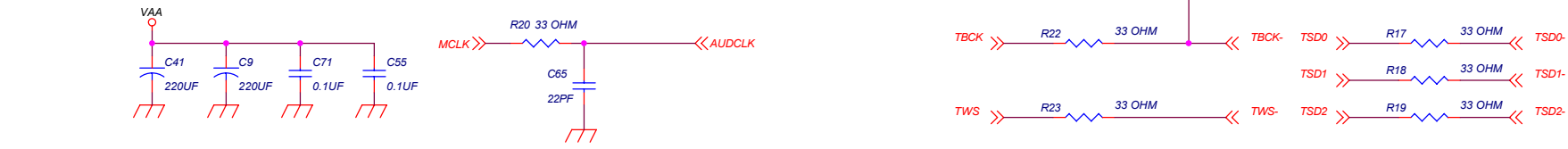
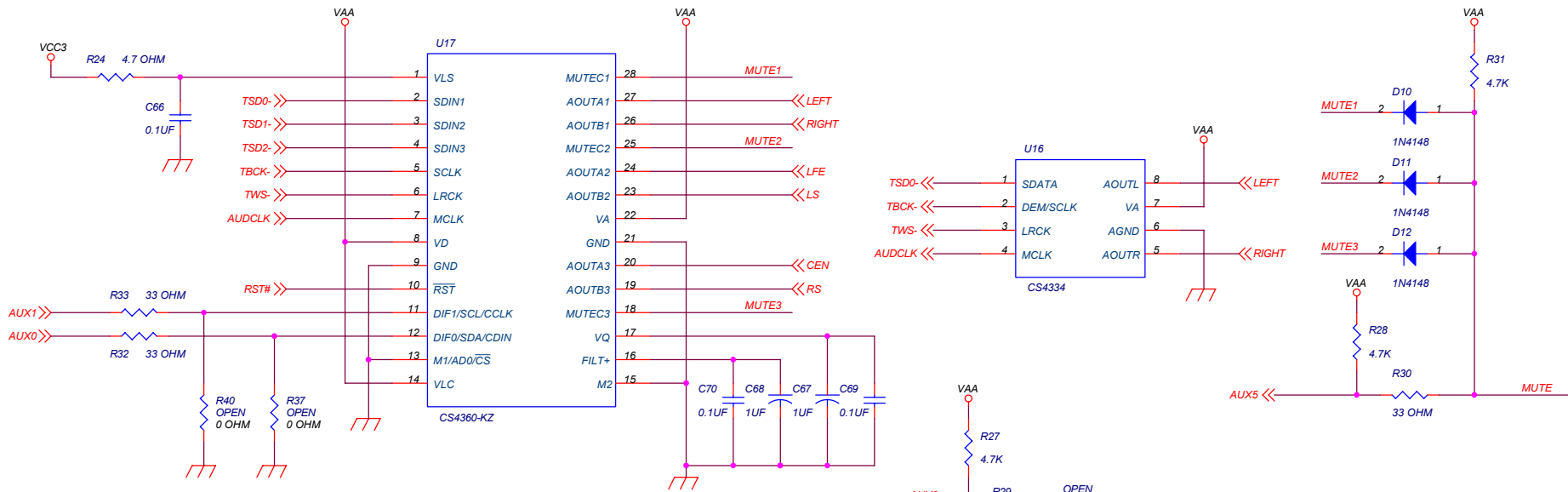
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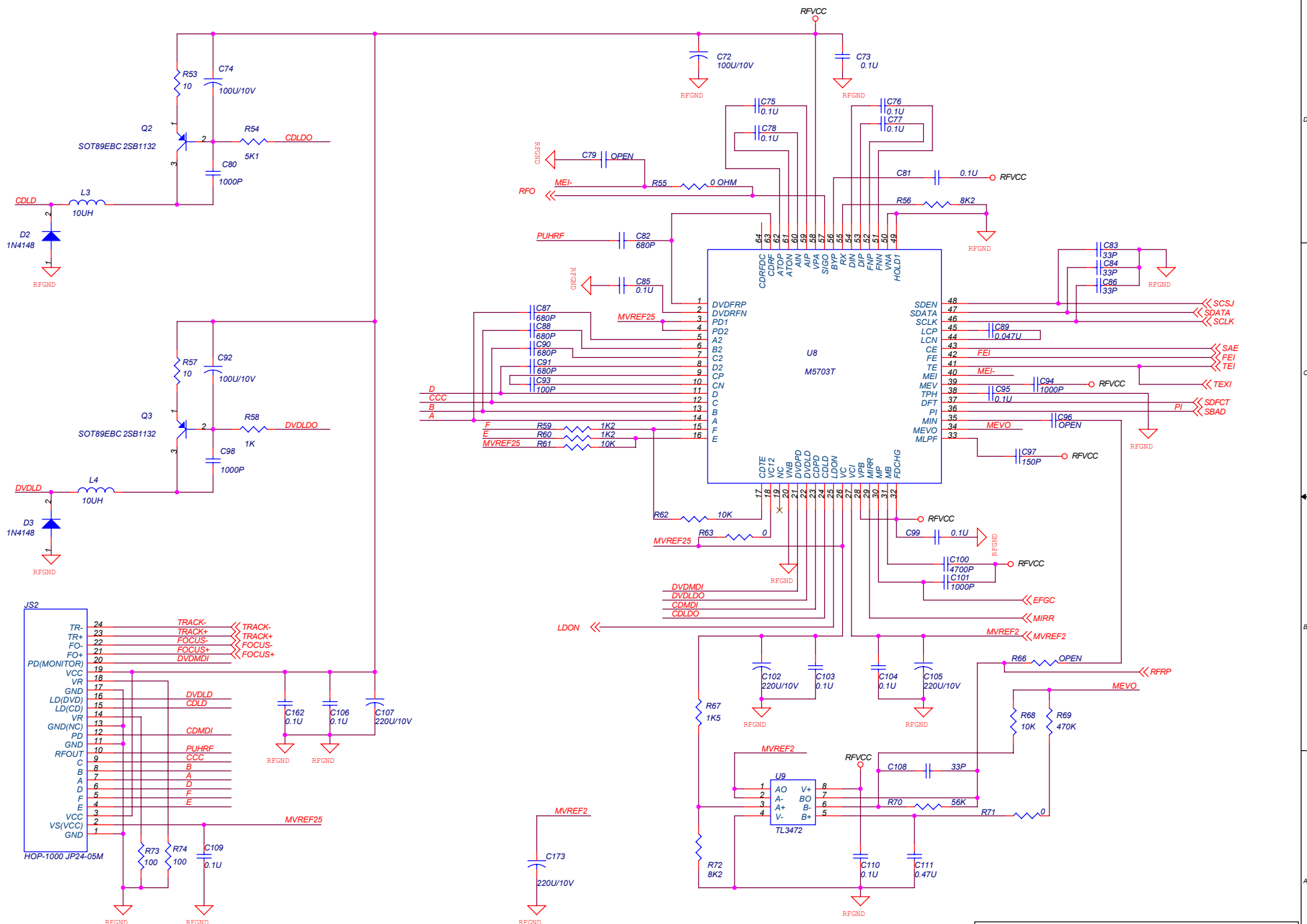




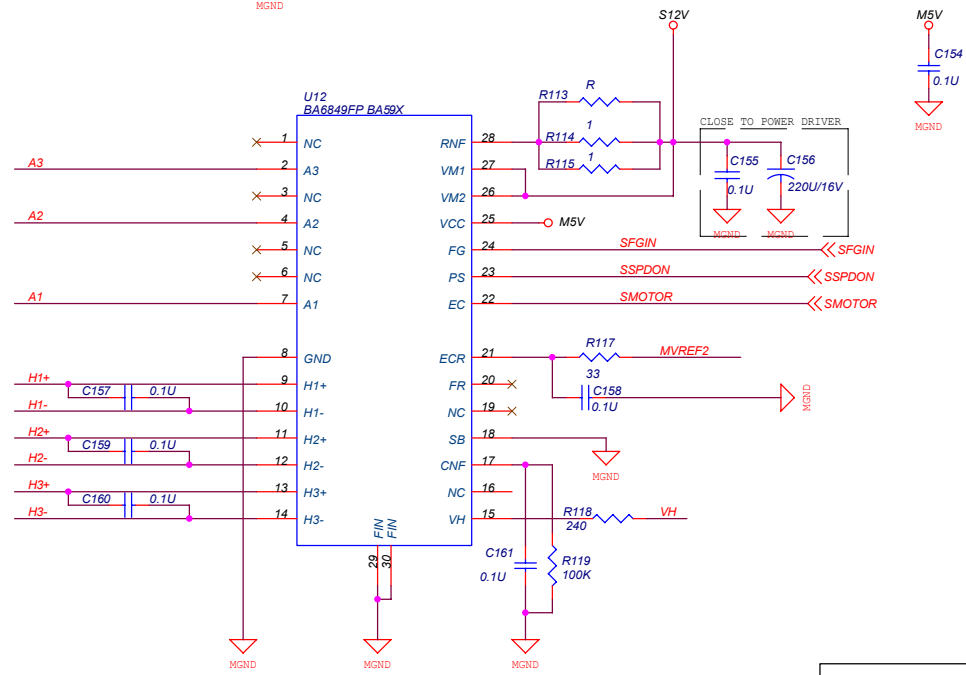
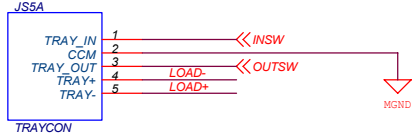
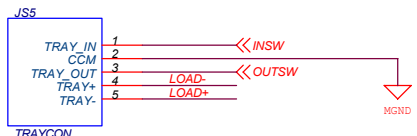
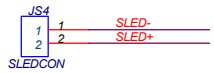
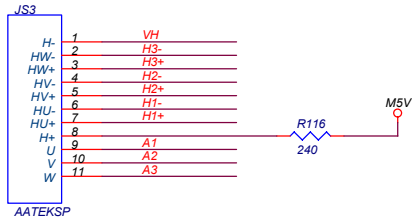
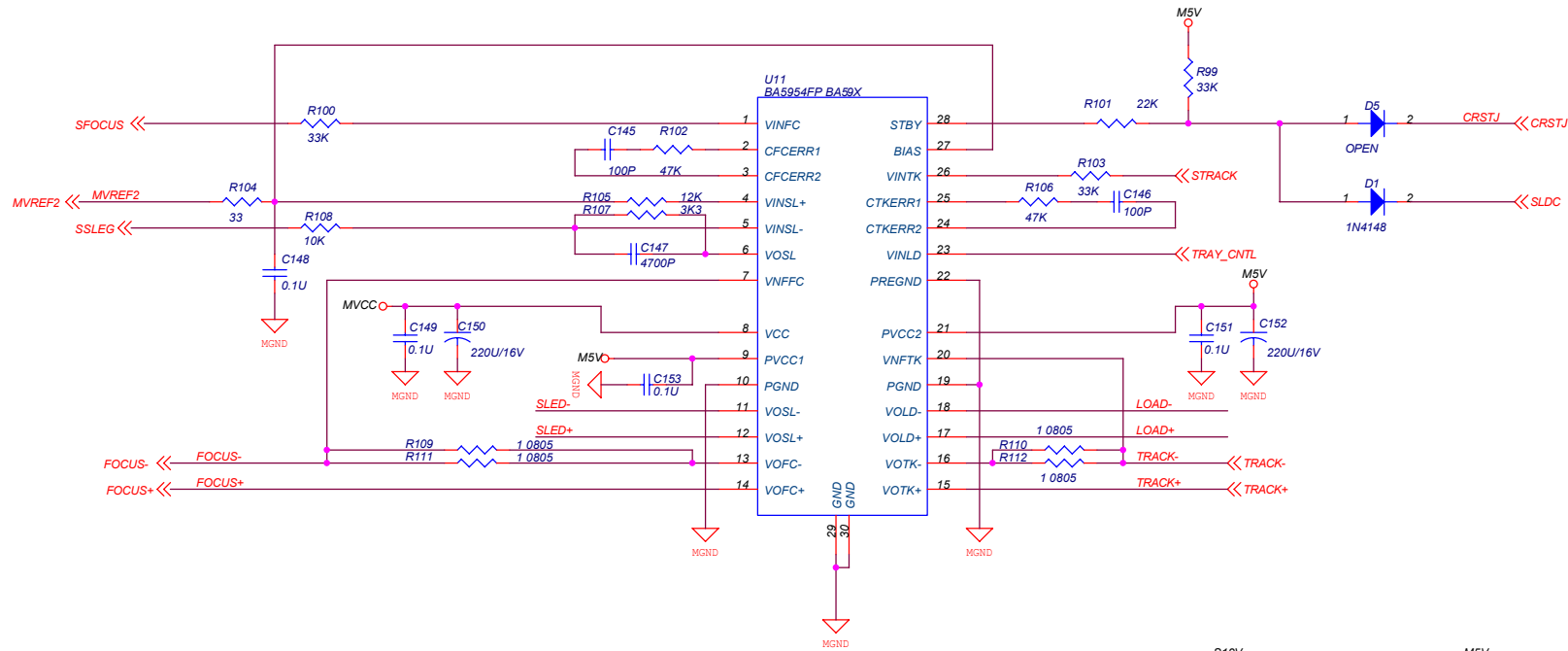
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DVD Loader ATAPI Interface		
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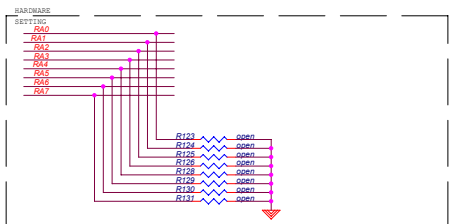
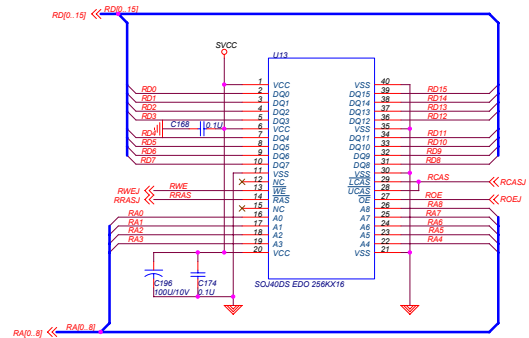
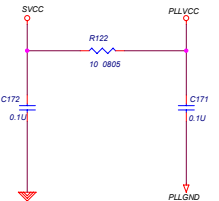
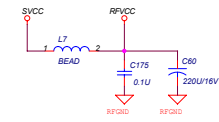
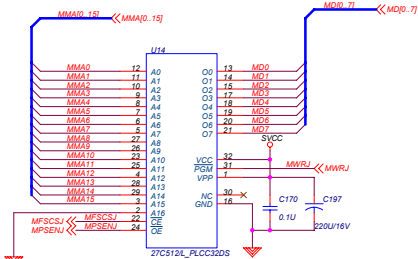
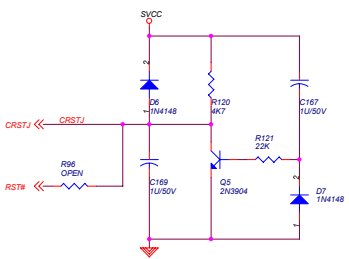
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AUDIO & VIDEO		
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M5703T PRE-AMP		
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DRIVER		
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HARDWARE SETTING (For M705):

- RA[7] : Microprocessor programmable I/O port 1 pin control
 - 1 : By internal microprocessor
 - 0 : By registers to decided
- RA[6] : ~~Internal microprocessor~~ output
 - 1 : Normal operation
 - 0 : System test pin
- RA[4] : ~~Master/slave~~
 - 1 : Slave
 - 0 : Master
- RA[3] : ~~Master/slave~~ processor I/F pins configuration
 - 1 : Normal mode
 - 0 : M705
- RA[2] : ~~Compatible mode~~
 - 1 : Select internal clock
 - 0 : Select external clock
- RA[1-0] : ~~MC7 Mode selection~~
 - 11 : Normal Mode (internal up, internal address latch)
 - 10 : Output up Mode (ICE Mode)
 - 01 : Test mode for internal up testing
 - 00 : internal up mode with external address latch

