

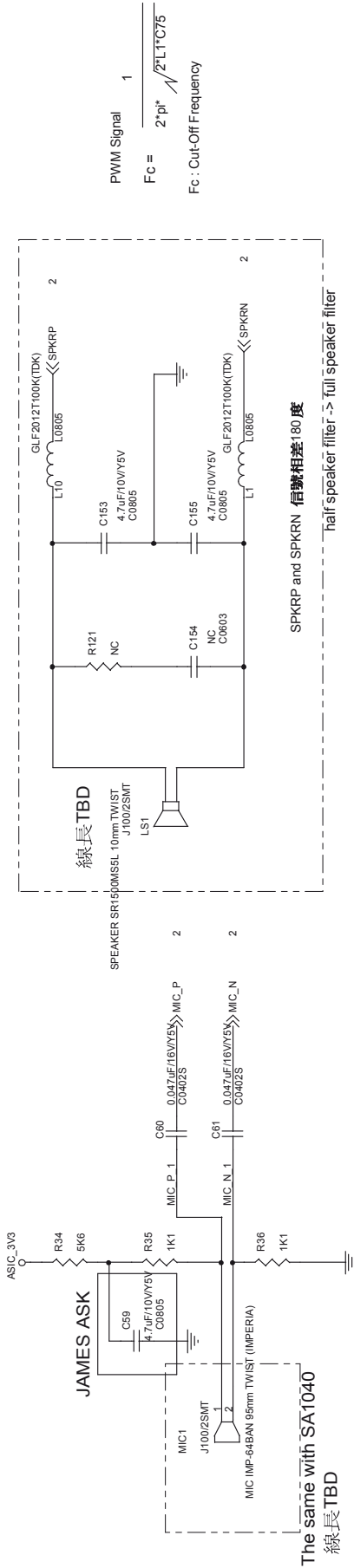
8. Schematic Diagrams

8-1 Main PCB-1

REV	REVISION HISTORY	Date
A0	<p>TEST PAD IS A 40 MIL ROUND PAD</p> <p>Component limit SOC BGA-54 H:1.2mm LTFBGA-293 H:1.2mm TQFP-48 H:1.2mm TQFP-100 H:1.2mm TSOP/IL-54 H:1.2mm EMD3/SOT-416/SC-75A H:0.7mm UMD3/SOT-323/SC-70 H:0.9mm MSOP-10 H: mm MCPH3 H:0.85mm uSOP-8 H:1.0mm</p> <p>SOP-8 H:1.75mm SOT-23-5 H:0.9~1.3mm SOD-123 H:1.35mm SOT-23 H:1.10mm A-Size H:1.6mm B-Size H:1.9mm D-Size H:2.8mm HC-49US H:3mm DT-26 H:1.8mm MicroPAK H:0.8mm</p>	2009/04/XX
A1		
A2		
B		
C		

Page	
1	COVER PAGE
2	COACH-11S-1
3	COACH-11S-2
4	COACH-11S-3/Power I/F
5	DDR2 SDRAM/NAND/SD CARD
6	AUDIO/MIC/AV OUT/CFG
7	LENS DRIVER
8	TG_ADDI9000
9	POWER_AT1868
10	MAIN to STROBE & UI
11	LCD_TPO(990000535)
12	Modify Note
13	SYSTEM BLOCK DIAGRAM
14	POWER BLOCK DIAGRAM
15	
16	

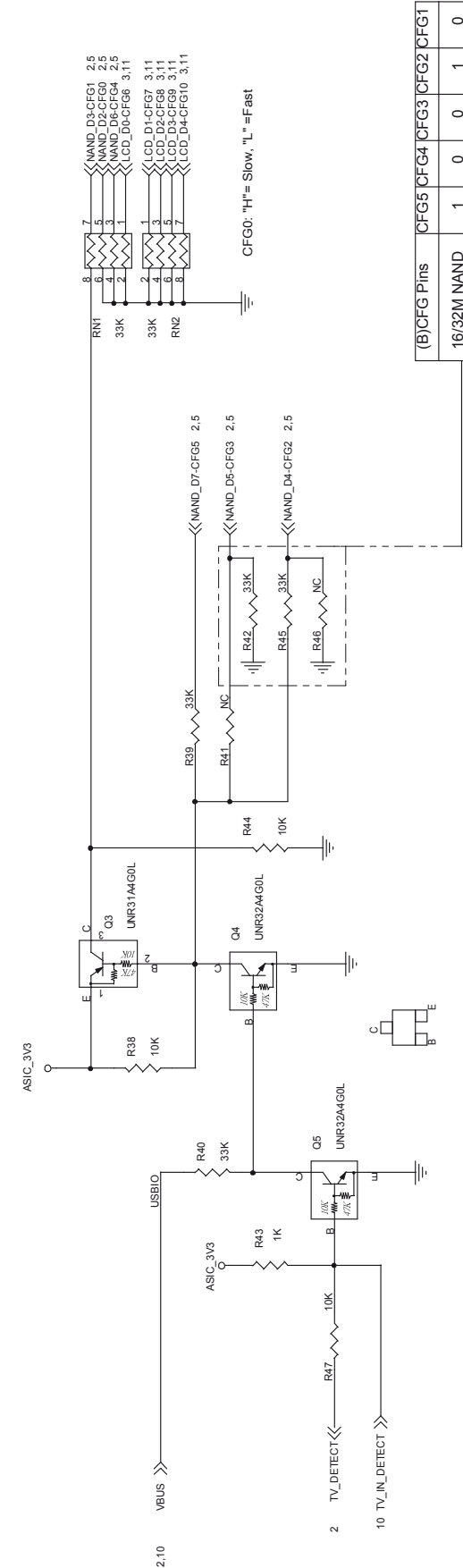
8-6 Main PCB-6



PWM Signal 1

$$F_c = \frac{2 \cdot \pi \cdot f}{2 \cdot L \cdot C \cdot 75}$$

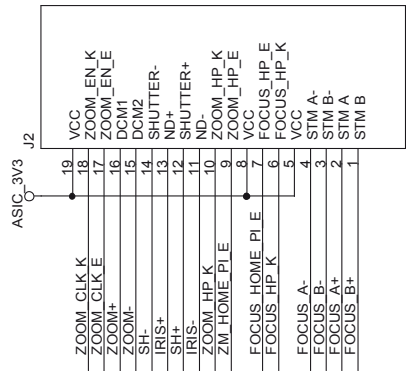
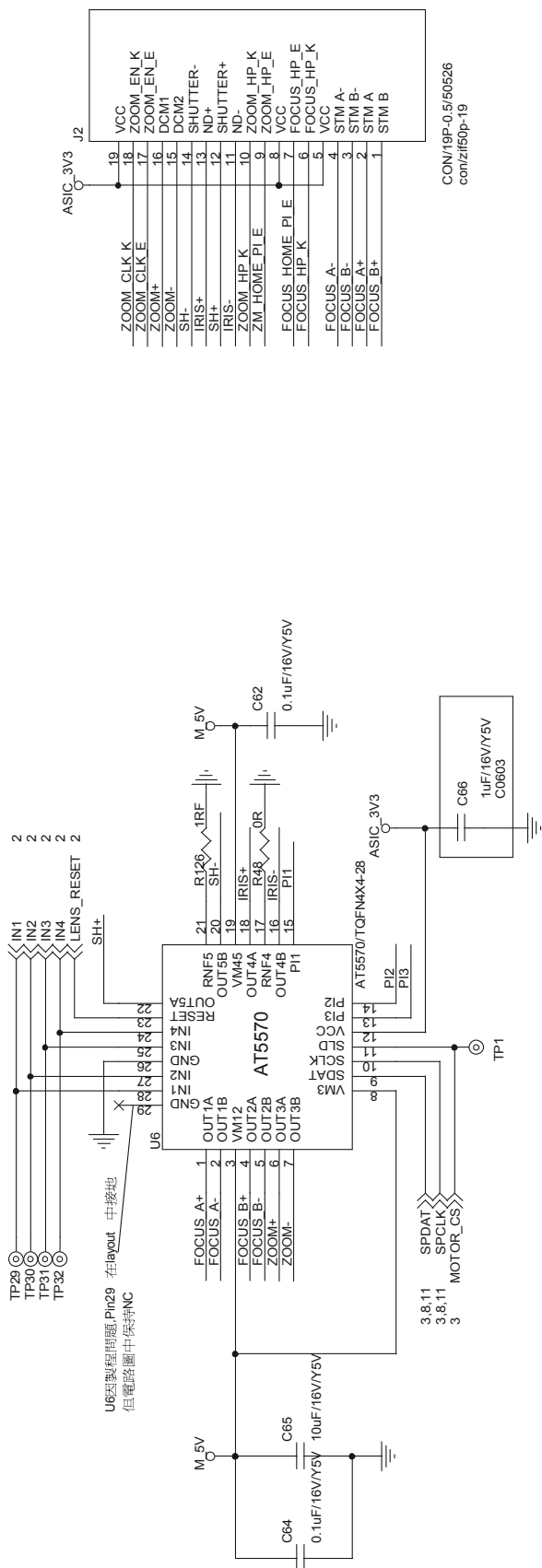
Fc : Cut-Off Frequency



CONFIGURE STATUS	
TV_DETECT	USBIO
USB DOWNLOAD	L
NAND BOOT	Open
AV MODE	H/L
	L

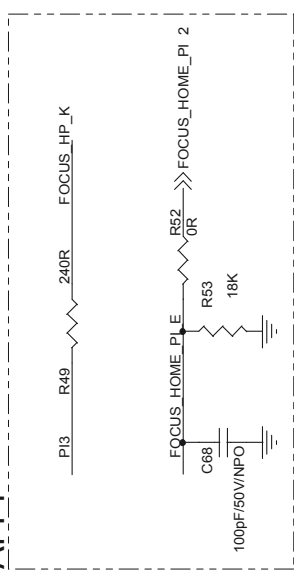
(B)CFG Pins	CFG5	CFG4	CFG3	CFG2	CFG1
16/32M NAND	1	0	0	1	0
64/128M NAND	1	0	1	0	0
USB Boot	0	0	0	0	1

8-7 Main PCB-7

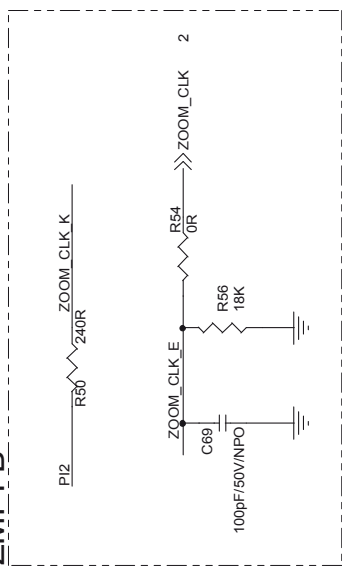


CON/10P-0.5/50526
comzif50p-19

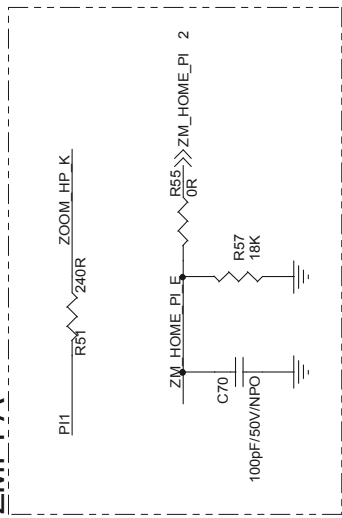
AFPI



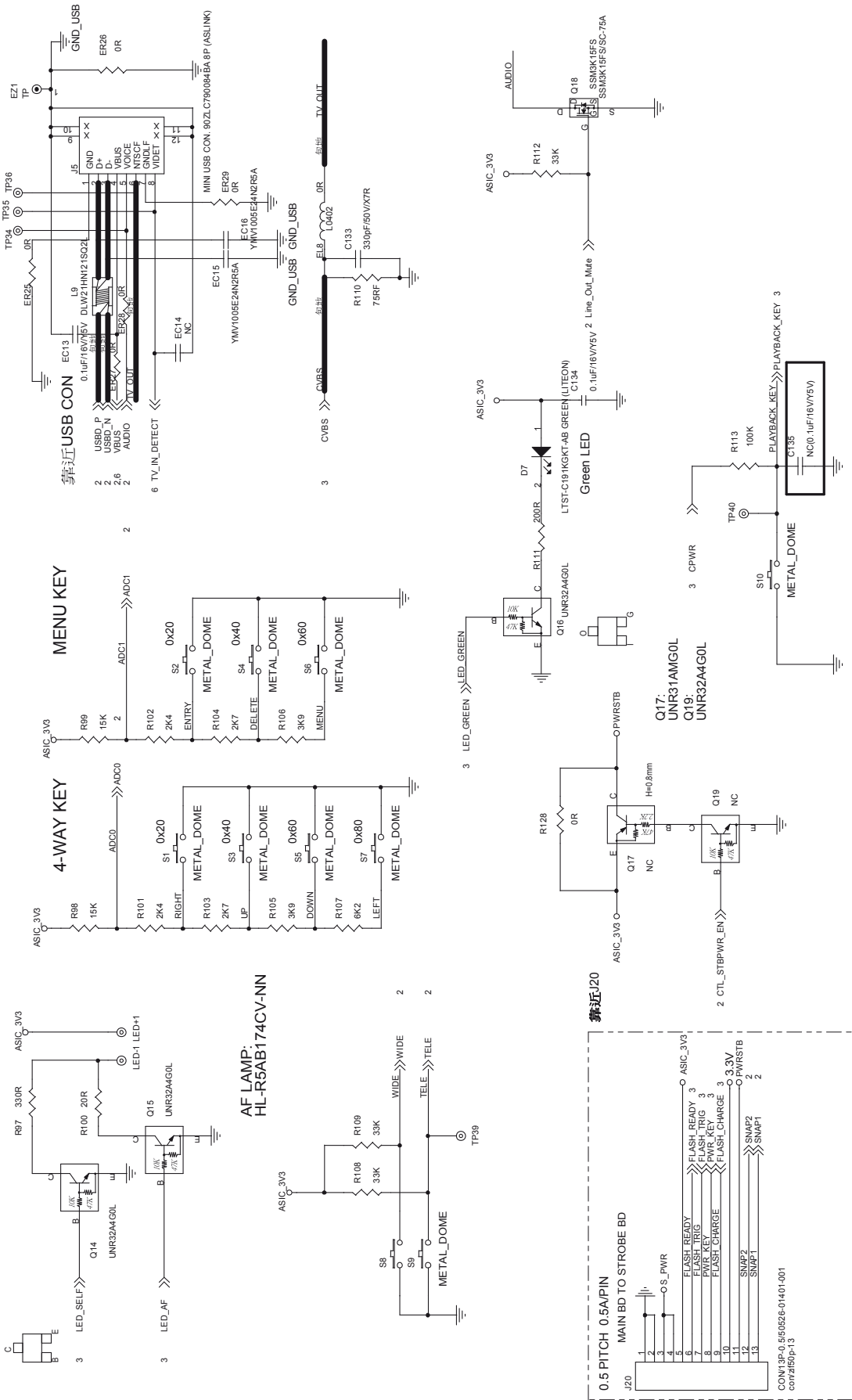
ZMPI-B



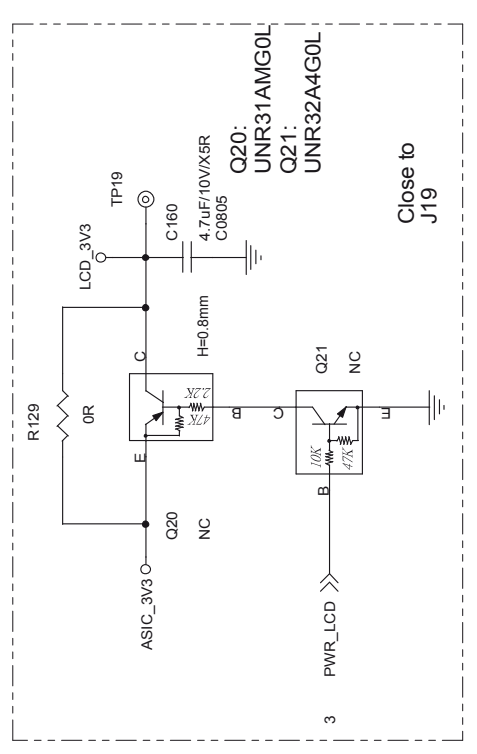
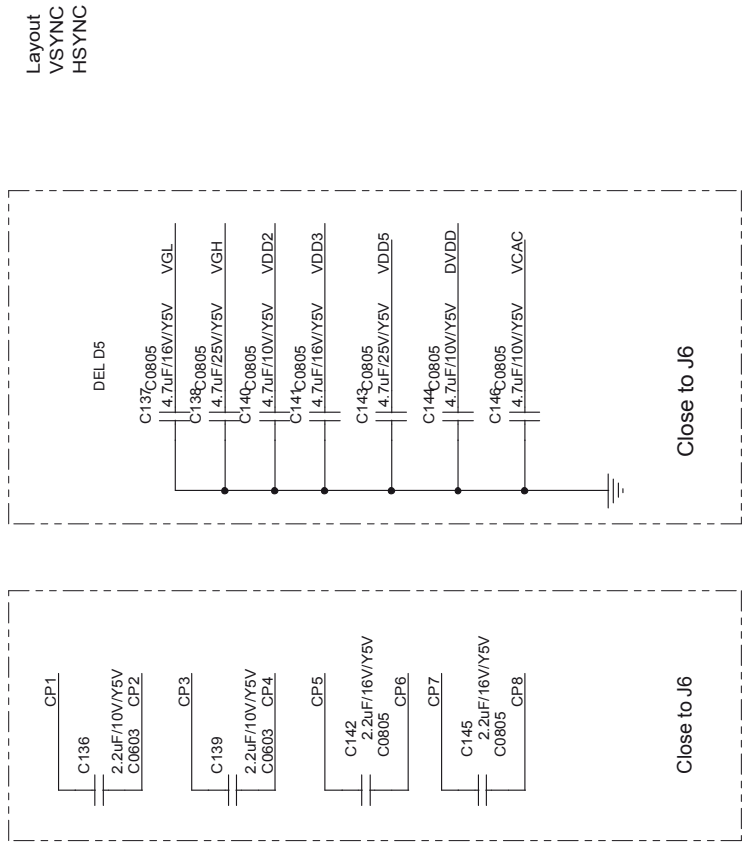
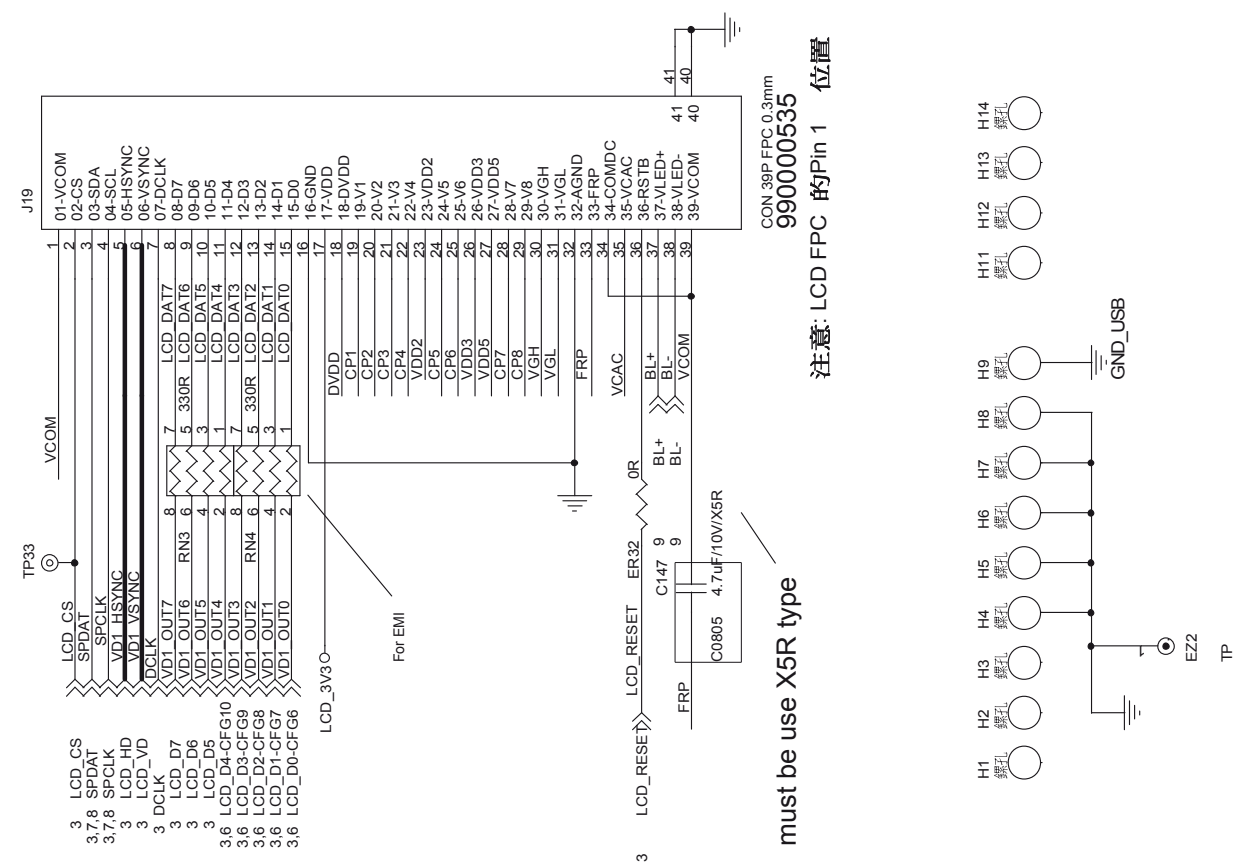
ZMPI-A



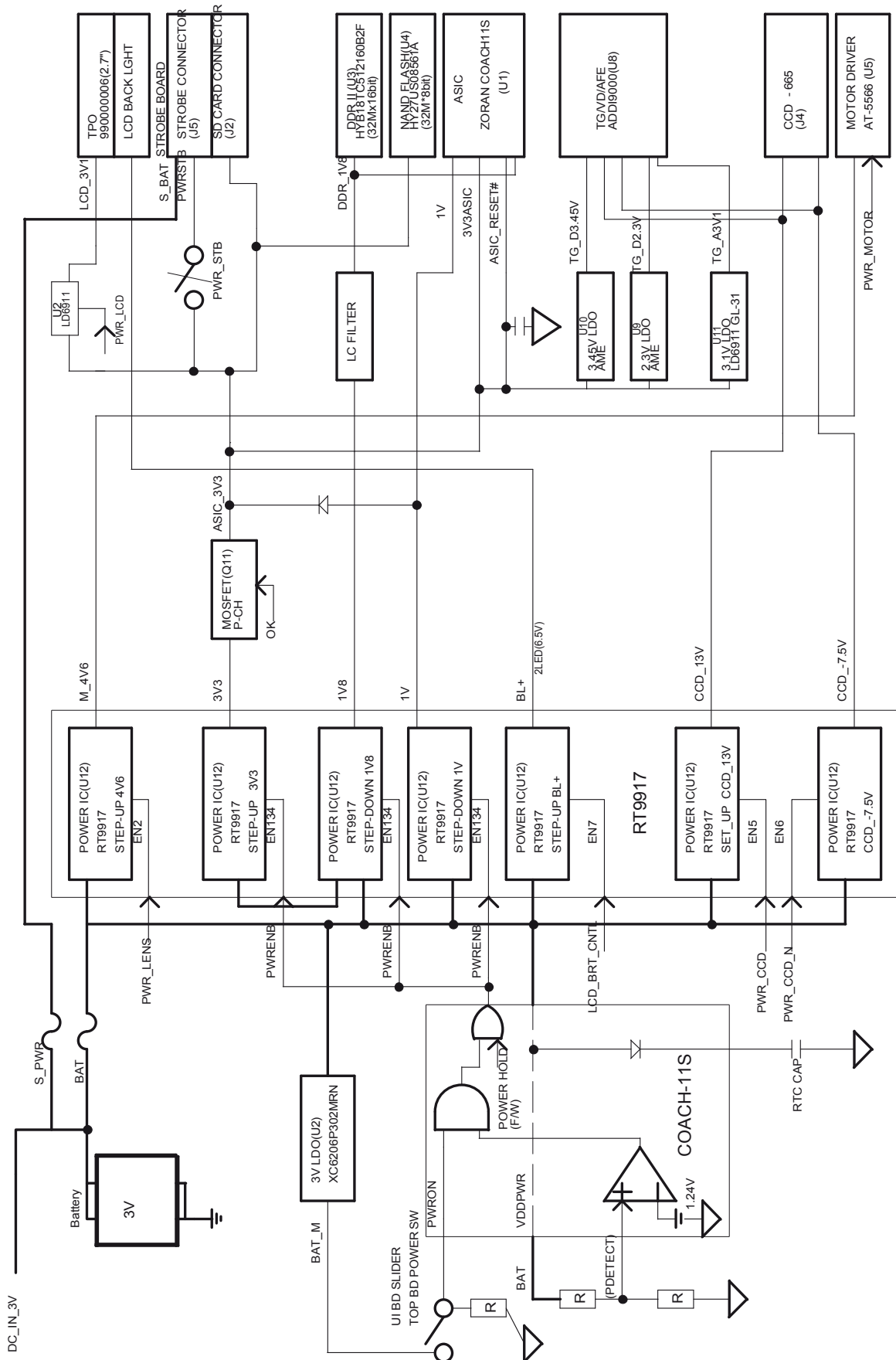
8-10 Main PCB-10



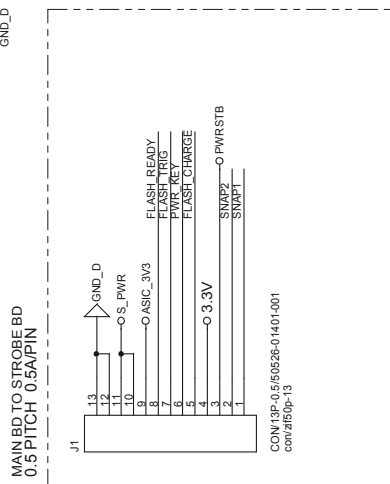
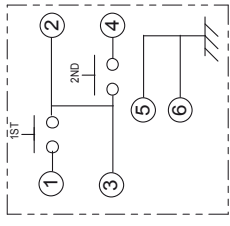
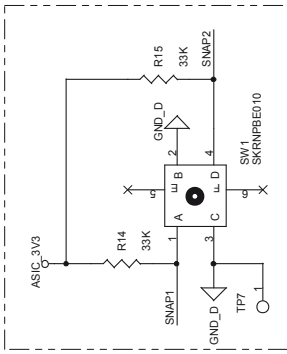
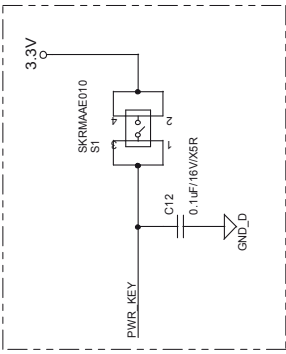
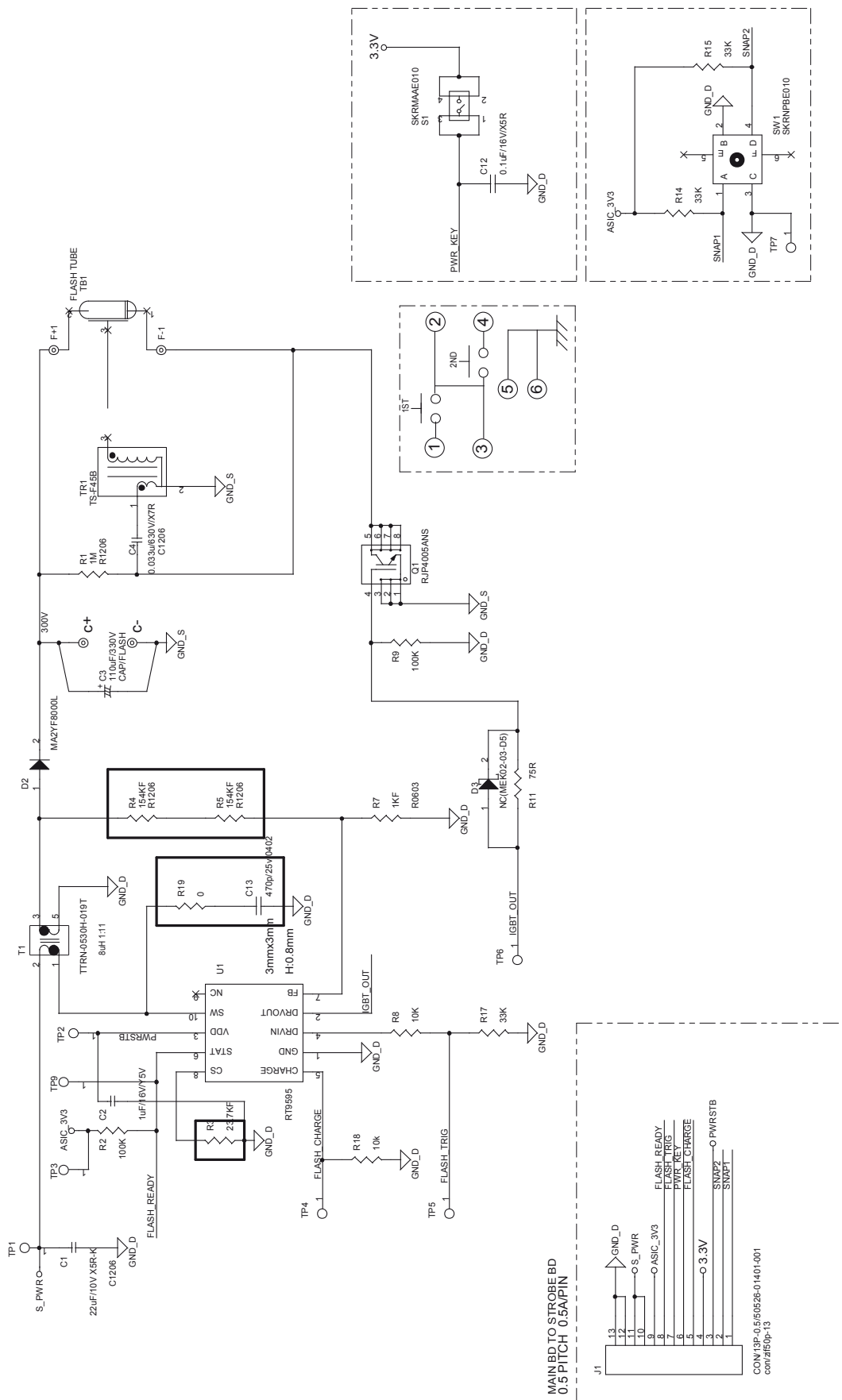
8-11 Main PCB-11



8-12 Main PCB-12



8-13 Strobe PCB



8-14 CCD FPC PCB

