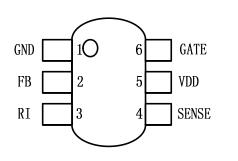
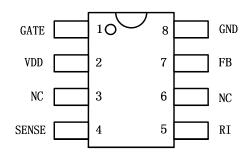
P-DIP8&SOP8





Terminal Assignment

Pin Name	I/O	Description		
GND	P	Ground		
FB	Ι	Feedback input pin. The PWM duty cycle is determined by voltage level into this pin and SENSE pin input.		
RI	Ι	Internal Oscillator frequency setting pin. A resistor connected between RI and GND sets the PWM frequency.		
SENSE	I	Current sense input pin. Connected to MOSFET current sensing resistor node.		
VDD	P	Chip DC power supply pin.		
GATE	О	Totem-pole gate drive output for the power MOSFET.		

Ordering Information

Part number	Mark	Package	Temperature
OB2263	S1XY	SOT23-6	–20°C to +85°C