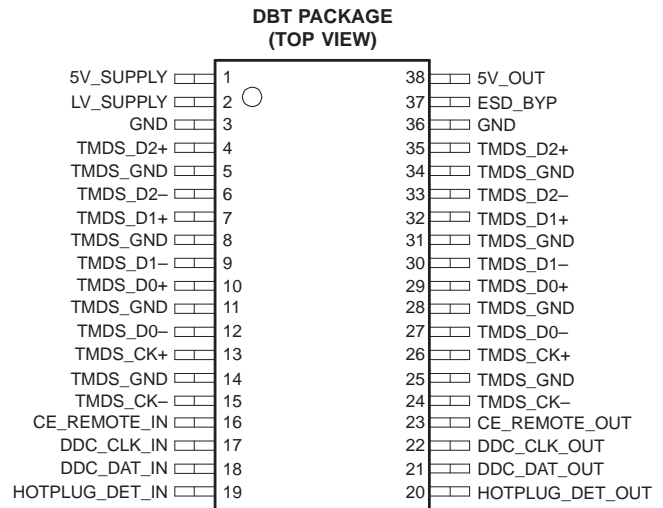


FEATURES

- **Single-Chip ESD Solution for High-Definition Multimedia Interface (HDMI) Driver**
- **0.9 pF Capacitance for High-Speed Transition Minimized Differential Signaling (TMDS) Lines**
- **0.05-pF Matching Capacitance Between the Differential Signal Pair**
- **Integrated Level Shifting for the Control Lines**
- **±8-kV Contact ESD Protection on External Lines**
- **38-Pin Thin Shrink Small-Outline Package (TSSOP) Provides Seamless Layout Option With HDMI Connector**
- **Backdrive Protection**
- **Lead-Free Package**
- **On-Chip Current Regulator With 55-mA Current Output**



APPLICATIONS

- PCs
- Consumer Electronics
- Set-Top Boxes
- DVD/HD Players

DESCRIPTION/ORDERING INFORMATION

The TPD12S521 is a single-chip ESD solution for the high-definition multimedia interface (HDMI) transmitter port. In many cases, the core ICs, such as the scalar chipset, may not have robust ESD cells to sustain system-level ESD strikes. In these cases, the TPD12S521 provides the desired system level ESD protection, such as the IEC61000-4-2 (Level 4) ESD, by absorbing the energy associated with the ESD strike.

While providing the ESD protection, the TPD12S521 adds little or no additional glitch in the high-speed differential signals (see [Figure 3](#) and [Figure 4](#)). The high-speed transition minimized differential signaling (TMDS) lines add only 0.9 pF capacitance to the lines. In addition, the monolithic integrated circuit technology ensures that there is excellent matching between the two-signal pair of the differential line. This is a direct advantage over discrete ESD clamp solutions where variations between two different ESD clamps may significantly degrade the differential signal quality.

The low-speed control lines offer voltage-level shifting to eliminate the need for an external voltage level-shifter IC. The control line ESD clamps add 3.5 pF capacitance to the control lines. The 38-pin DBT package offers seamless layout routing option to eliminate the routing glitch for the differential signal pair.

The TPD12S521 provides an on-chip regulator with current output ratings of 55 mA at pin 38. Maximum overcurrent protection output drop at 55 mA on 5V_OUT is 100 mV. This current enables the HDMI receiver detection even when the receiver device is powered-off. DBT package pitch (0.5 mm) matches with HDMI connector pitch. In addition, pin mapping follows the same order as the HDMI connector pin mapping. This HDMI receiver port protection and interface device is specifically designed for next-generation HDMI transmitter protection.

PRODUCT PREVIEW



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

TPD12S521
SINGLE-CHIP HDMI TRANSMITTER PORT PROTECTION AND INTERFACE DEVICE

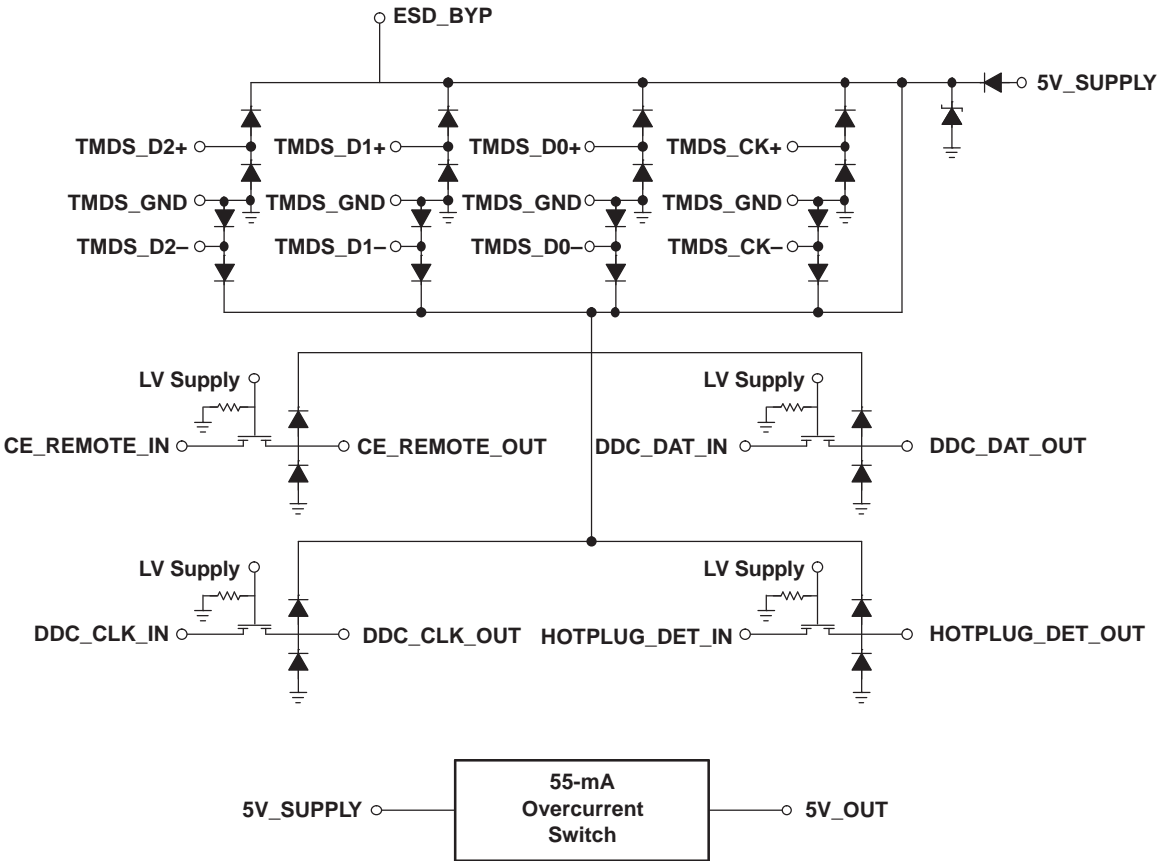
SLVS639–OCTOBER 2007

ORDERING INFORMATION

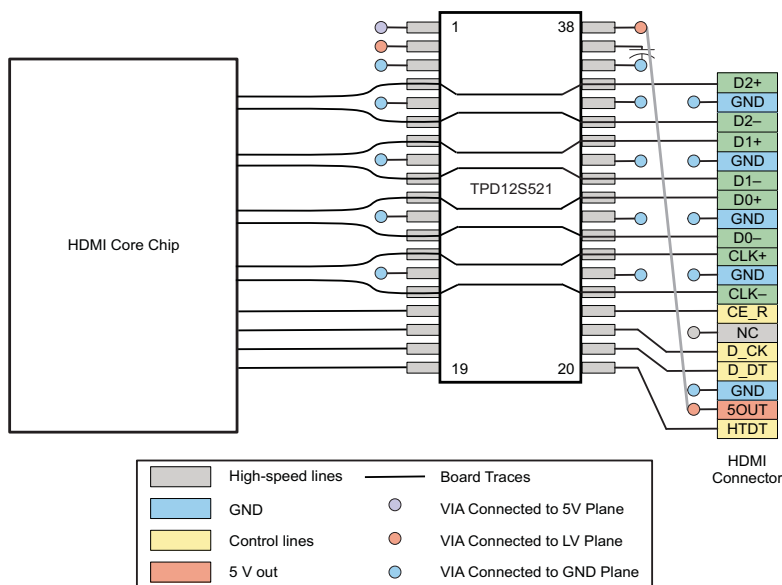
T _A	PACKAGE ⁽¹⁾⁽²⁾	STANDARD FINISH		LEAD-FREE FINISH	
		ORDERABLE PART NUMBER ⁽³⁾	TOP-SIDE MARKING	ORDERABLE PART NUMBER ⁽³⁾	TOP-SIDE MARKING
–40°C to 85°C	TSSOP-38	TPD12S521DBTR	PREVIEW	TPD12S521DBTR	PREVIEW

- (1) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.
 (2) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.
 (3) Parts are shipped in tape-and-reel form, unless otherwise specified.

ELECTRICAL SCHEMATIC



PRODUCT PREVIEW



A. External bypass capacitors and resistor components not included

Figure 1. Board Layout for HDMI Transmitter Using TPD12S521DBTR

PIN DESCRIPTION

PIN NO.	NAME	DESCRIPTION
4, 35	TMDS_D2+	TMDS 0.9-pF ESD protection ⁽¹⁾
6, 33	TMDS_D2–	TMDS 0.9-pF ESD protection ⁽¹⁾
7, 32	TMDS_D1+	TMDS 0.9-pF ESD protection ⁽¹⁾
9, 30	TMDS_D1–	TMDS 0.9-pF ESD protection ⁽²⁾
10, 29	TMDS_D0+	TMDS 0.9-pF ESD protection ⁽²⁾
12, 27	TMDS_D0–	TMDS 0.9-pF ESD protection ⁽²⁾
13, 26	TMDS_CLK+	TMDS 0.9-pF ESD protection ⁽²⁾
15, 24	TMDS_CLK–	TMDS 0.9-pF ESD protection ⁽²⁾
16	CE_REMOTE_IN	LV_SUPPLY referenced logic level in
23	CE_REMOTE_OUT	5V_SUPPLY referenced logic level out, plus 3.5-pF ESD
17	DDC_CLK_IN	LV_SUPPLY referenced logic level in
22	DDC_CLK_OUT	5V_SUPPLY referenced logic level out, plus 3.5-pF ESD
18	DDC_DAT_IN	LV_SUPPLY referenced logic level in
21	DDC_DAT_OUT	5V_SUPPLY referenced logic level out, plus 3.5-pF ESD
19	HOTPLUG_DET_IN	LV_SUPPLY referenced logic level in
20	HOTPLUG_DET_OUT	5V_SUPPLY referenced logic level out, plus 3.5-pF ESD
2	LV_SUPPLY	Bias for CE/DDC/HOTPLUG level shifters
1	5V_SUPPLY	Current source for 5V_OUT
38	5V_OUT	55-mA minimum overcurrent protected 5-V output. This output must be bypassed with a 0.1-μF ceramic capacitor.
37	ESD_BYP	ESD bypass. This pin must be connected to a 0.1-μF ceramic capacitor.
3, 5, 8, 11, 14, 25, 28, 31, 34, 36	GND/TMDS_GND	GND reference

(1) These two pins must be connected together inline on the PCB.

(2) These two pins must be connected together inline on the PCB.

Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)

	MIN	MAX	UNIT
V_{CC5} V_{CC3}		6	V
DC voltage at any channel input	GND – 0.5	$V_{CC} + 0.5$	V
T_{stg} Storage temperature range	–65	150	°C

Recommended Operating Conditions

		MIN	NOM	MAX	UNIT
Operating supply voltage	5V_SUPPLY		5	5.5	V
Bias supply voltage	LV_SUPPLY	1	3.3	5.5	V
Operating temperature range		–40		85	°C

Electrical Characteristics

over operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
I _{CC5}	Operating supply current	5V_SUPPLY = 5 V			110	130	μA
I _{CC3}	Bias supply current	LV_SUPPLY = 3.3 V			1	5	μA
V _{DROP}	5V_OUT overcurrent output drop	5V_SUPPLY = 5 V, I _{OUT} = 55 mA			65	100	mV
I _{SC}	5V_OUT short-circuit current limit	5V_SUPPLY= 5 V, 5V_OUT = GND		90	135	175	mA
I _{OFF}	OFF-state leakage current, level-shifting NFET	LV_SUPPLY = 0 V			0.1	5	μA
I _{BACKDRIVE}	Current conducted from output pins to V_SUPPLY rails when powered down	5V_SUPPLY < V _{CH_OUT}	TMDS_D[2:0]+/-, TMDS_CK+/-, CE_REMOTE_OUT, DDC_DAT_OUT, DDC_CLK_OUT, HOTPLUG_DET_OUT, 5V_OUT		0.1	5	μA
V _{ON}	Voltage drop across level-shifting NFET when ON	LV_SUPPLY = 2.5 V, V _S = GND, I _{DS} = 3 mA		75	95	140	mV
V _F	Diode forward voltage	I _F = 8 mA, T _A = 25°C ⁽¹⁾	Top diode	0.6	0.85	0.95	V
			Bottom diode	0.6	0.85	0.95	
V _{ESD}	ESD withstand voltage	Pins 4, 7, 10, 13, 20–24, 27, 30, 33 ⁽¹⁾⁽²⁾		IEC	±8		kV
		Pins 1, 2, 16–19, 37, 38 ⁽¹⁾⁽³⁾		HBM	±2		
V _{CL}	Channel clamp voltage at 8-kV HBM ESD	T _A = 25°C ⁽¹⁾⁽³⁾	Positive transients	9			V
			Negative transients	–9			
R _{DYN}	Dynamic resistance	I = 1 A, T _A = 25°C ⁽⁴⁾	Positive transients	3			Ω
			Negative transients	1.5			
I _{LEAK}	TMDS channel leakage current	T _A = 25°C ⁽¹⁾			0.01	1	μA
C _{IN} , TMDS	TMDS channel input capacitance	5V_SUPPLY= 5 V, Measured at 1 MHz, V _{BIAS} = 2.5 V ⁽¹⁾			0.9	1.1	pF
ΔC _{IN} , TMDS	TMDS channel input capacitance matching	5V_SUPPLY= 5 V, Measured at 1 MHz, V _{BIAS} = 2.5 V ⁽¹⁾⁽⁵⁾			0.05		pF
C _{MUTUAL}	Mutual capacitance between signal pin and adjacent signal pin	5V_SUPPLY= 0 V, Measured at 1 MHz, V _{BIAS} = 2.5 V ⁽¹⁾			0.07		pF
C _{IN}	Level-shifting input capacitance, capacitance to GND	5V_SUPPLY= 0 V, Measured at 100 KHz, V _{BIAS} = 2.5 V ⁽¹⁾	DDC		3.5	4	pF
			CEC		3.5	4	
			HP		3.5	4	

(1) This parameter is specified by design and verified by device characterization.

(2) Standard IEC 61000-4-2, C_{DISCHARGE} = 150 pF, R_{DISCHARGE} = 330 Ω

(3) Human-Body Model (HBM) per MIL-STD-883, Method 3015, C_{DISCHARGE} = 100 pF, R_{DISCHARGE} = 1.5 kΩ

(4) These measurements performed with no external capacitor on ESD_BYP.

(5) Intrapair matching, each TMDS pair (i.e., D+, D–)

PRODUCT PREVIEW

TYPICAL PERFORMANCE

Typical Filter Performance ($T_A = 25^\circ\text{C}$, DC Bias = 0 V, 50- Ω Environment)

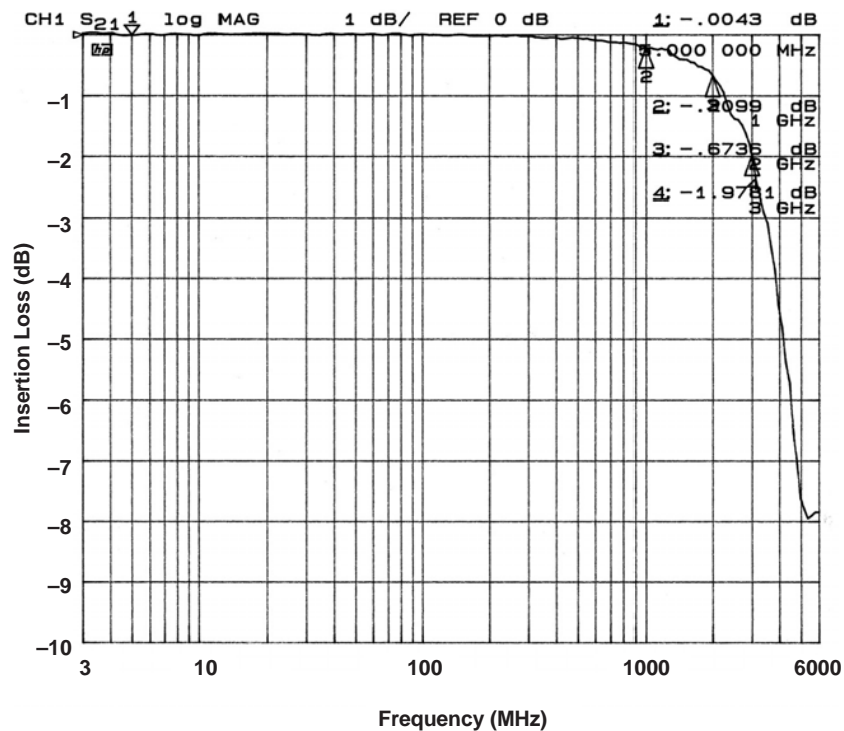


Figure 2. Insertion Loss vs Frequency (TMD5_D1- to GND)

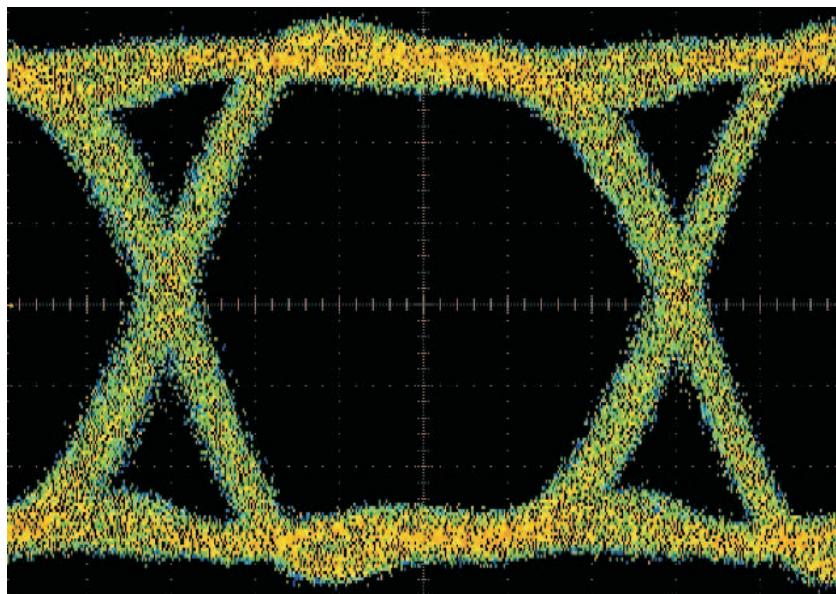


Figure 3. Eye Diagram With TPD12S521

TYPICAL PERFORMANCE (continued)

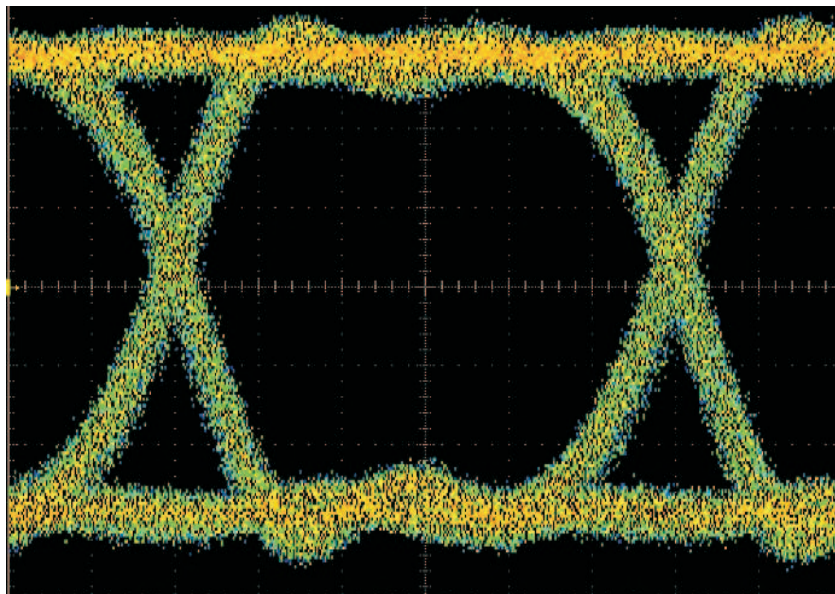


Figure 4. Eye Diagram Without TPD12S521

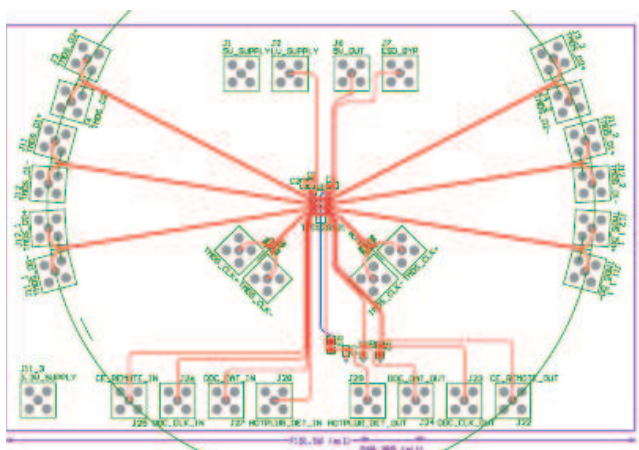


Figure 5. Test Board to Measure Eye Diagram for the TPD12S521 (Refer to Eye Diagram Plot)

PRODUCT PREVIEW

IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products

Amplifiers	amplifier.ti.com
Data Converters	dataconverter.ti.com
DSP	dsp.ti.com
Interface	interface.ti.com
Logic	logic.ti.com
Power Mgmt	power.ti.com
Microcontrollers	microcontroller.ti.com
RFID	www.ti-rfid.com
Low Power Wireless	www.ti.com/lpw

Applications

Audio	www.ti.com/audio
Automotive	www.ti.com/automotive
Broadband	www.ti.com/broadband
Digital Control	www.ti.com/digitalcontrol
Military	www.ti.com/military
Optical Networking	www.ti.com/opticalnetwork
Security	www.ti.com/security
Telephony	www.ti.com/telephony
Video & Imaging	www.ti.com/video
Wireless	www.ti.com/wireless

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265
Copyright © 2007, Texas Instruments Incorporated