

CN3
HDMI/90D
CON-HDMI

20 SHIELD
21 SHIELD
18 SHIELD
17 +5V
15 CEC/DDC GND
16 DDC SCL
13 DDC SDA
19 CEC
22 HPD
2 SHIELD
2 Dat2 shield
5 Dat1 shield
8 Dat0 shield
11 clk shield
23 SHIELD

10 CLK+
12 CLK-
7 DAT0+
9 DAT0-
4 DAT1+
6 DAT1-
1 DAT2+
3 DAT2-

+5V_HDMI

+5V_HDMI
C146 100nF/10V
C0402

+5V_HDMI
R200 1.8K/5% R0402
R201 1.8K/5% R0402

+5V_Normal
+5V_HDMI
FB2 L0603 Bead/6R/500mA

R221 100R/5% R0402
R227 100R/5% R0402

D11 NC/CDS2C16GTH_3pF
D10 NC/CDS2C16GTH_3pF
D9 NC/CDS2C16GTH_3pF
D8 NC/CDS2C16GTH_3pF

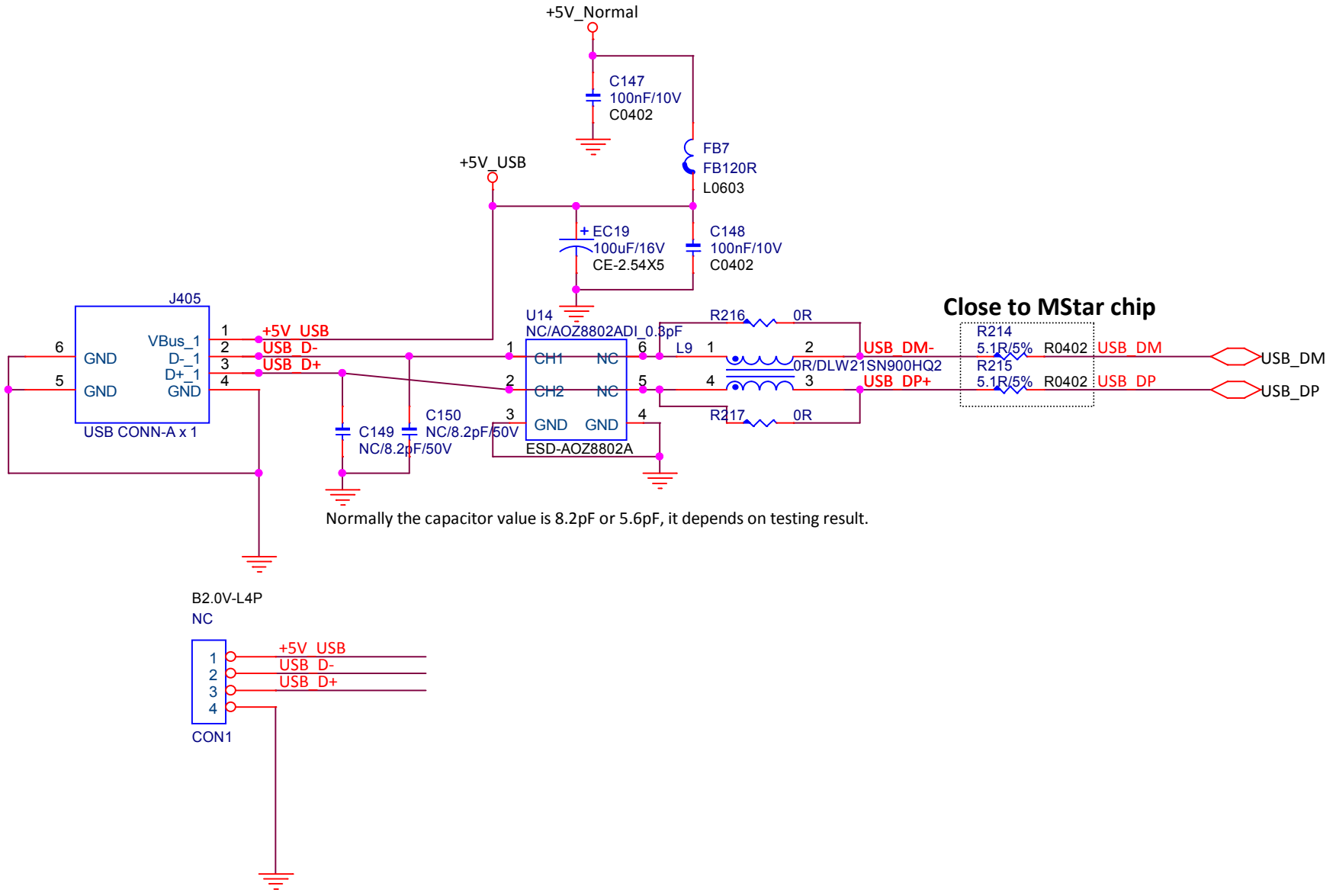
HDMI-TXSCL
HDMI-TXSDA

R202 200R/5% R0402
HDMI-TXCEC
HDMI-TXHDP
R246 1K/5% R0402
R203 10K/5% R0402

Close to MStar chip

U11 NC/RClamp0524P
U12 NC/RClamp0524P

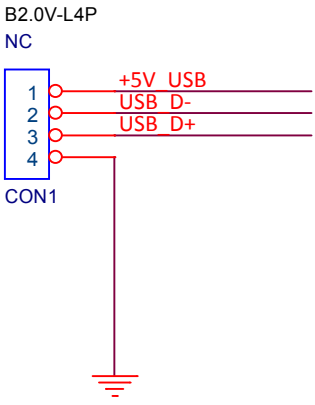
HDMI CLKP R204 10R/5% R0402 HDMI-TXCLKP
HDMI CLKN R205 10R/5% R0402 HDMI-TXCLKN
HDMI D0+ R206 10R/5% R0402 HDMI-TX0P
HDMI D0- R207 10R/5% R0402 HDMI-TX0N
HDMI D1+ R208 10R/5% R0402 HDMI-TX1P
HDMI D1- R209 10R/5% R0402 HDMI-TX1N
HDMI D2+ R210 10R/5% R0402 HDMI-TX2P
HDMI D2- R211 10R/5% R0402 HDMI-TX2N

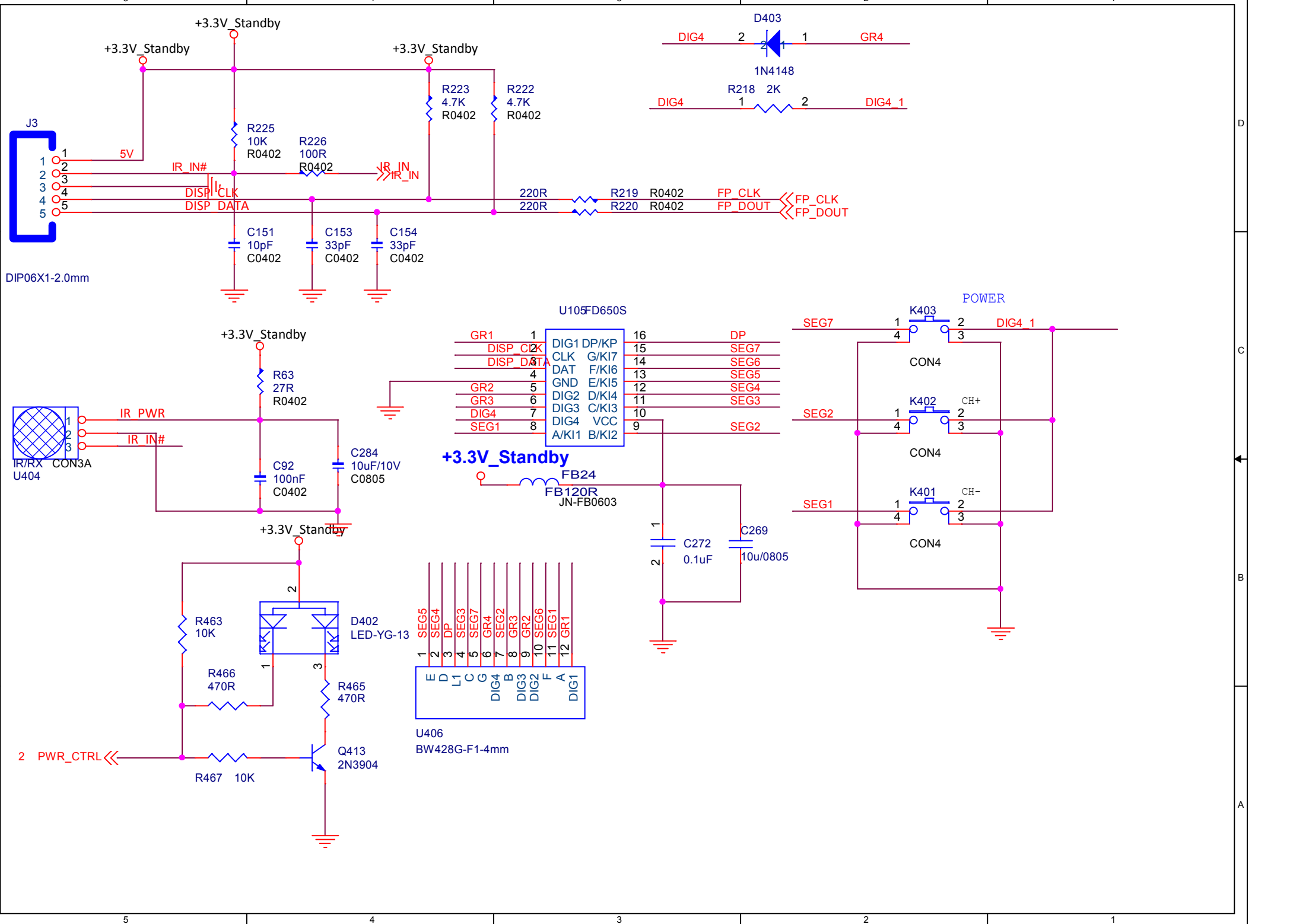


Normally the capacitor value is 8.2pF or 5.6pF, it depends on testing result.

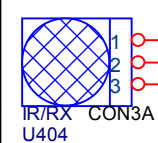
Close to MStar chip

- R214 5.1R/5% R0402
- R215 5.1R/5% R0402

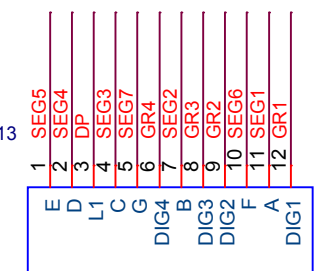




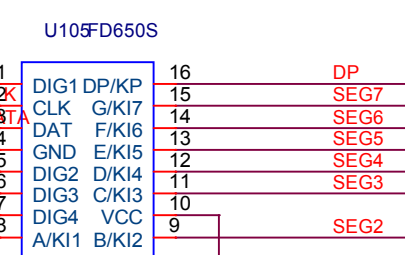
DIP06X1-2.0mm



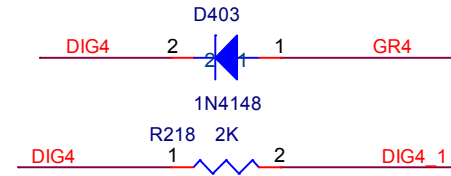
IR/RX CON3A U404



U406
BW428G-F1-4mm

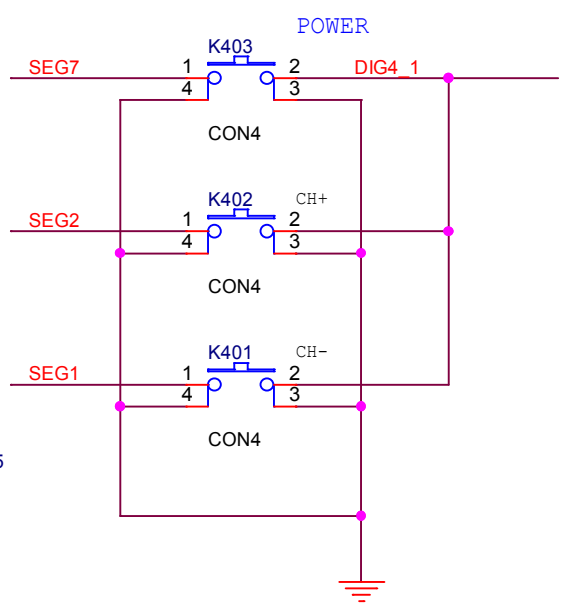


U105FD650S



D403
1N4148

R218 2K



POWER

CON4

CON4

CON4

2 PWR_CTRL

A

A

D

D

C

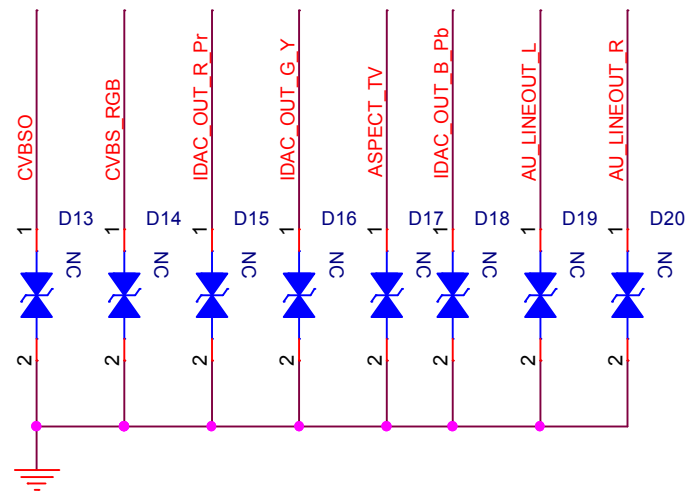
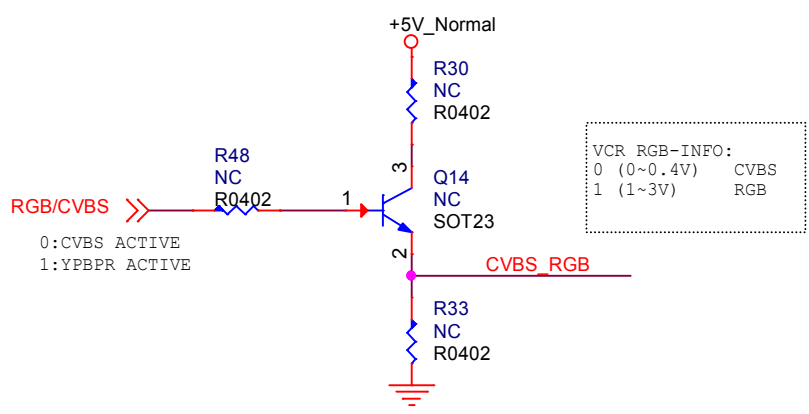
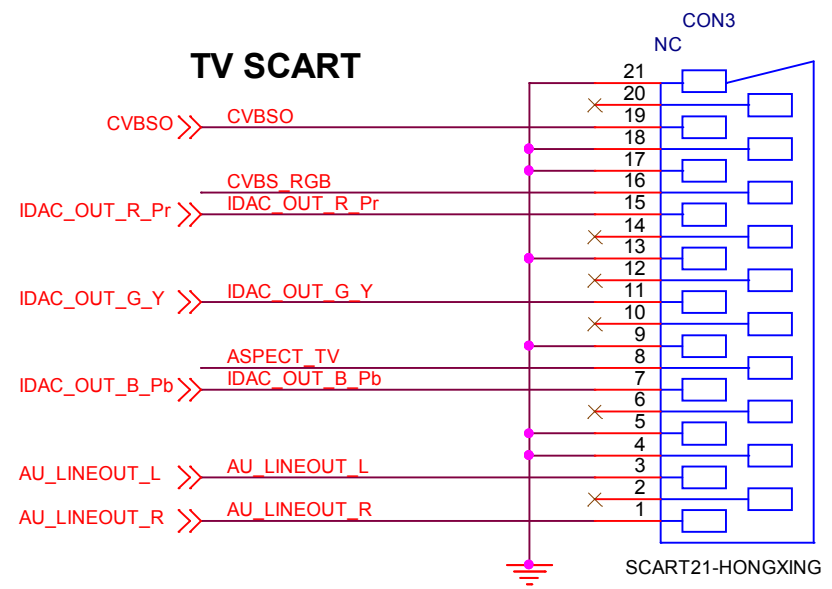
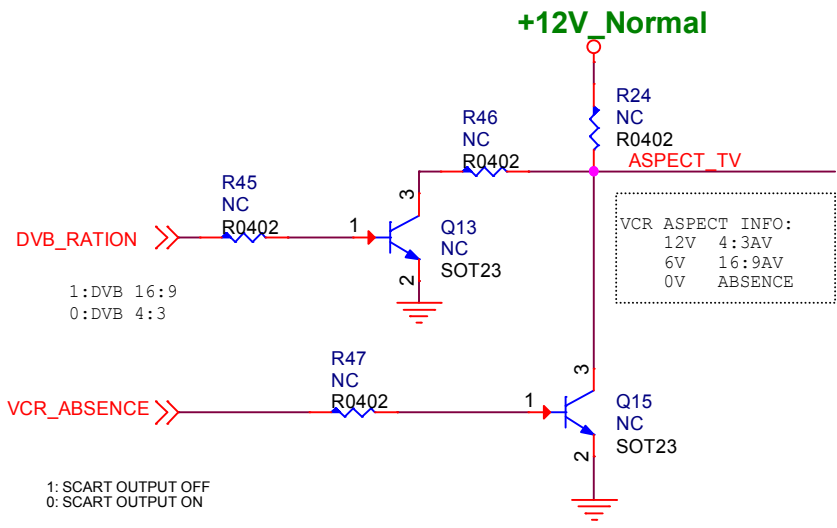
C

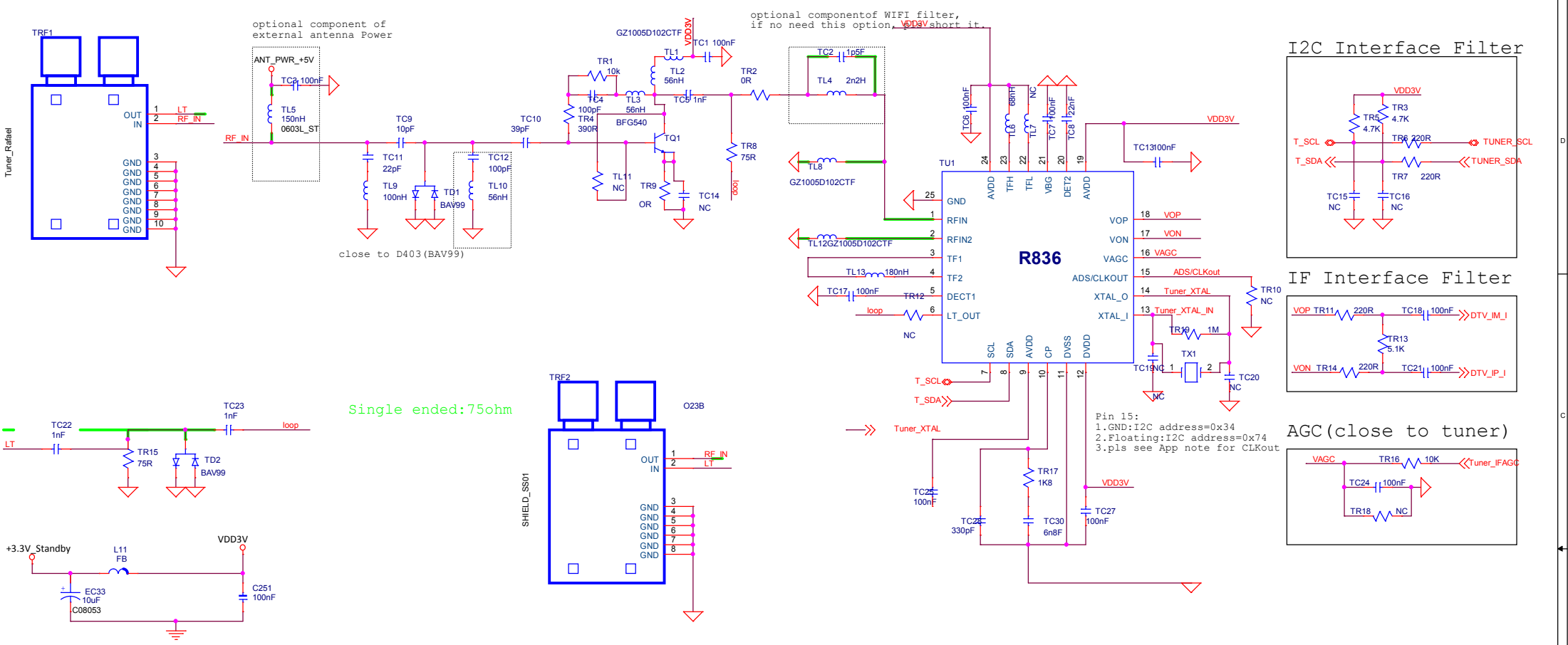
B

B

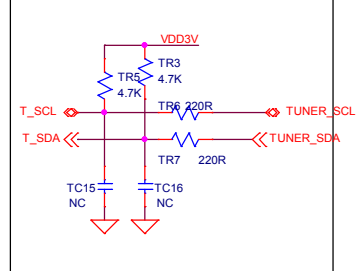
A

A

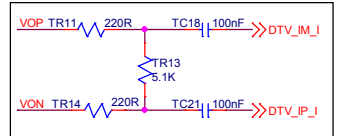




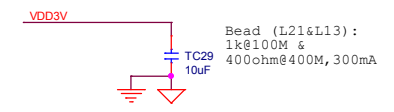
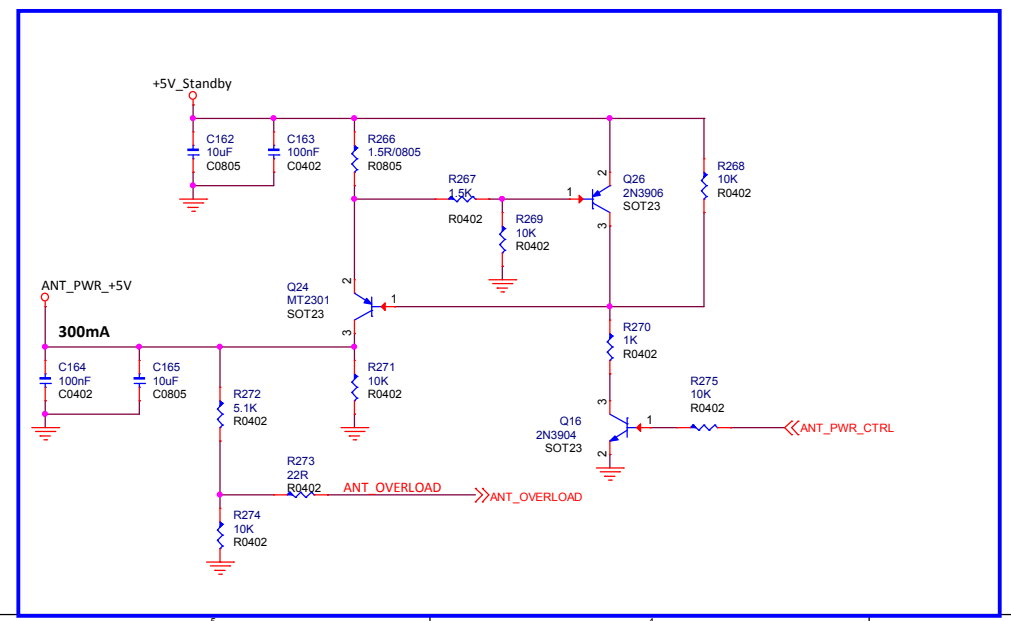
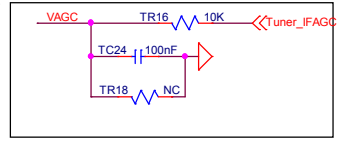
I2C Interface Filter



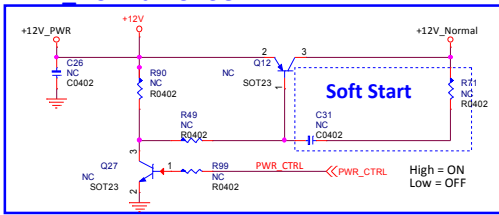
IF Interface Filter



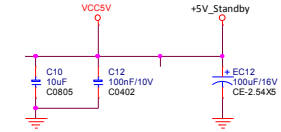
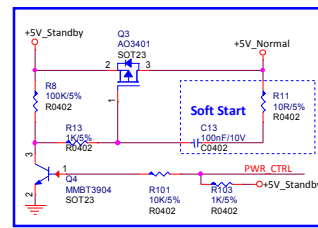
AGC (close to tuner)



+12V Normal for SCART

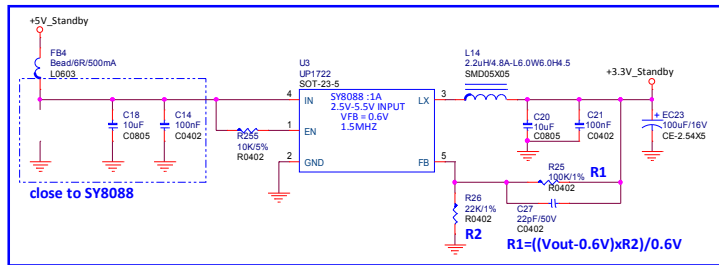


5V Normal Power



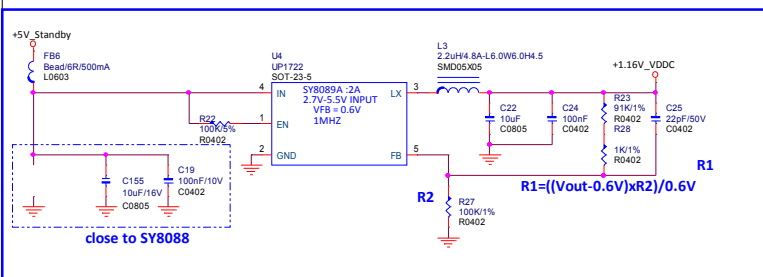
$$R1 = ((V_{out} - 0.6V) \times R2) / 0.6V$$

+3.3V Standby



$$R1 = ((V_{out} - 0.6V) \times R2) / 0.6V$$

+1.16V Core Power



$$R1 = ((V_{out} - 0.6V) \times R2) / 0.6V$$

+1.5V +1.8V_DDR

