2. GENERAL BLOCK DIAGRAM

The control section will be explained as shown in the block diagram.

(1) ASIC (IC4) ..... Composed mainly of an address decoder, modem control section, CPU and RTC.
Controls the general FAX operations.
Controls the operation panel I/F.
Controls the thermal head I/F and CIS I/F.
Executes image processing.
I/O ports

(2) ROM (IC2) ..... Contains all of the program instructions for unit operations and voice prompt for TAM.

(3) Static RAM (IC1) ..... This memory is used mainly for the parameter working storage area.

(4) Flash memory (IC7) ..... This memory is used for TAM.

(5) MODEM (IC5) ..... Executes modulation and demodulation for the FAX and SP-Phone communication.

(6) CODEC (IC6) ..... Executes code and decode for Digital SP-Phone communication.

(7) Read section ..... Contact Image Sensor (CIS) to read transmitting documents.

(8) Thermal Head ..... Contains heating elements for dot matrix image printing.

(9) Motor driver (IC8) ..... Drives the motor and CIS LED.

(10) Reset circuit (IC3) ..... The reset pulse is made from 5V power supply, and then it is input to ASIC(IC4).

(11) Analog board ..... Composed of an ITS circuit and NCU circuit.

(12) Sensor section ..... Composed of a cover open sensor, document sensor, recording paper sensor, JAM sensor, motor position sensors, and read position sensor.

(13) Power supply ..... Supplies +8V and +24V to the unit.

General Block Diagram