

CLEVO M720SR System Block Diagram

AC-IN, CHARGER
SHEET 32

MULTI I/O BOARD
SPK_R, RJ-11, LED
LID, HOT KEY, USB, 3G
SHEET 34,35

14.318 MHz
Clock Generator
ICS9LPR600
56pins TSSOP
17.1*8.1*1.2mm
SHEET 17

**Intel Merom
PROCESSOR**
479 pins socket P
35*35*2.7mm
SHEET 2,3

Memory Termination
SHEET 10

DDRII SO-DIMMO
SHEET 9
DDRII SO-DIMM1
SHEET 10

SYSTEM POWER, 2.5V
SHEET 27

+VCORE
SHEET 28

VDD3, VDD5, 3.3V, 5V
SHEET 29

1.05VS, 1.5V, 1.2V
SHEET 30

1.8V, 0.9VS
SHEET 31

LCD CONNECTOR,
INVERTER
SHEET 12

LVDS (TV)
Sis307ELV
169balls BGA
13*13*1.7mm
SHEET 11

**NORTH BRIDGE
SisM672**
852balls TEBGA
35*35*2.4mm
SHEET 4,5,6,7,8

Clock Buffer
ICS9P935
28pins SSOP
17.1*8.1*1.2mm
SHEET 17

CLICK BOARD
TOUCH PAD
Synaptic
810602-1703
SHEET 33

CRT
SHEET 12

FSB
667/800 MHz

533/667(/800) MHz

SPDIF OUT
MIC IN
HP OUT

AZALIA
MDC
MODULE

Azalia Codec
Realtek
ALC883
48pins LQFP
9*9*1.6mm
SHEET 24

AUDIO AMP
ANPAC
APA2020
24pins TSSOP
9.8*6.4*1.2mm
SHEET 25

HP AMP
TI
TPA4411
20pins TQFN
4.15*4.15*0.8mm
SHEET 24

INT SPK
SHEET 25,34

INT MIC
SHEET 24

32.768 KHz
EC
ITE 8512E
128pins LQFP
14*14*1.6mm
SHEET 26

SPI
SHEET 26

LPC

33 MHz
LPC
SHEET 18

MuTIOL 1G

**SOUTH BRIDGE
Sis968**
570balls mBGA
27*27*2.5mm
SHEET 13,14,15,16

AZALIA LINK 24 MHz

PCI BUS 33 MHz

PCIE 100 MHz

GMAC

INT. K/B
SHEET 26

EC SMBUS

THERMAL SENSOR
F75383M
SHEET 2

SMART FAN
SHEET 21

SMART BATTERY
SHEET 32

32.768KHz

SATA I/II 3.0Gb/s

PATA-133

USB2.0
480 Mbps

10/100M PHY
RTL8201CL
48pins LQFP
9*9*1.7mm
SHEET 23

New Card
SOCKET
(USB0)
SHEET 20

Mini PCIE
SOCKET
(USB5)
SHEET 20

CARD READER
EnE MR510
128-pin LQFP
14*14*1.4mm
SHEET 22

24.576 MHz

SATA HDD
SHEET 18

ODD
SHEET 19

CCD (USB7)
SHEET 19

Bluetooth (USB3)
SHEET 19
(Optional)
3G CARD (USB3)
SHEET 35

USB2
SHEET 20

USB6
SHEET 20

USB & Phone
Jack B'd
USB4
SHEET 35

FINGER PRINTER BOARD
(USB1)
FingerPrint
SHEET 37
(Optional)
12 MHz

RJ-45

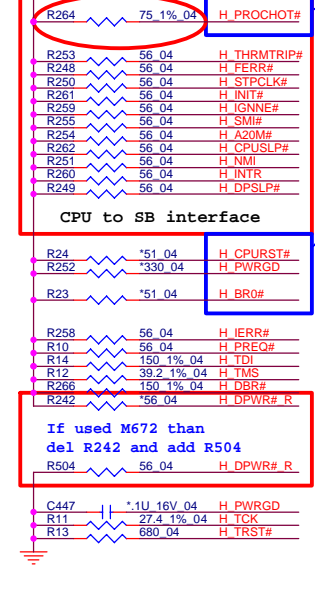
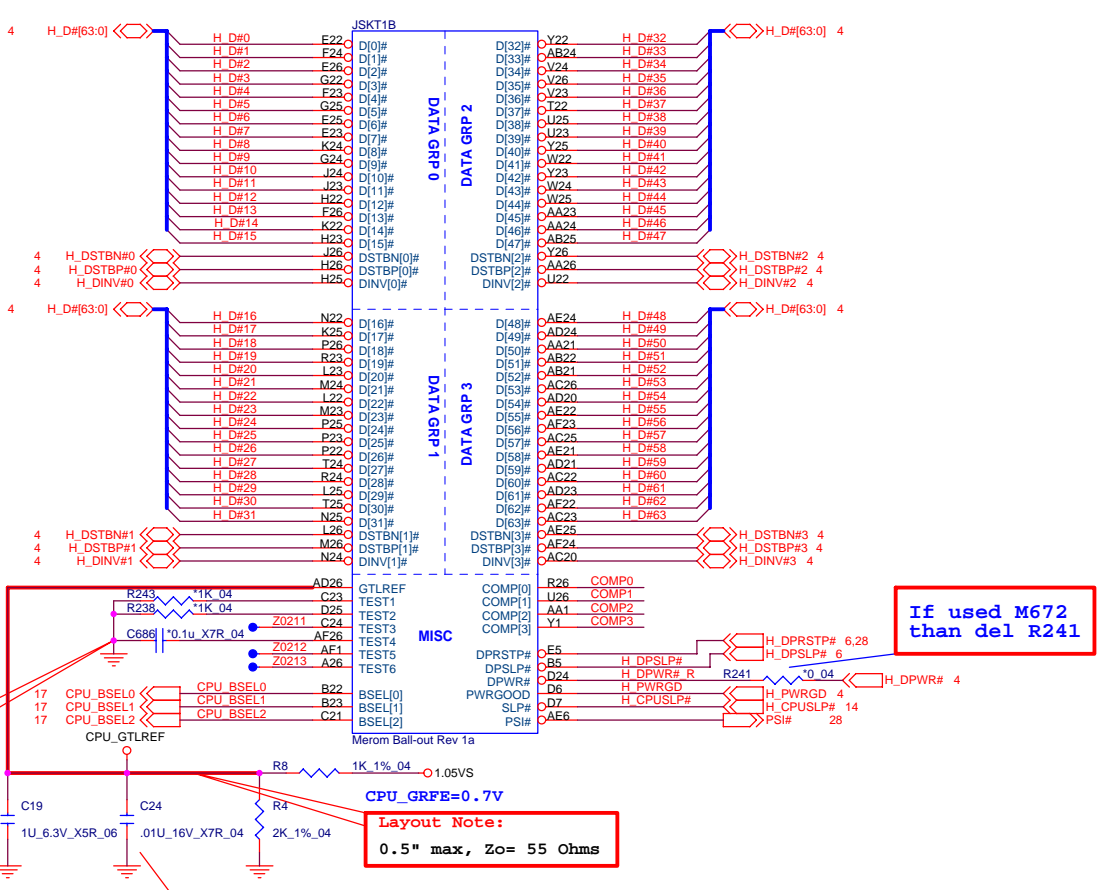
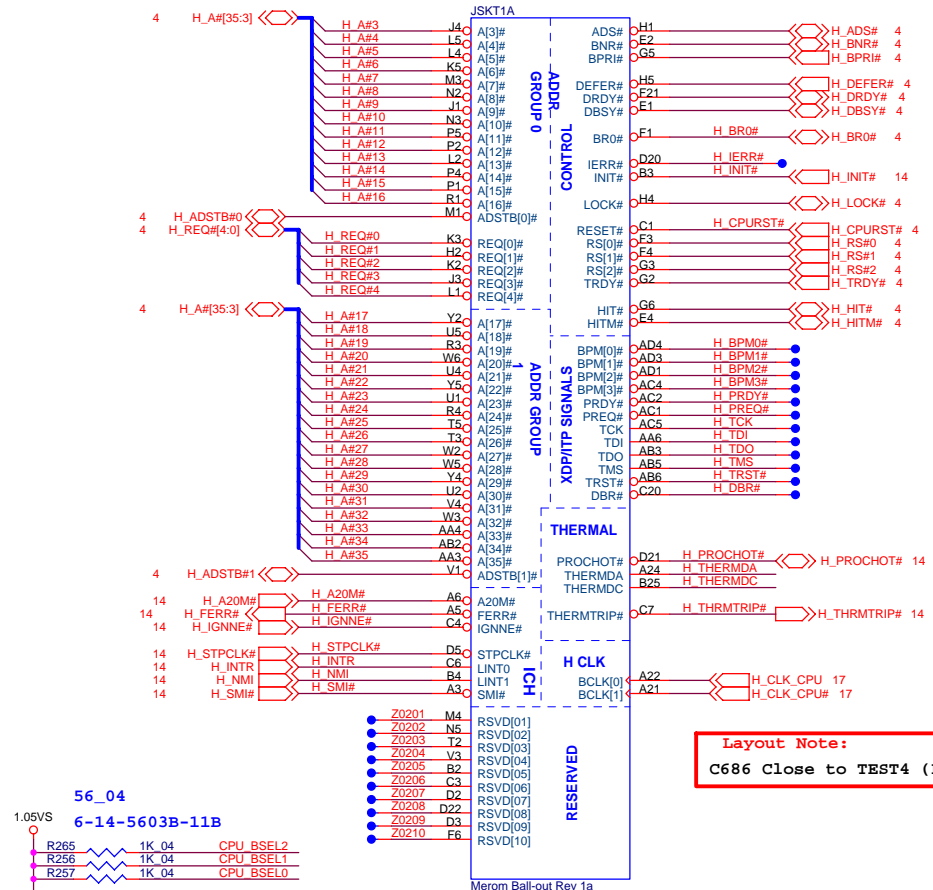
25 MHz

MINI PCIE
GOLAN

7IN1
SOCKET
SHEET 22

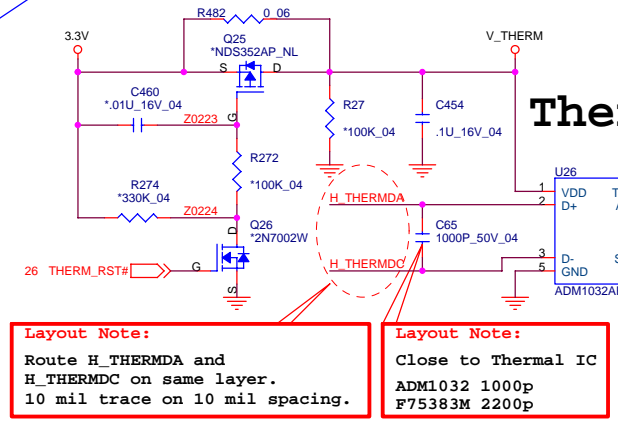
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Title [01]BLOCK DIAGRAM		
Size A3	Document Number M720SR Socket P 6-7P-M7205-002	Rev 2.0A
Date: Thursday, September 06, 2007	Sheet 1	of 37



Layout Note:
 If PROCHOT# is routed between CPU, IMVP and MCH, pull-up resistor has to be 68 ohm ± 5%. If not use, pull-up resistor has to be 56 ohm ± 5%.

IF USED Desktop CPU, H_CPURST#, H_PWRGD, H_BRD, need add pull high resistor



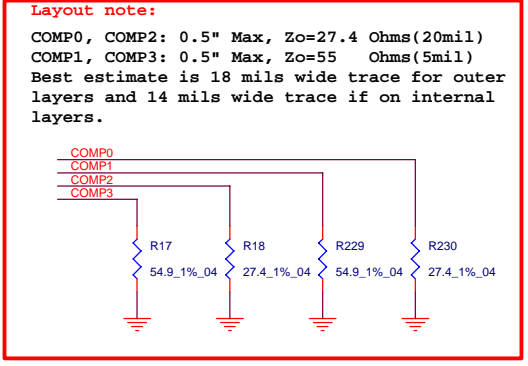
Layout Note:
 Route H_THERMDA and H_THERMDC on same layer. 10 mil trace on 10 mil spacing.

Layout Note:
 Close to Thermal IC
 ADM1032 1000p
 F75383M 2200p

Layout Note:
 C686 Close to TEST4 (Pin AF26)

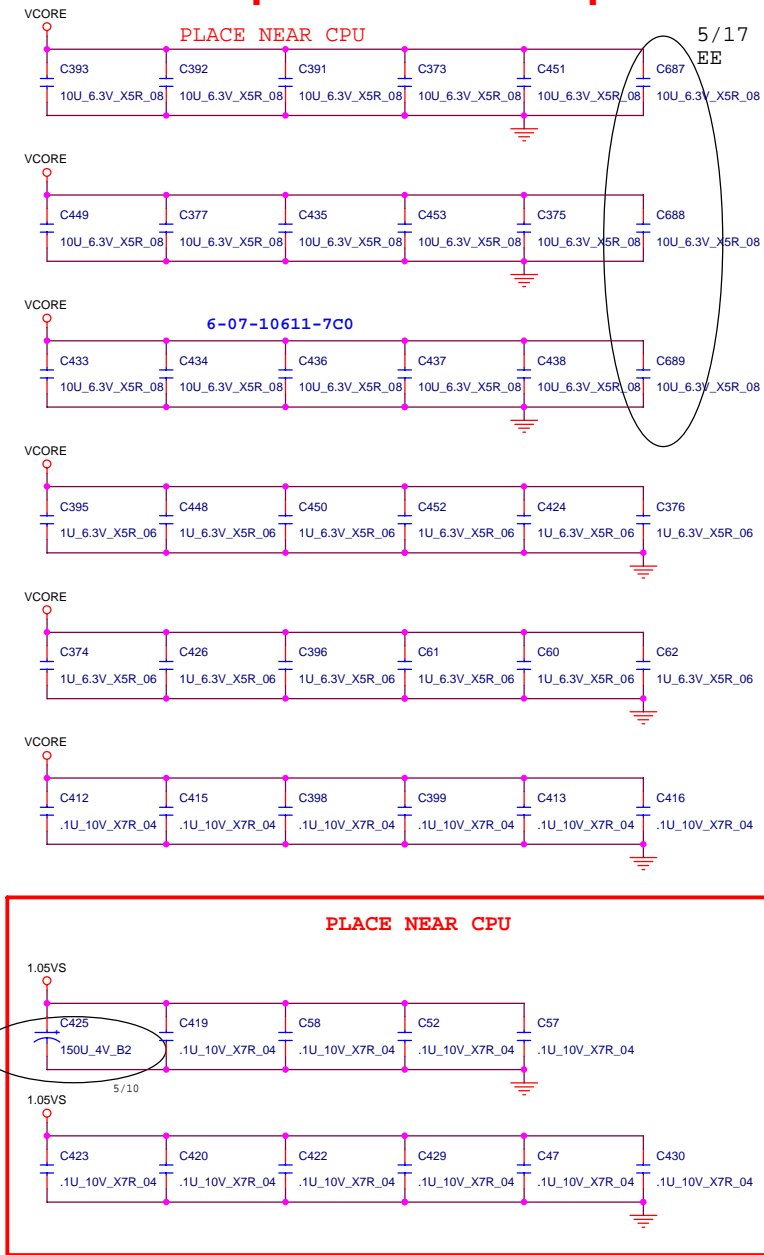
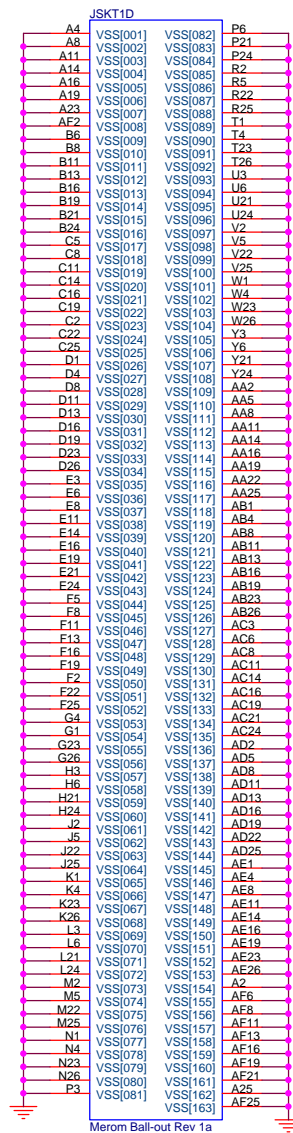
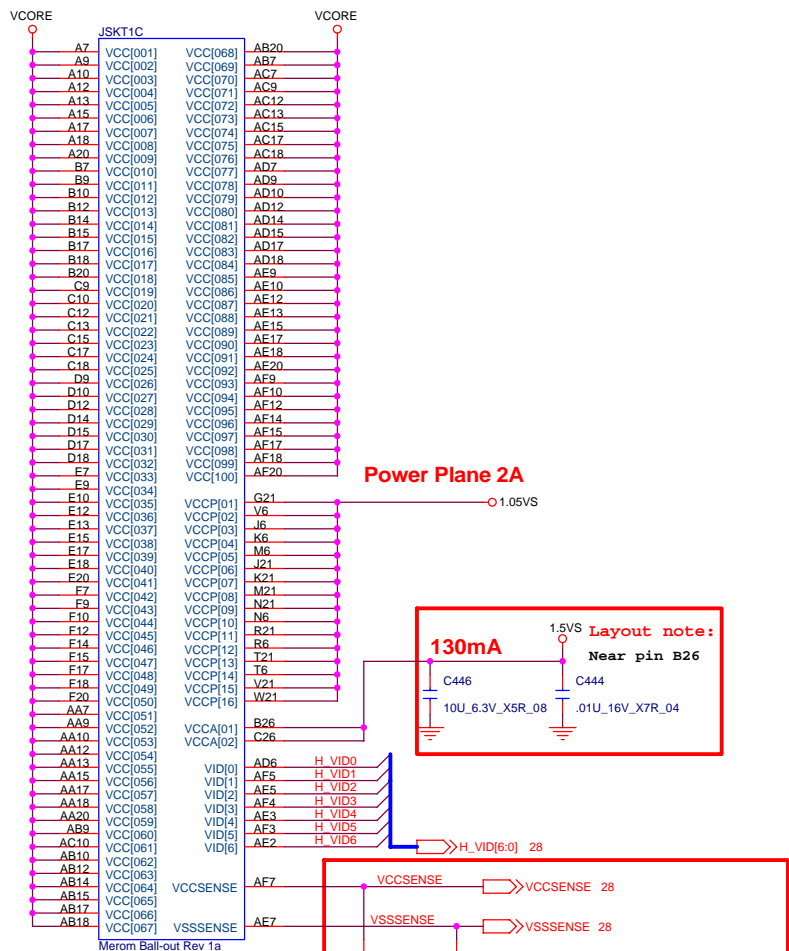
Layout Note:
 CPU_GRFE=0.7V
 0.5" max, Zo= 55 Ohms

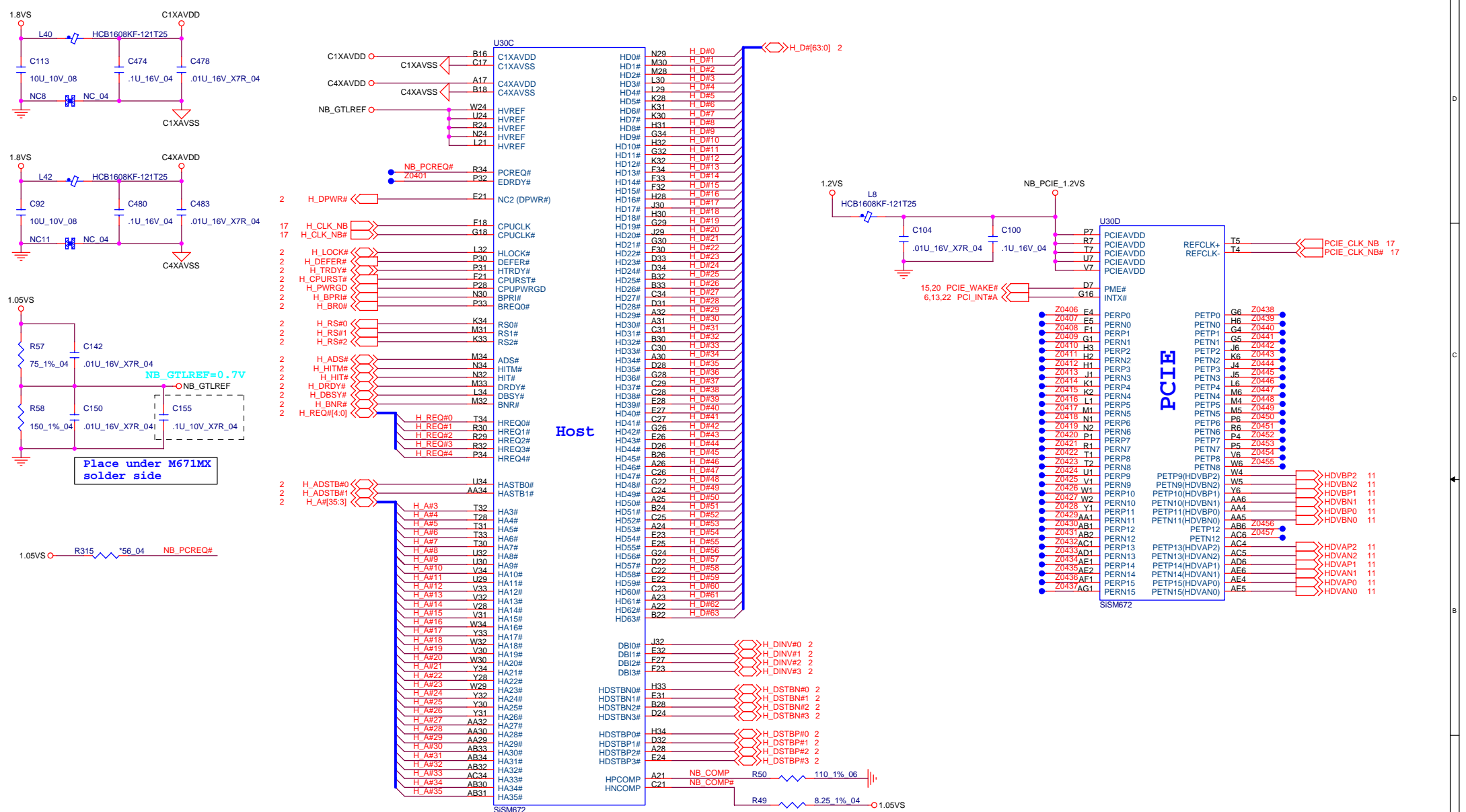
If used M672 than del R241



1.05VS	3.4,6,7,16,30
3.3V	12,14,15,16,18,19,20,23,25,27,29,30,31
VDD3	15,21,26,27,29,31,32

Check cap for santa rosa platform





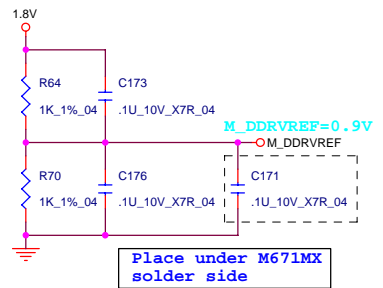
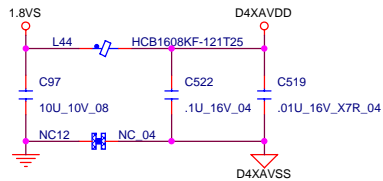
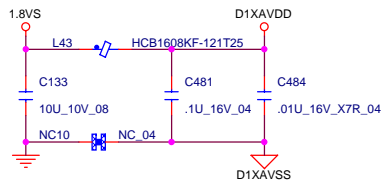
Legend:
 ⊏ 1.05VS 2,3,6,7,16,30
 ⊏ 1.2VS 7,27
 ⊏ 1.8VS 5,6,7,11,12,13,14,15,16,17,27

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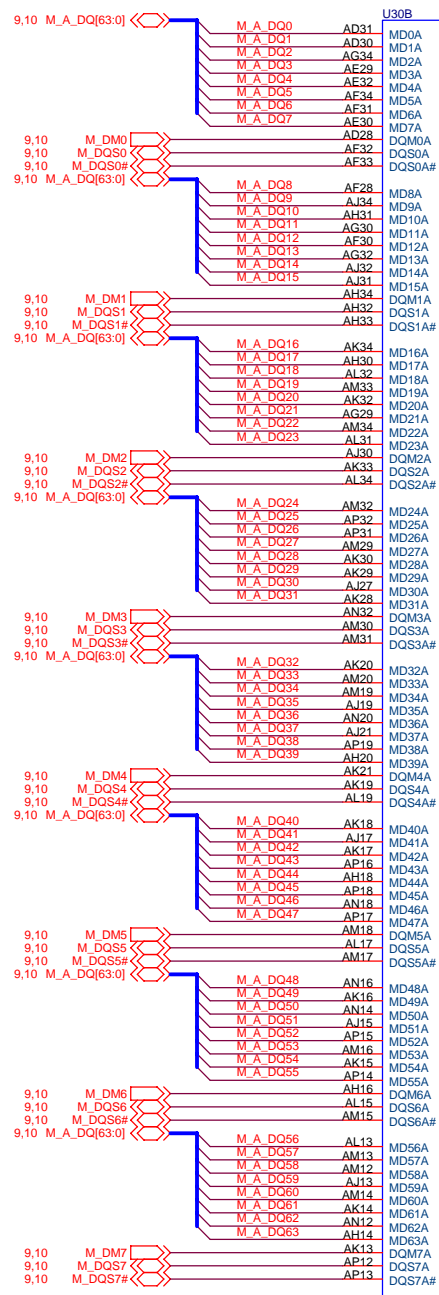
Title: [04]SiSM672_HOST_PCIE 1/5

Size: A3 Document Number: M720SR Socket P 6-71-M7200-D02A Rev: 2.0A

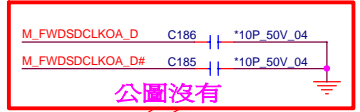
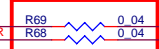
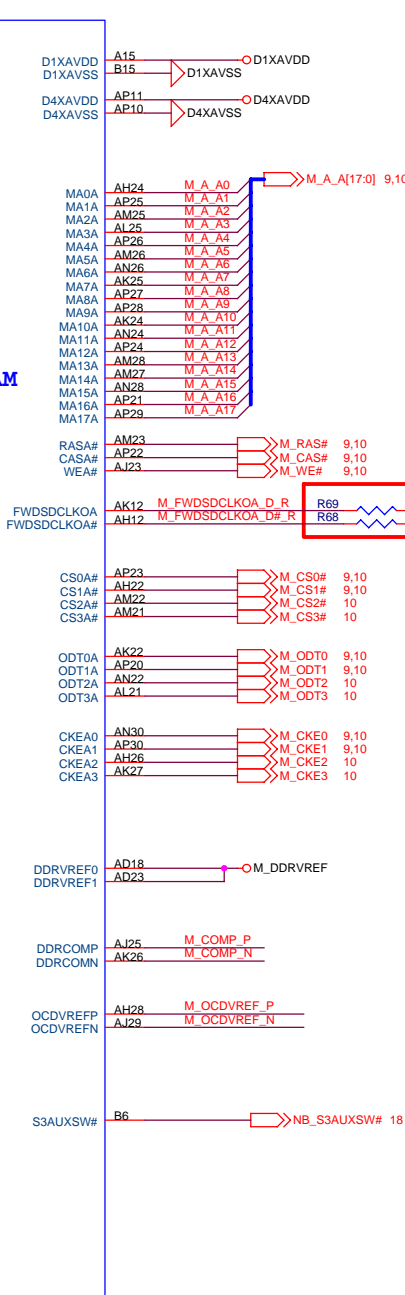
Date: Thursday, September 06, 2007 Sheet: 4 of 37



Place under M671MX solder side

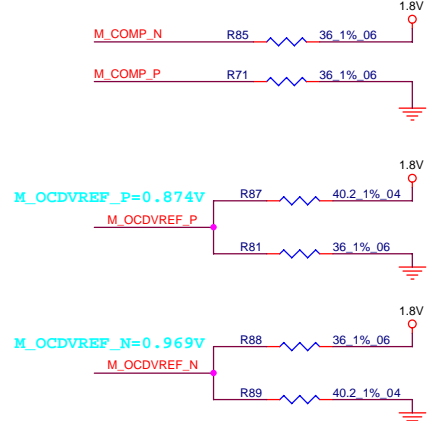


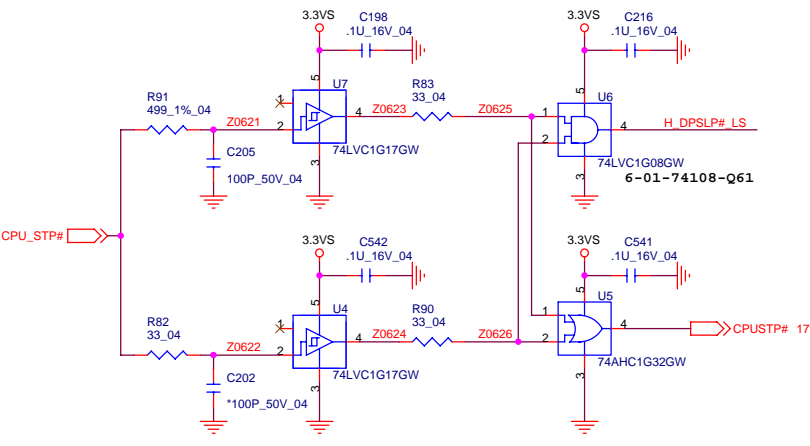
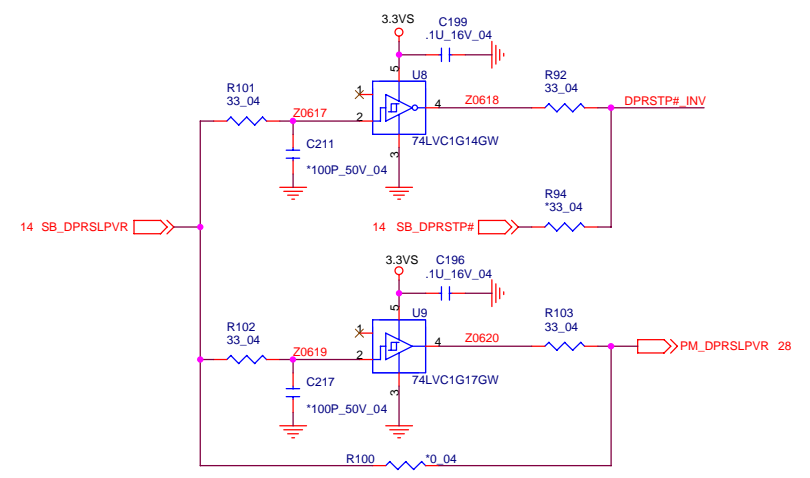
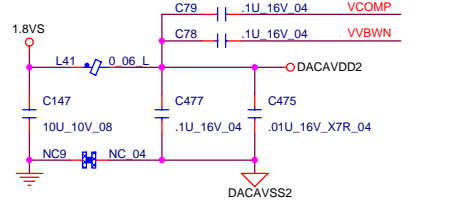
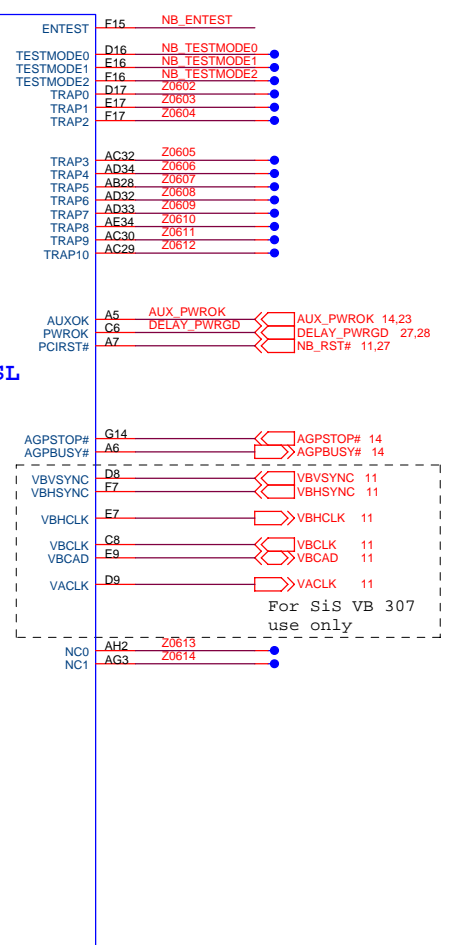
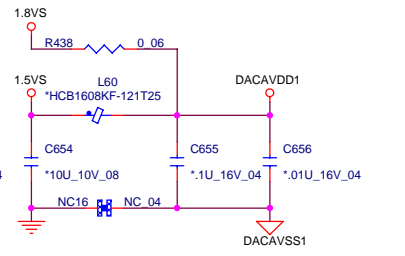
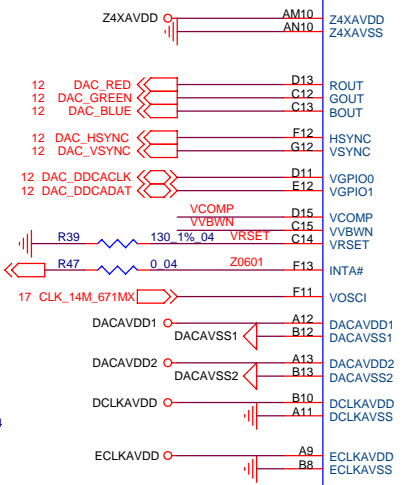
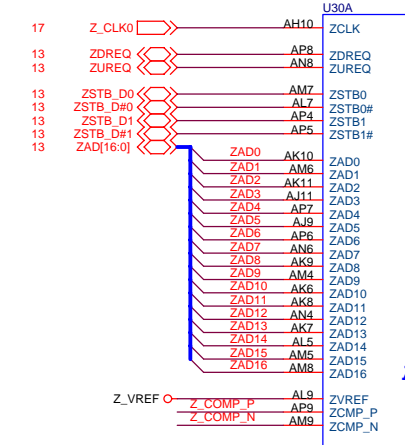
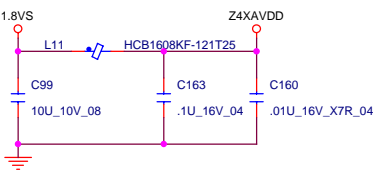
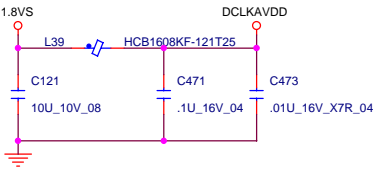
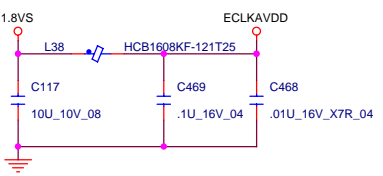
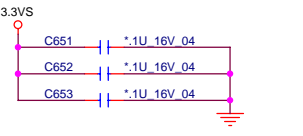
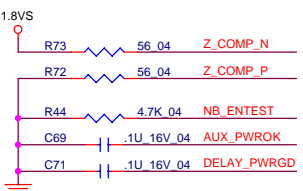
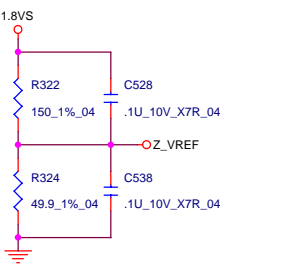
DRAM



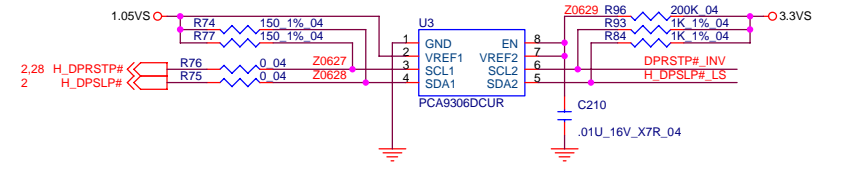
Place close to M671MX

公圖沒有

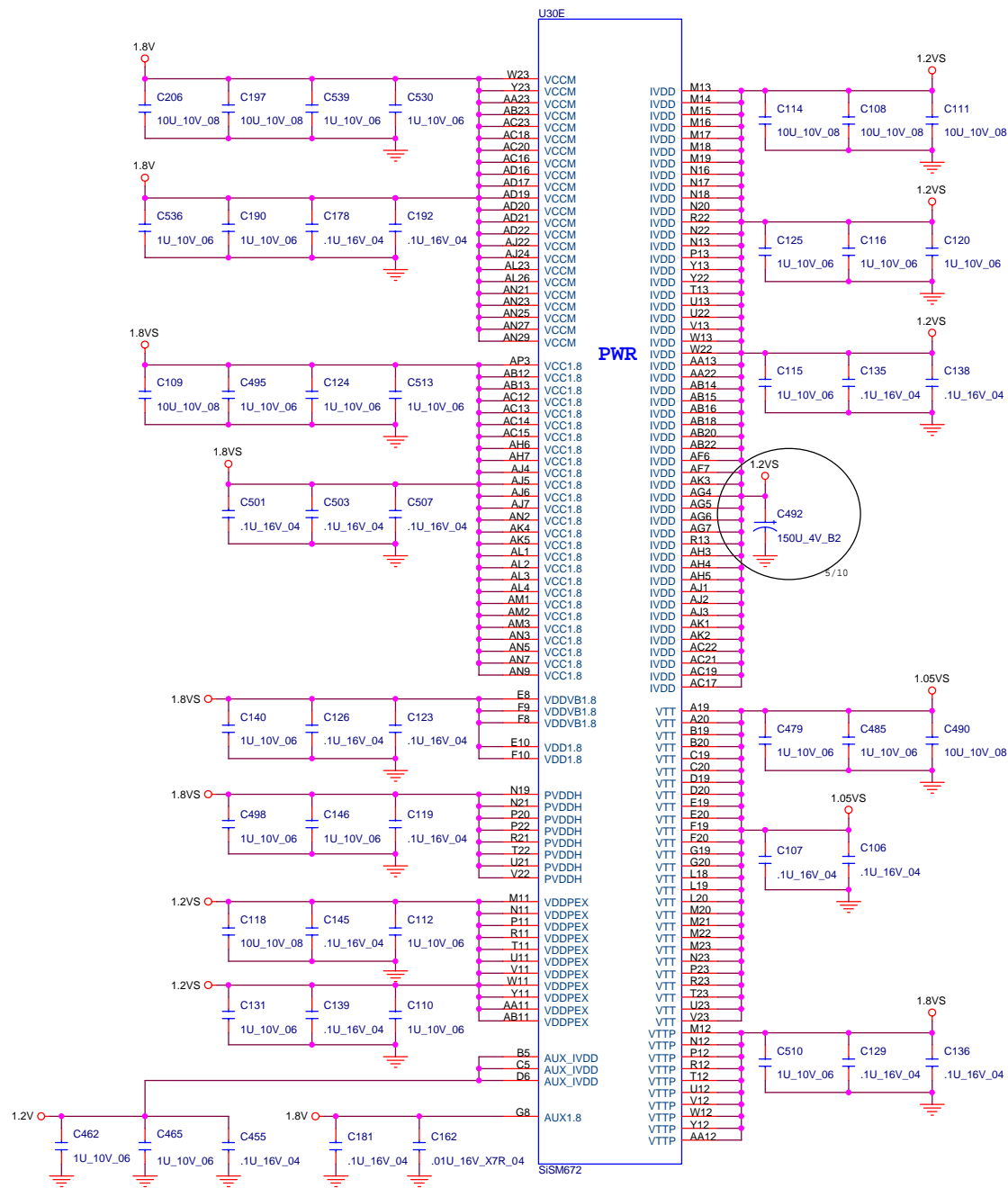




Level Shitt
1.05V ==> 3.3V

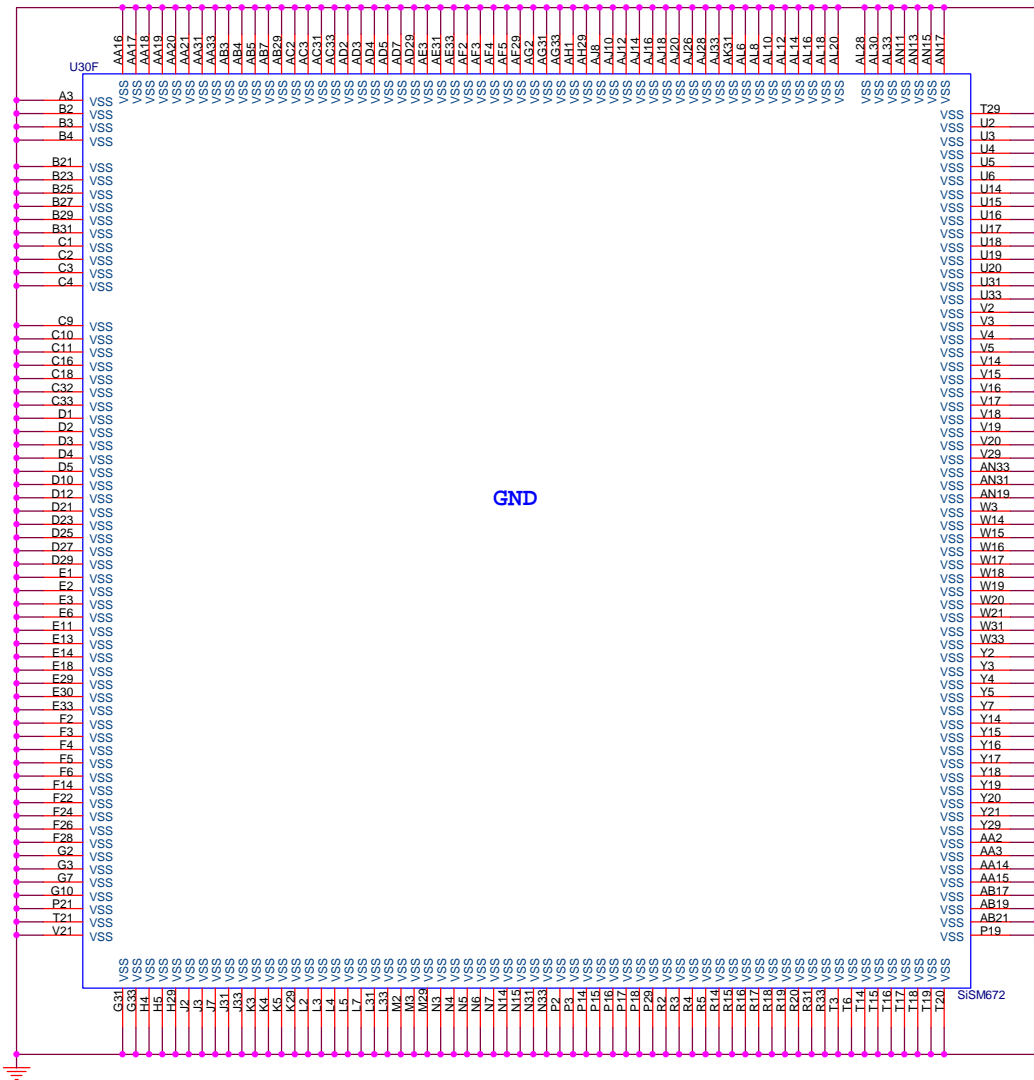


- 1.05VS 2,3,4,7,16,30
- 1.5VS 3,20,27
- 1.8VS 4,5,7,11,12,13,14,15,16,17,27
- 3.3VS 10,11,12,13,14,15,16,17,18,19,20,21,22,24,26,27,28

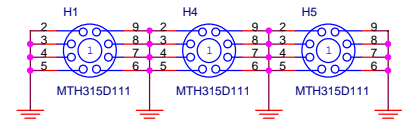
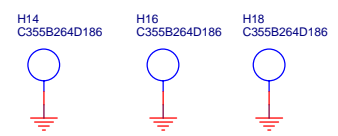


- 1.05VS 2,3,4,6,16,30
- 1.2V 27,30
- 1.2VS 4,27
- 1.8V 5,9,10,14,15,16,27,30,31
- 1.8VS 4,5,6,11,12,13,14,15,16,17,27

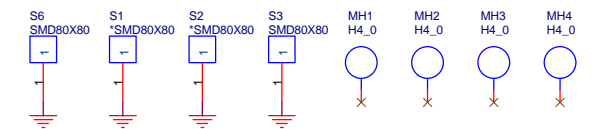
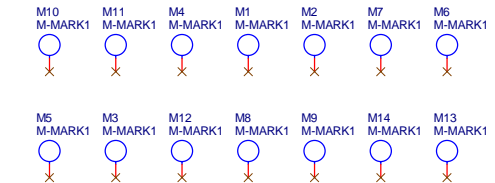
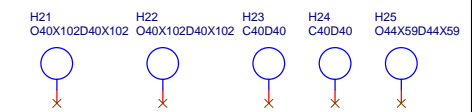
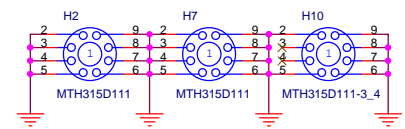
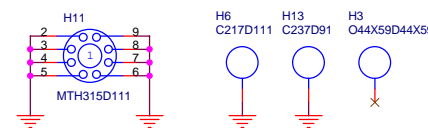
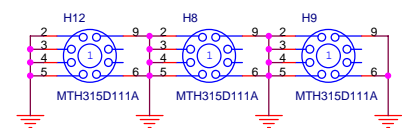
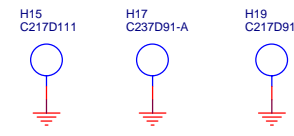
CLEVO CO. 藍天電腦			
Title [07]SiSM672_PWR 4/5			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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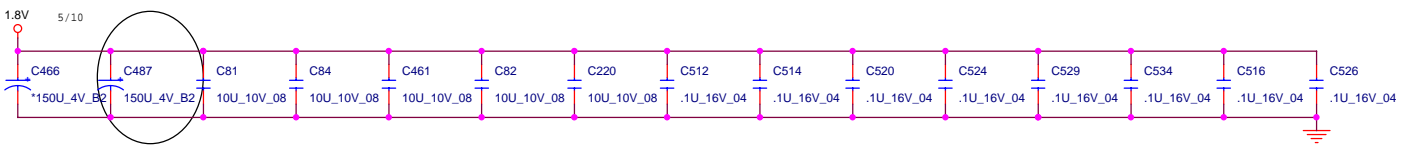
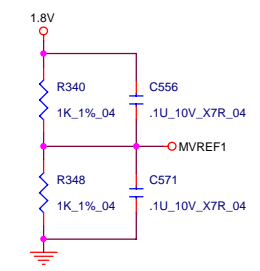
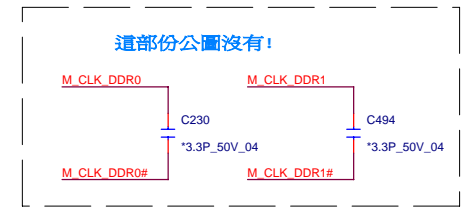
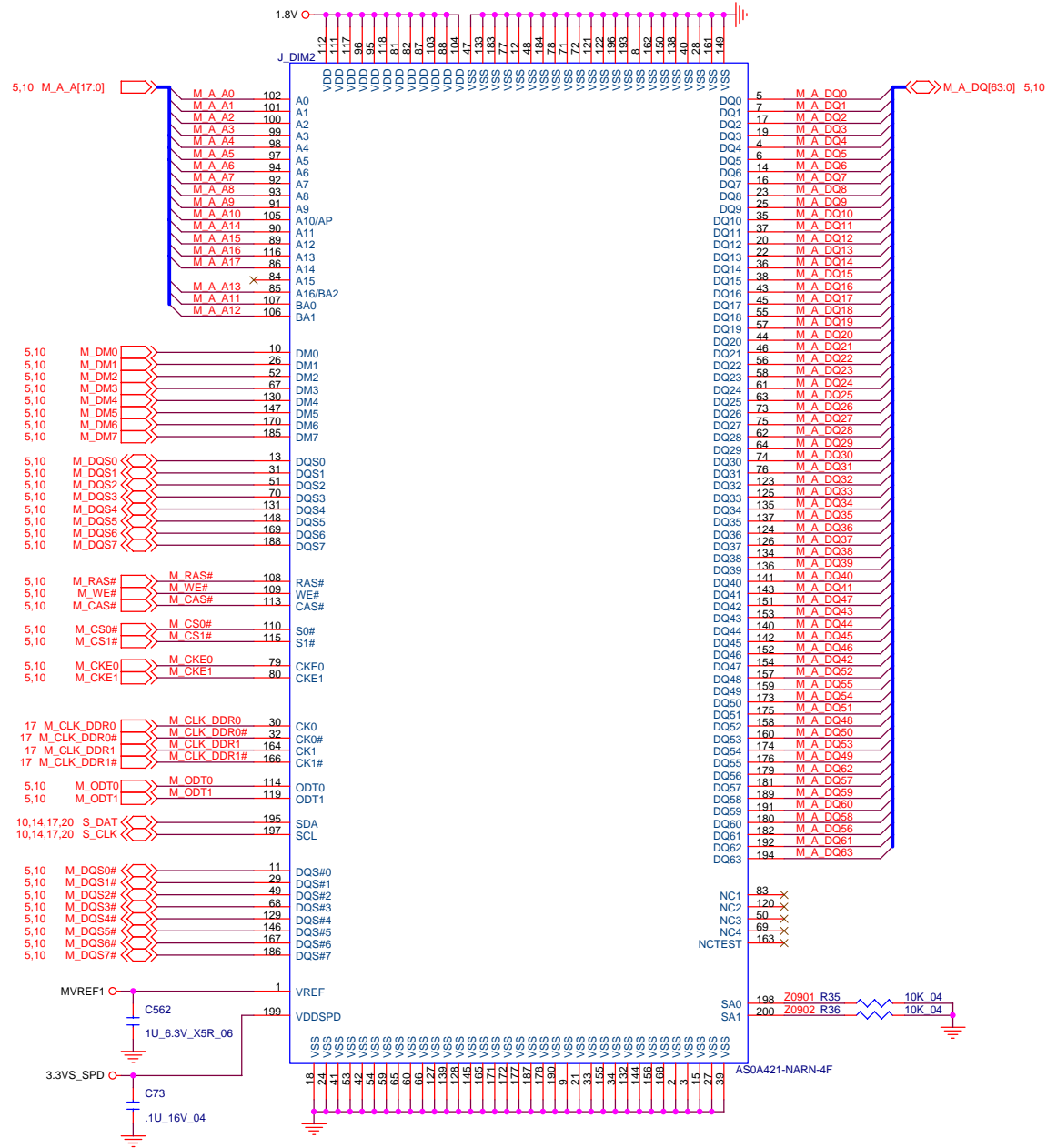
H12, H13, H14
銅柱 6-34-M52GS-020



H16, H17, H18, H19
銅柱 6-34-M52NS-020



SO-DIMM 1



1.8V 5.7,10,14,15,16,27,30,31
3.3VS_SPD 10

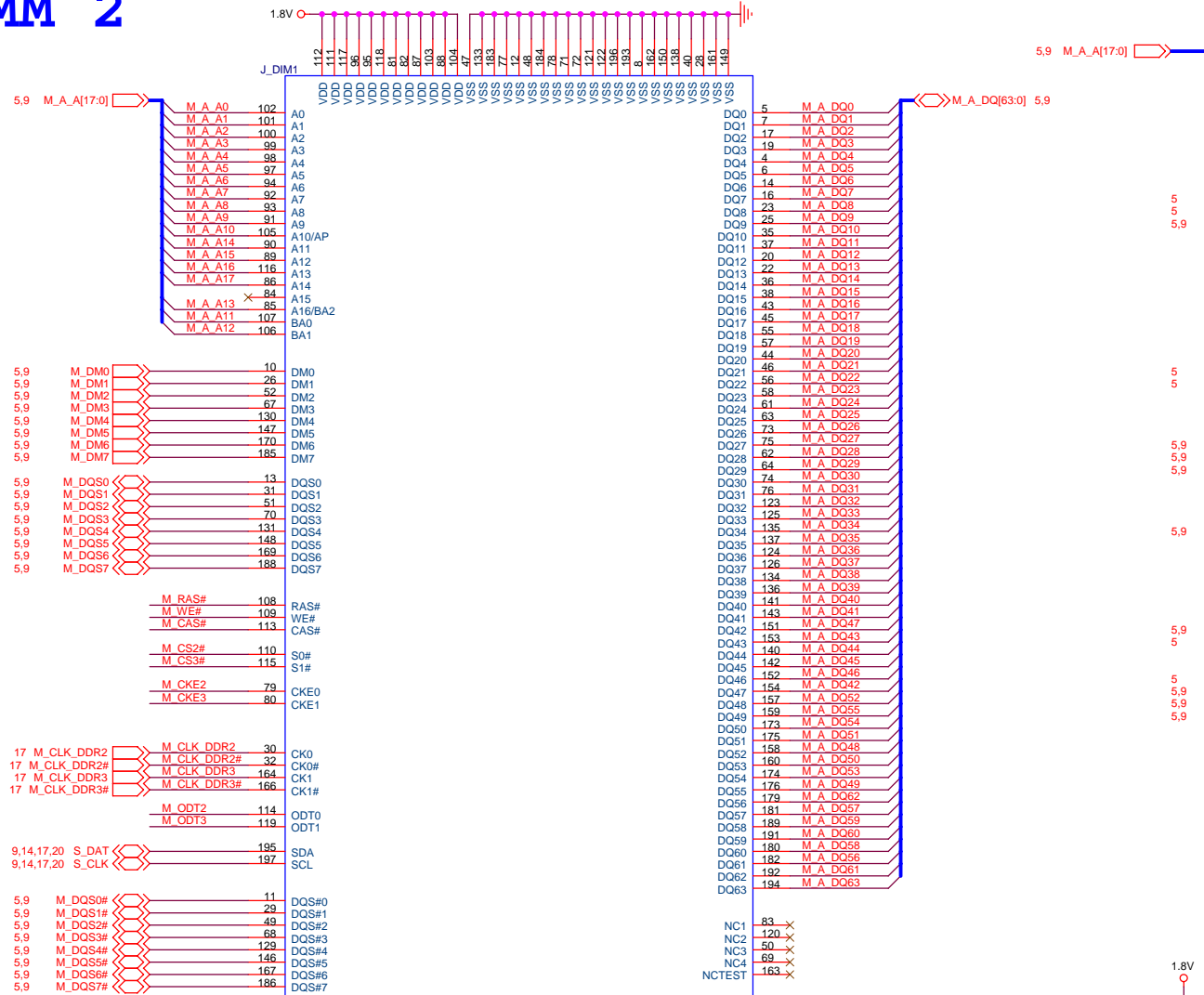
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Title [09]DDRII SO-DIMM_1

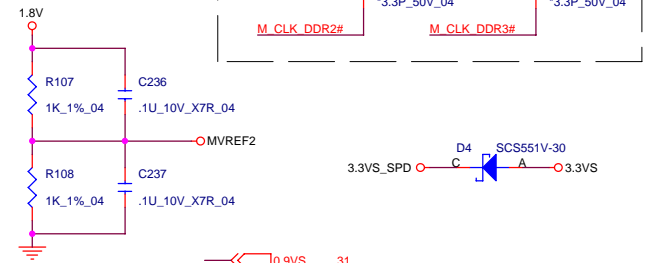
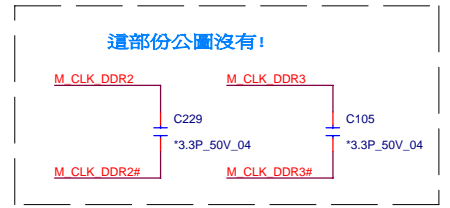
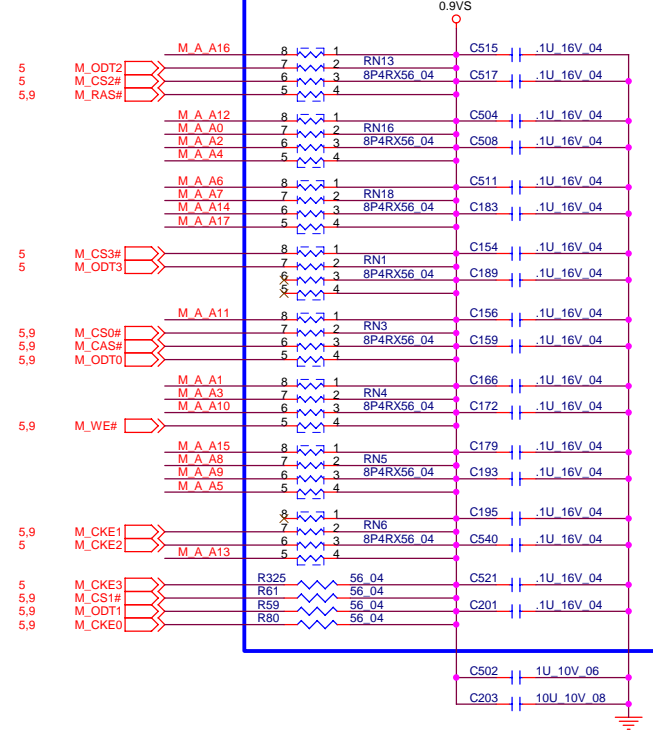
Size A3 Document Number M720SR Socket P 6-71-M7200-D02A Rev 2.0A

Date: Thursday, September 06, 2007 Sheet 9 of 37

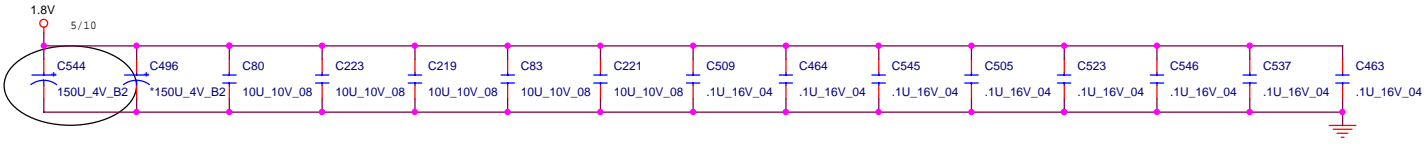
SO-DIMM 2



Layout Note:
Two pieces of 56 ohms must use one 0.1U bypass capacitor.

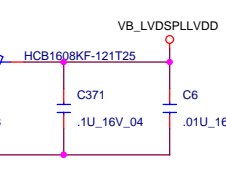
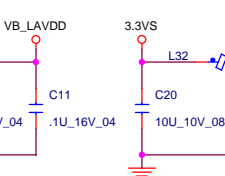
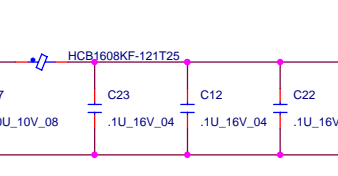
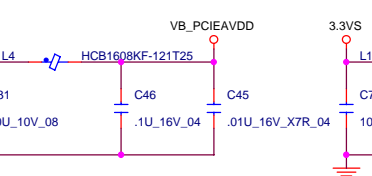
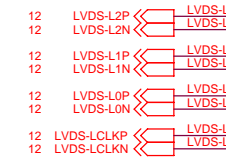
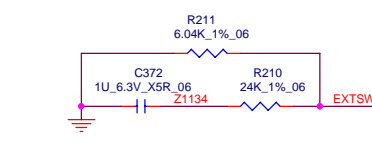
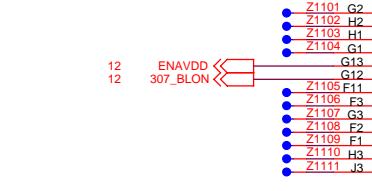
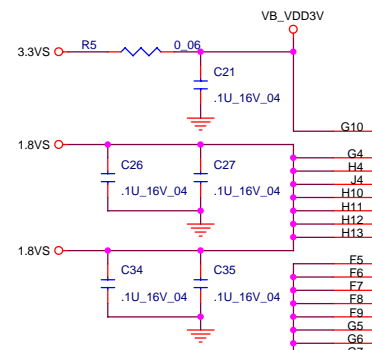
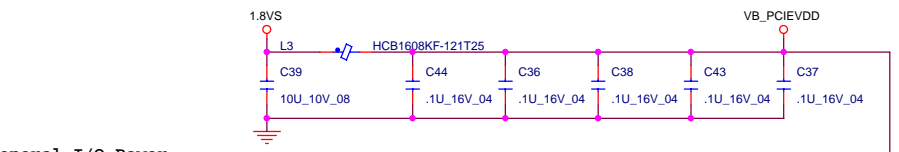


- 0.9V_S 31
- 1.8V 5,7,9,14,15,16,27,30,31
- 3.3V_S 6,11,12,13,14,15,16,17,18,19,20,21,22,24,26,27,28
- 3.3V_S_SPD 9



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Title [10]DDRII SO-DIMM_2		
Size A3	Document Number M720SR Socket P	Rev 2.0A
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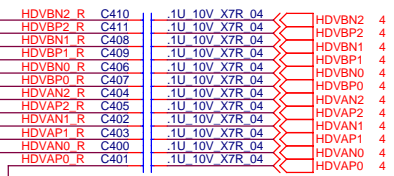
General I/O Power



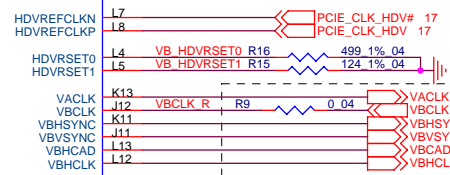
307ELV

L1 ← → L1
L1 <= 400mils
Close to 307 side

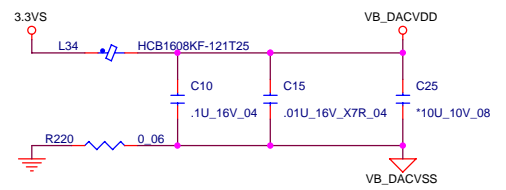
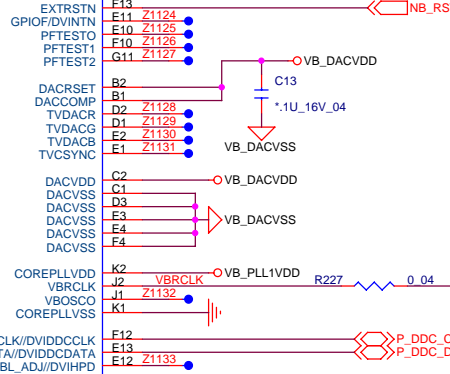
L1 <= 400mils
Close to 307 side



HDV Signals



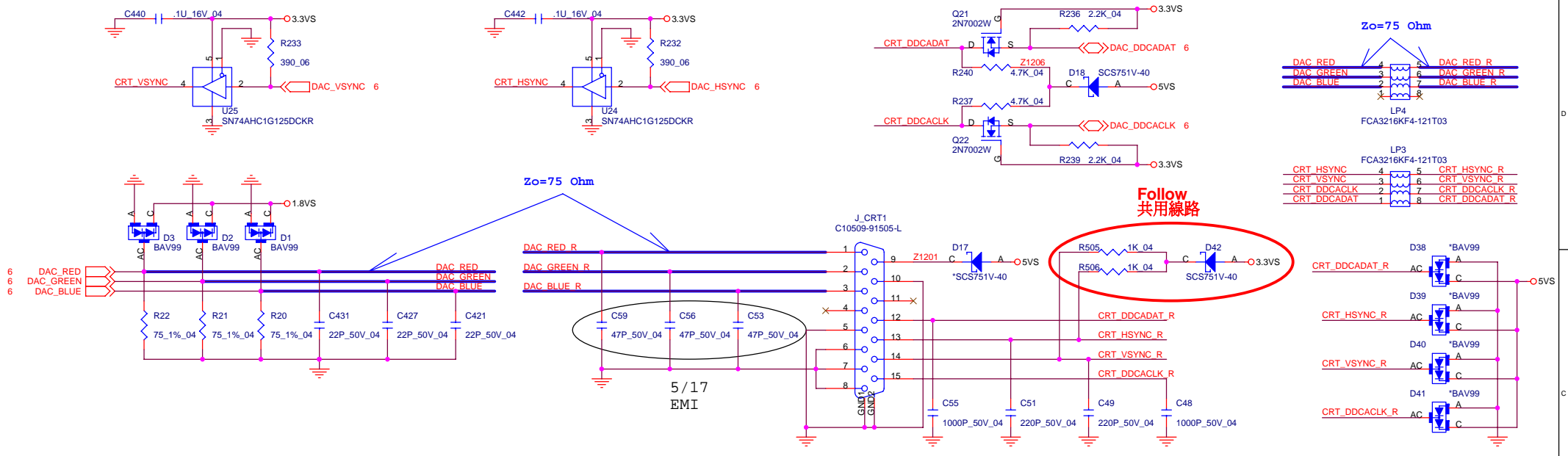
Side-Band Signals



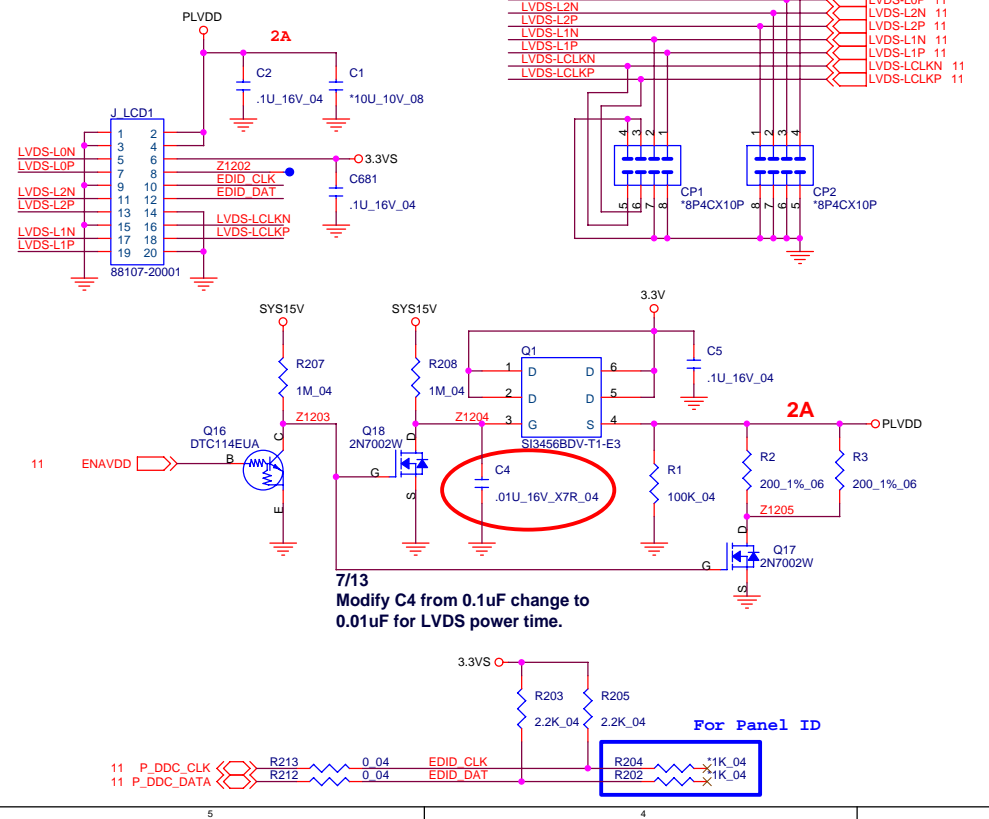
1.8VS 4,5,6,7,12,13,14,15,16,17,27
3.3VS 6,10,12,13,14,15,16,17,18,19,20,21,22,24,26,27,28

CLEVO CO. 藍天電腦			
Title [11]SIS307ELV			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	
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CRT



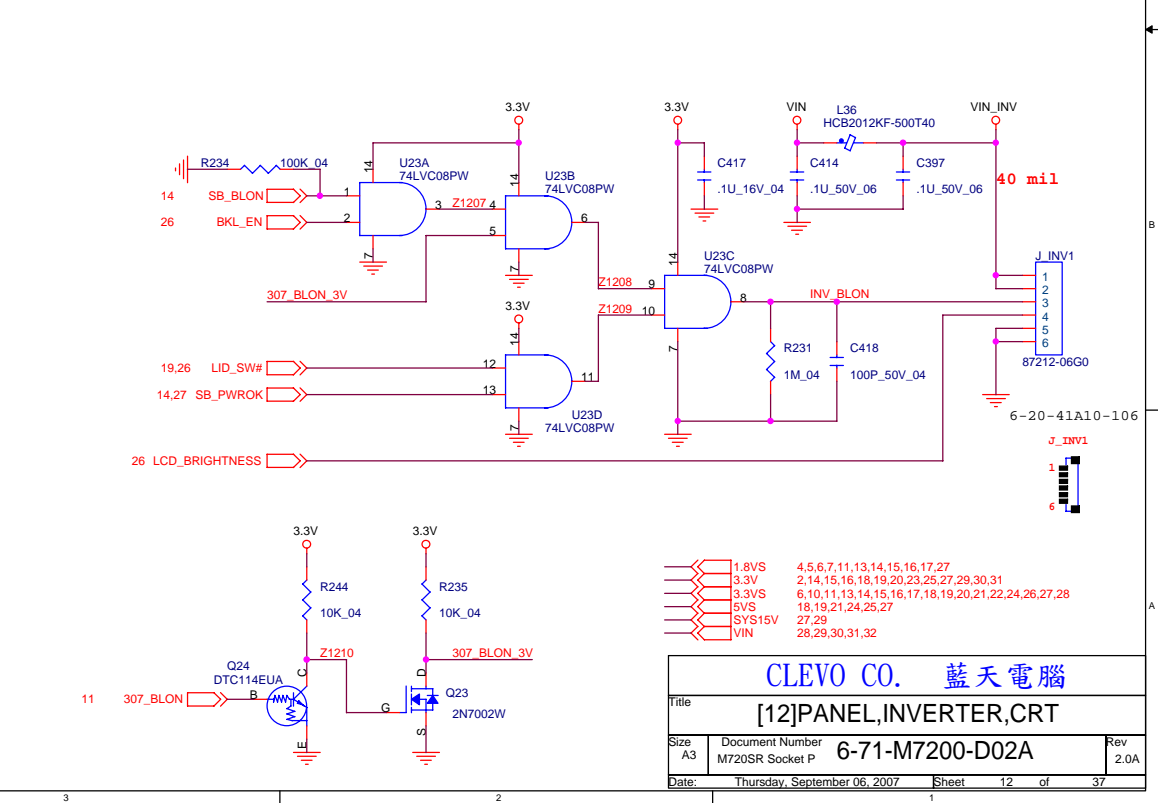
PANEL



7/13
Modify C4 from 0.1uF change to 0.01uF for LVDS power time.

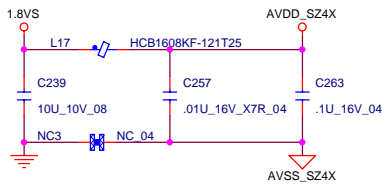
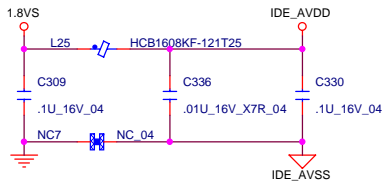
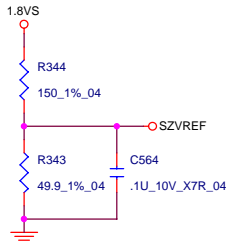
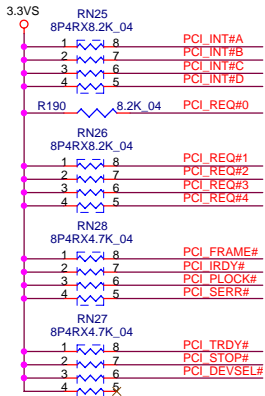
For Panel ID

INVERTER CONNECTOR

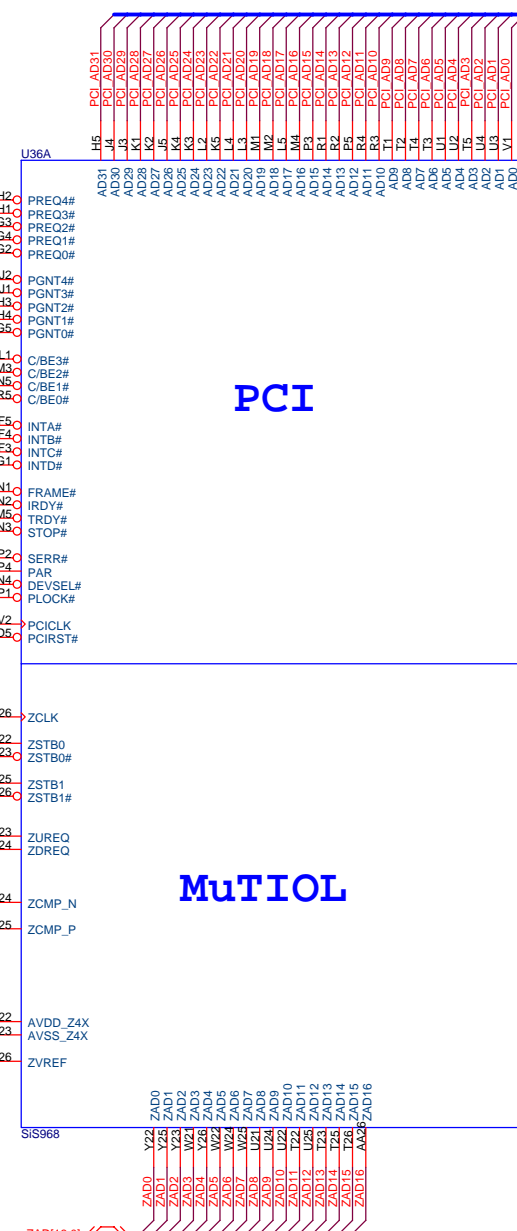
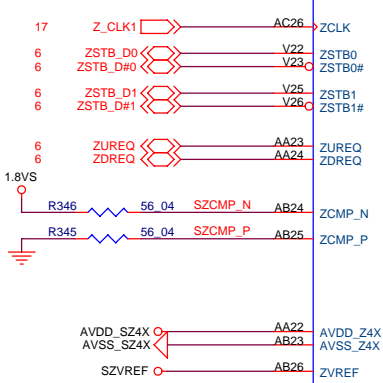
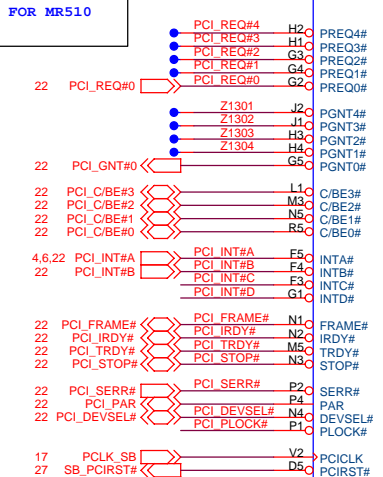


1.8VS	4,5,6,7,11,13,14,15,16,17,27
3.3V	2,14,15,16,18,19,20,23,25,27,29,30,31
3.3VS	6,10,11,13,14,15,16,17,18,19,20,21,22,24,26,27,28
5VS	18,19,21,24,25,27
SYS15V	27,29
VIN	28,29,30,31,32

CLEVO CO. 藍天電腦	
Title [12]PANEL,INVERTER,CRT	
Size A3	Document Number M720SR Socket P 6-71-M7200-D02A
Date: Thursday, September 06, 2007	Sheet 12 of 37



PCI_REQ#0
PCI_GNT#0
FOR MR510

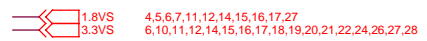


MuTIOL

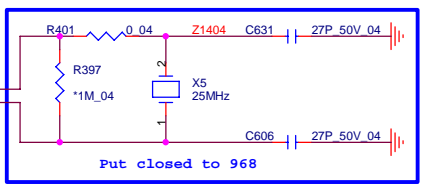
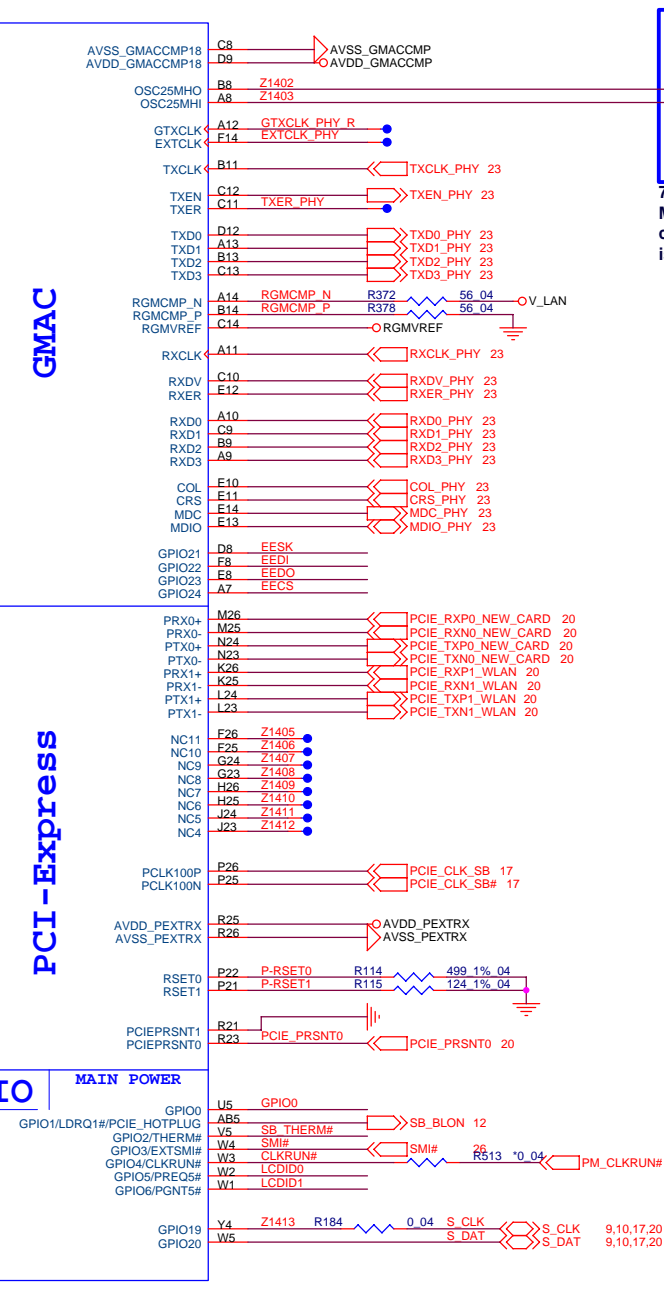
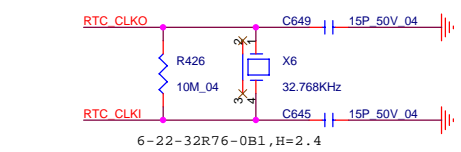
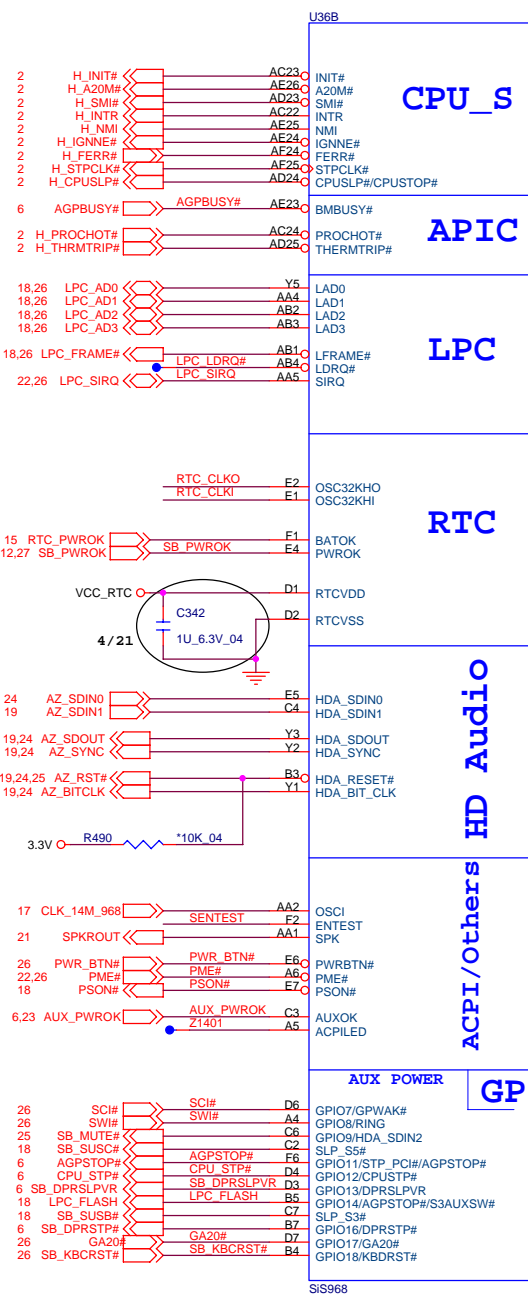
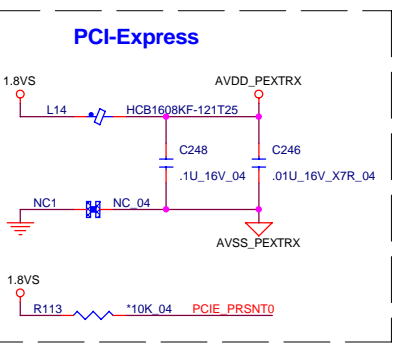
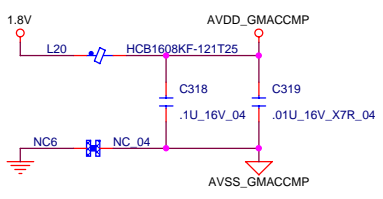
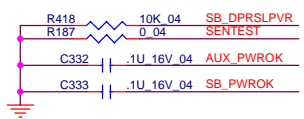
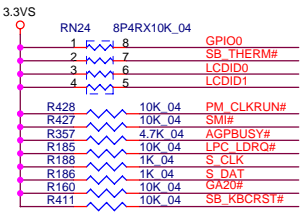
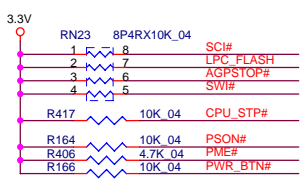
PCI

IDE

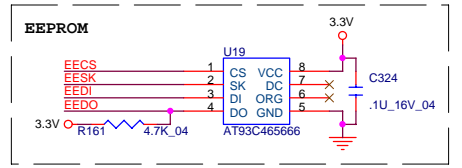
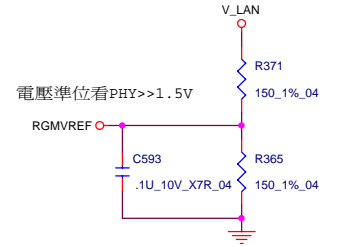
SPI



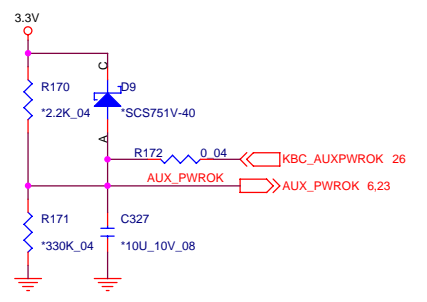
CLEVO CO. 藍天電腦			
Title [13]968_PCI_IDE_MuTIOL_SPI 1/4			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
Date: Thursday, September 06, 2007	Sheet 13	of 37	



7/13
Modify C606,C631 from 20PF
change to 27PF for OSC
issue.



PCI-Express X1
Lane 0 New Card
Lane 1 Mini Card(Wireless LAN)



- 1.8V 5,7,9,10,15,16,27,30,31
- 1.8VS 4,5,6,7,11,12,13,15,16,17,27
- 3.3VS 6,10,11,12,13,15,16,17,18,19,20,21,22,24,26,27,28
- 3.3V 2,12,15,16,18,19,20,23,25,27,29,30,31
- VCC_RTC 15
- V_LAN 16

CLEVO CO. 藍天電腦

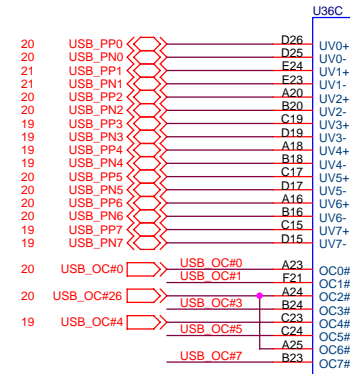
Title [14]968_PCIE_LAN_GPIO 2/4

Size A3 Document Number M720SR Socket P 6-71-M7200-D02A Rev 2.0A

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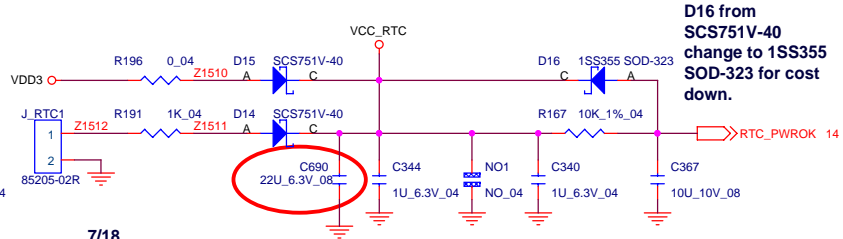
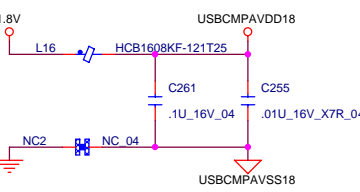
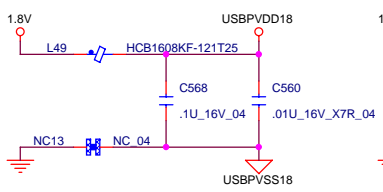
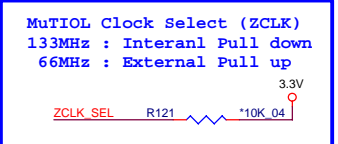
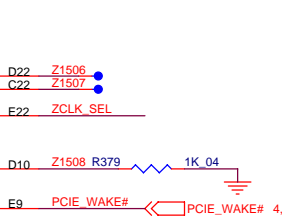
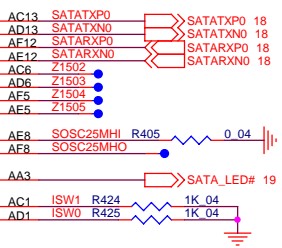
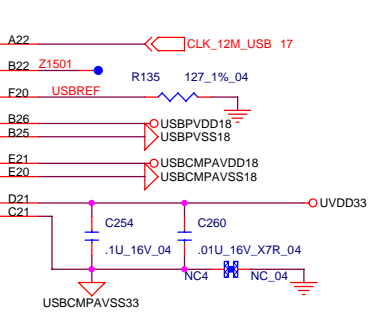
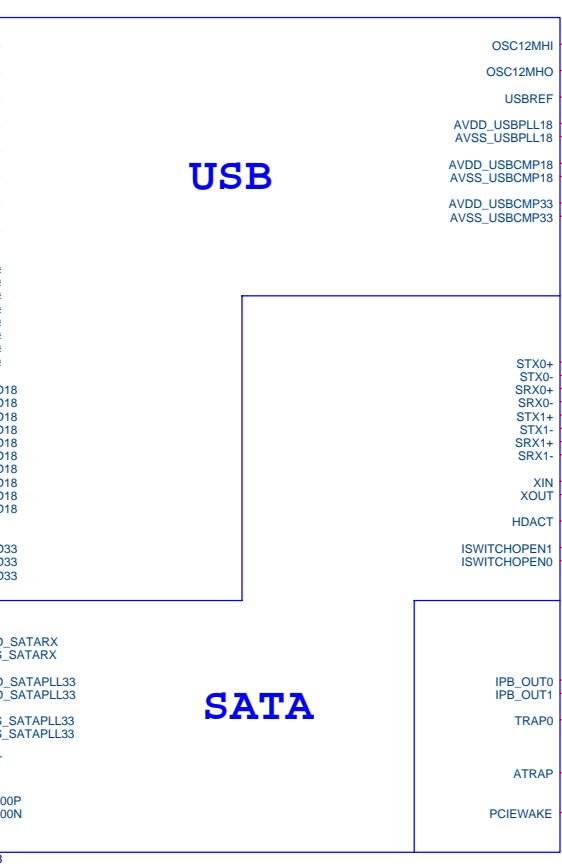
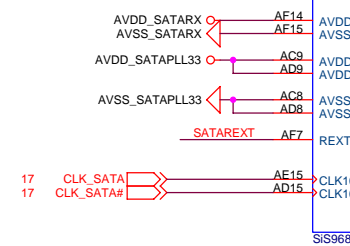
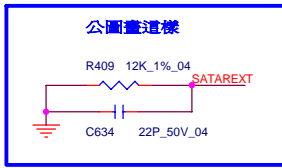
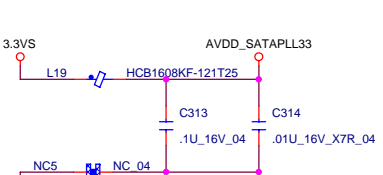
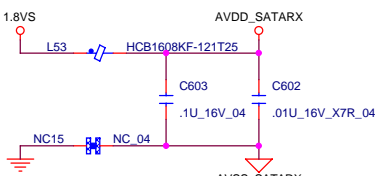
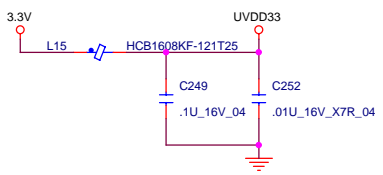
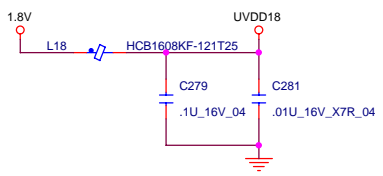
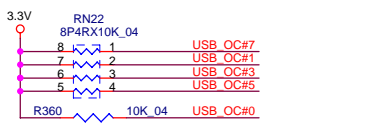
- USB0: NEW CARD
- USB1: FINGER PRINTER
- USB2: PORT0
- USB3: BLUETOOTH
- USB4: PORT2
- USB5: MINI CARD
- USB6: PORT1
- USB7: CCD

Zo=90 Ohm



USB

SATA

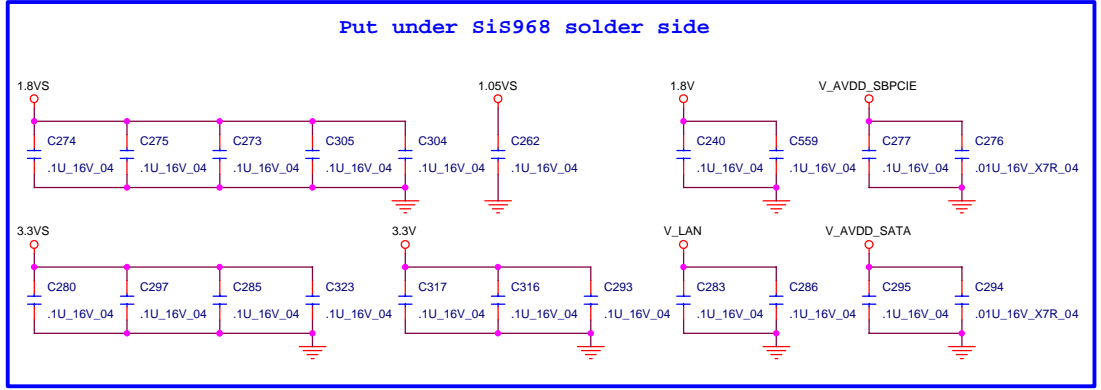
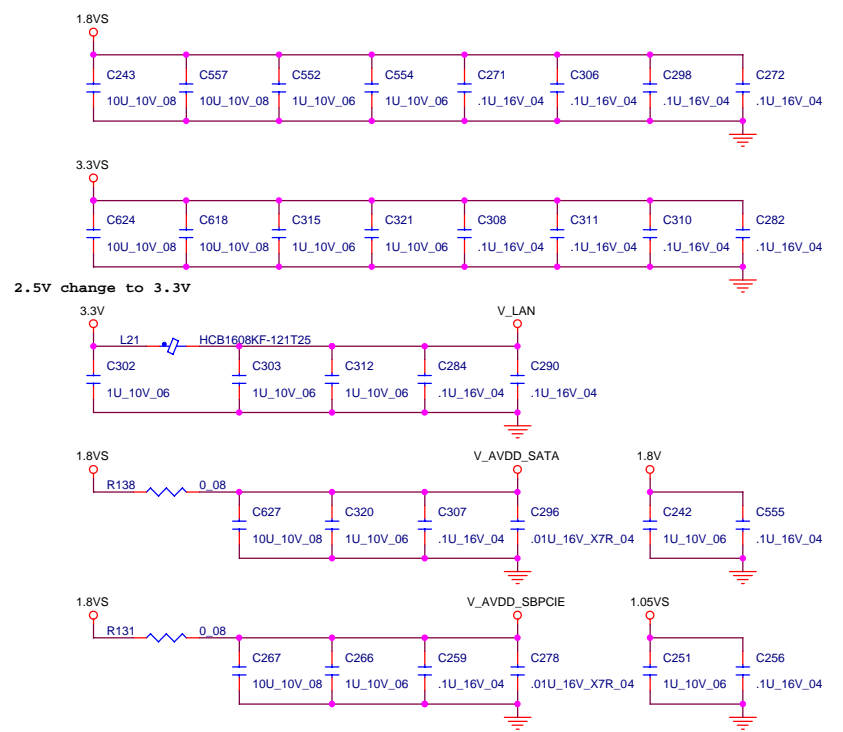
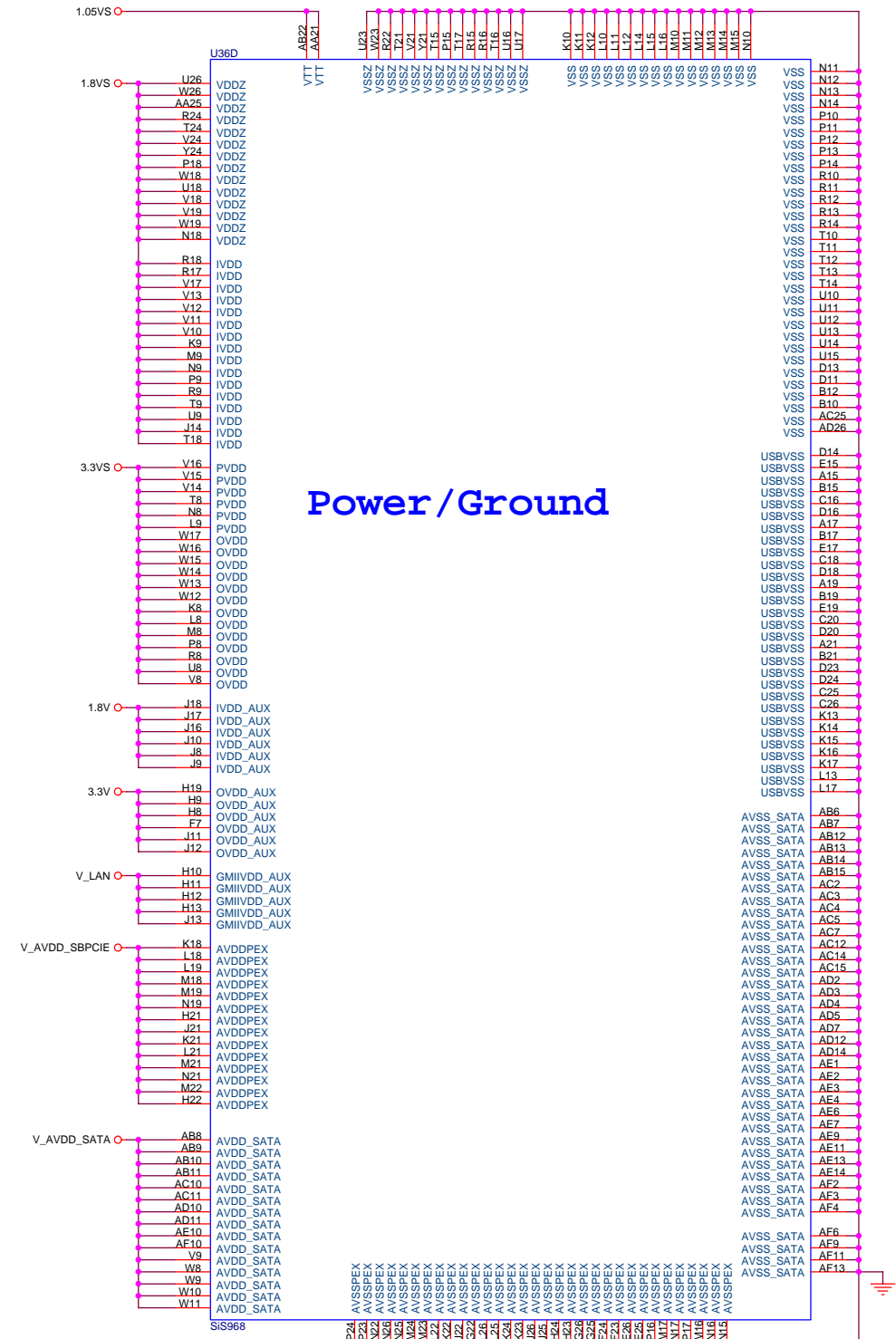


7/18
C690 from 10uF_10V_08 change to 22uF_6.3V_08 for RTC time issue.

- 1.8V 5,7,9,10,14,16,27,30,31
- 1.8VS 4,5,6,7,11,12,13,14,16,17,27
- 3.3V 2,12,14,16,18,19,20,23,25,27,29,30,31
- 3.3VS 6,10,11,12,13,14,16,17,18,19,20,21,22,24,26,27,28
- VDD3 2,21,26,27,29,31,32
- VCC_RTC 14

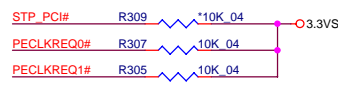
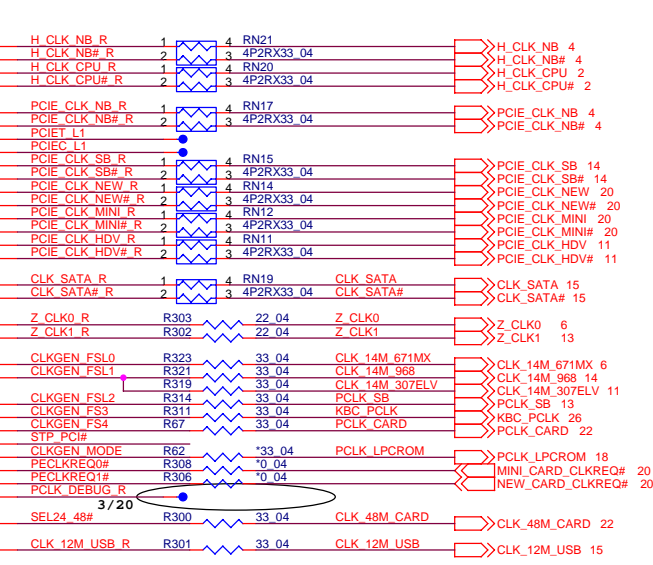
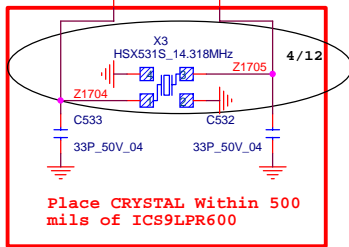
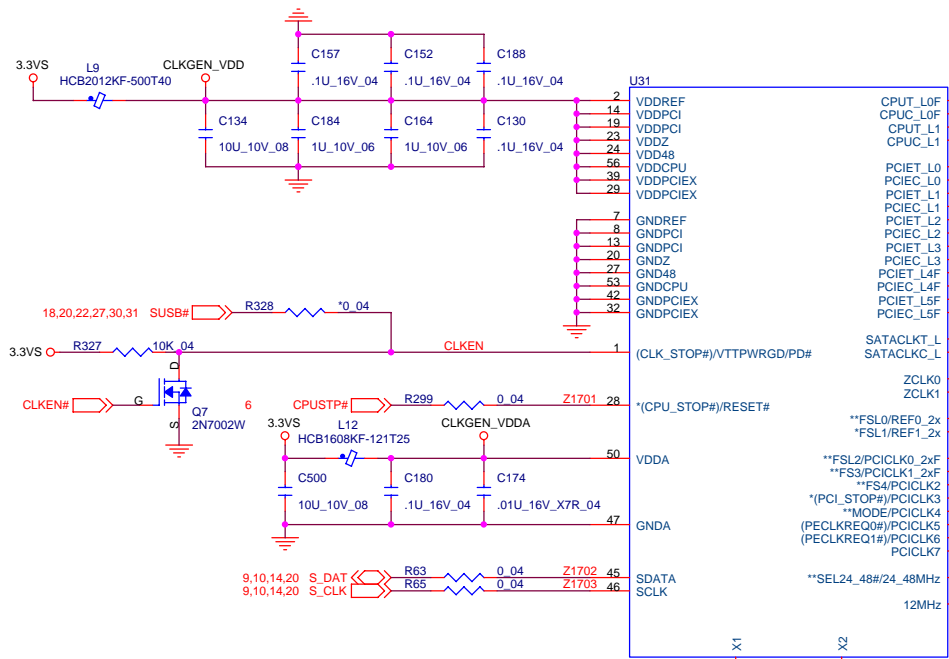
CLEVO CO. 藍天電腦		
Title [15]968_USB_SATA 3/4		
Size A3	Document Number M720SR Socket P 6-71-M7200-D02A	Rev 2.0A
Date: Thursday, September 06, 2007	Sheet 15	of 37

Power / Ground



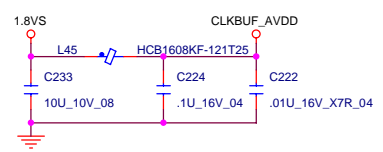
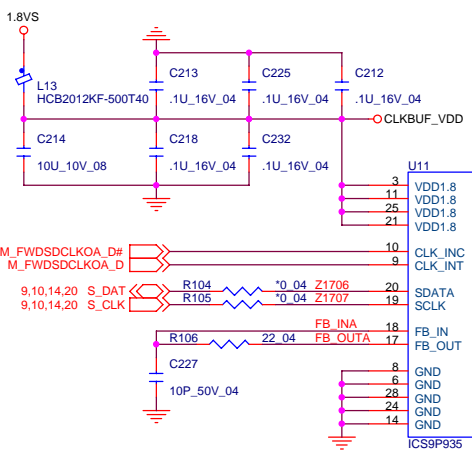
- 1.05VS 2,3,4,6,7,30
- 1.8V 5,7,9,10,14,15,27,30,31
- 1.8VS 4,5,6,7,11,12,13,14,15,17,27
- 3.3V 2,12,14,15,18,19,20,23,25,27,29,30,31
- 3.3VS 6,10,11,12,13,14,15,17,18,19,20,21,22,24,26,27,28
- V_LAN 14

CLEVO CO. 藍天電腦		
Title [16]968_PWR_GND 4/4		
Size A3	Document Number M720SR Socket P	Rev 2.0A
Date: Thursday, September 06, 2007		Sheet 16 of 37



FS4	FS3	BSEL2	BSEL1	BSEL0	Host Clock Frequency
0	1	1	0	1	100 MHz
0	1	0	0	1	133 MHz
0	1	0	1	1	166 MHz
0	1	0	1	0	200 MHz

Clock Generator Pin 15		
Status (LO)Non-STUFF	(HI)PULL1-UP	
Mode	Desktop mode	Mobile mode
Pin 1	VTTWRGD/PD#	CLK_STOP#
Pin 12	PCICLK3	PCI_STOP#
Pin 16	PCICLK5	PECLKREQ0#
Pin 17	PCICLK6	PECLKREQ1#
Pin 28	RESET#	CPU_STOP#



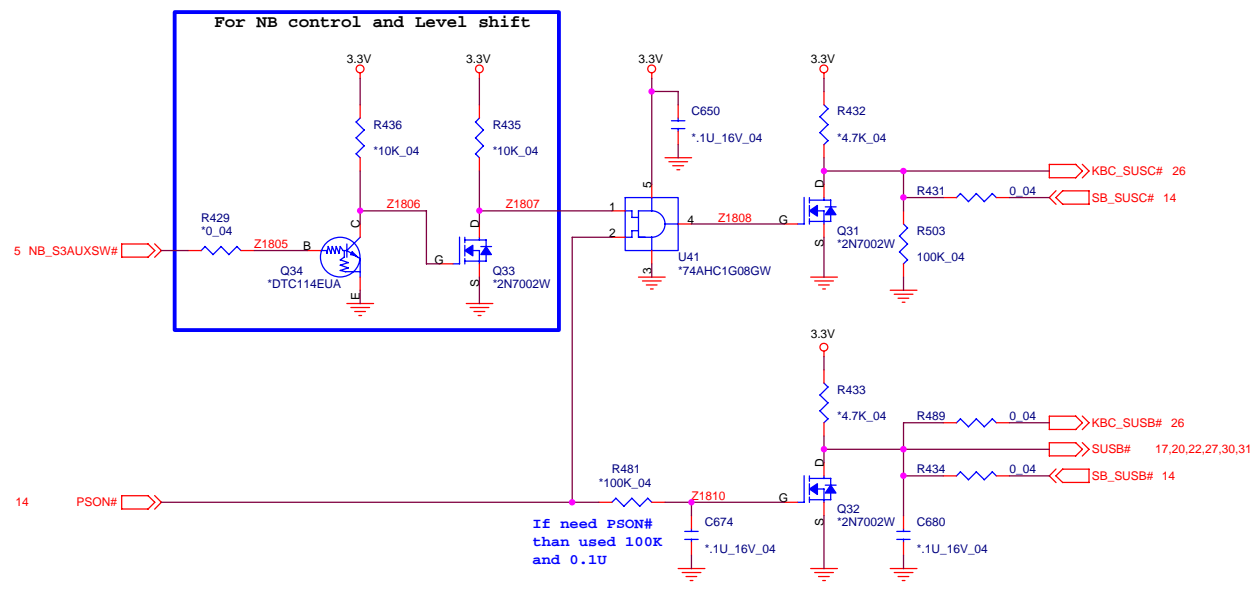
CLEVO CO. 藍天電腦

[17] CLK_GEN & CLK_BUFFER

Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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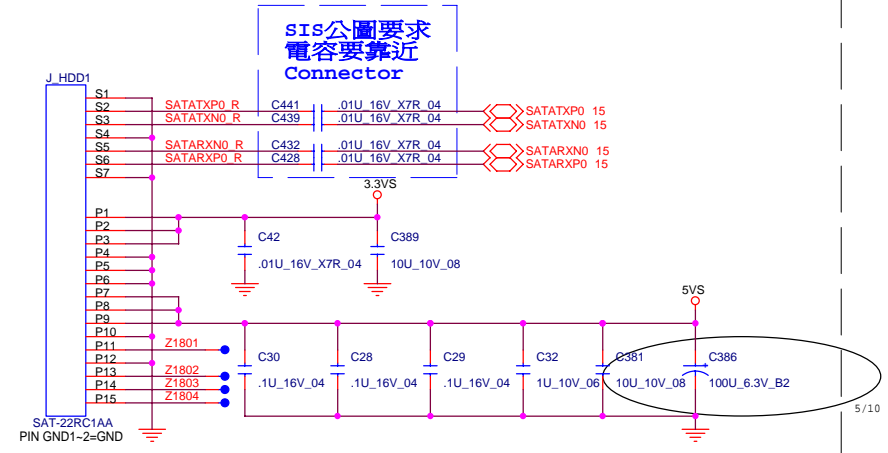
Date: Thursday, September 06, 2007 Sheet 17 of 37

POWER MANAGEMENT

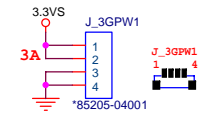


SIGNAL	S0/S1	S3	S4/S5
S3AUXSW#	1	0	1
PS0N#	0	1	1

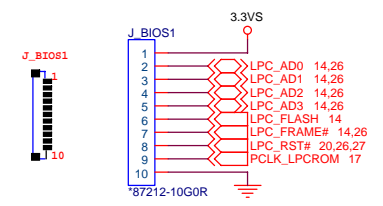
SATA HDD



3G POWER CONNECTOR



LPC BIOS CONN



- 3.3V 2,12,14,15,16,19,20,23,25,27,29,30,31
- 3.3VS 6,10,11,12,13,14,15,16,17,19,20,21,22,24,26,27,28
- 5VS 12,19,21,24,25,27

CLEVO CO. 藍天電腦

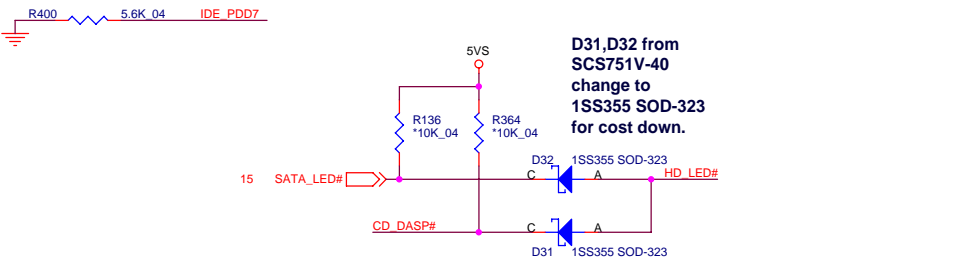
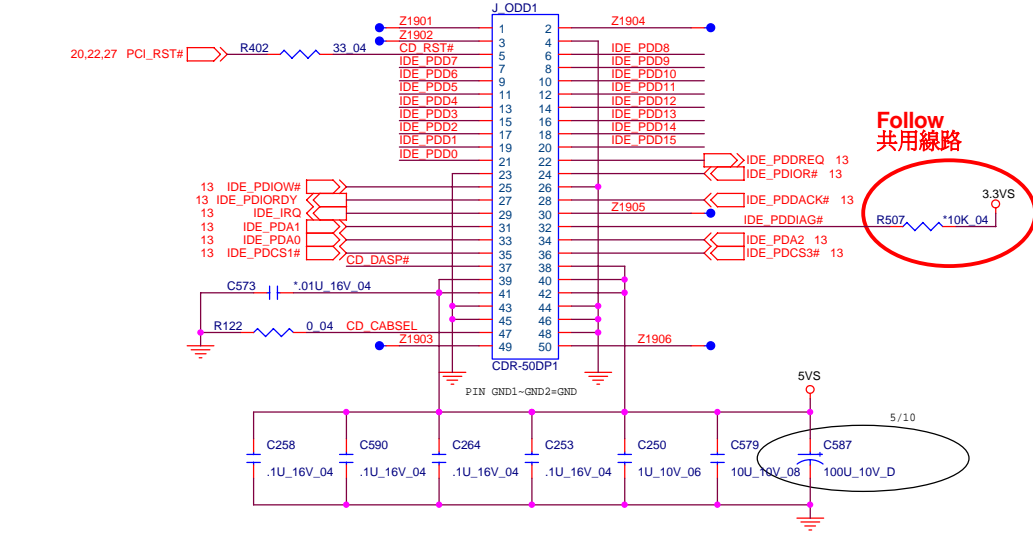
Title [18]SATA,3G POWER,PCI DEBUG

Size A3 Document Number M720SR Socket P 6-71-M7200-D02A Rev 2.0A

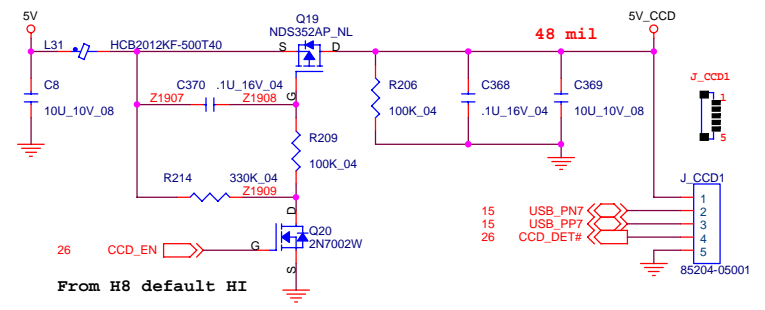
Date: Thursday, September 06, 2007 Sheet 18 of 37

ODD

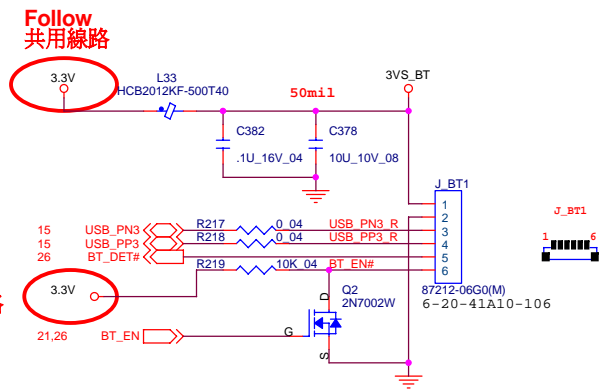
13 IDE_PDD[15:0] <<> IDE_PDD[15:0]



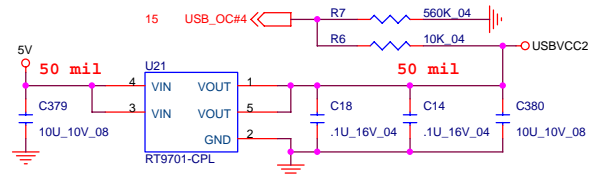
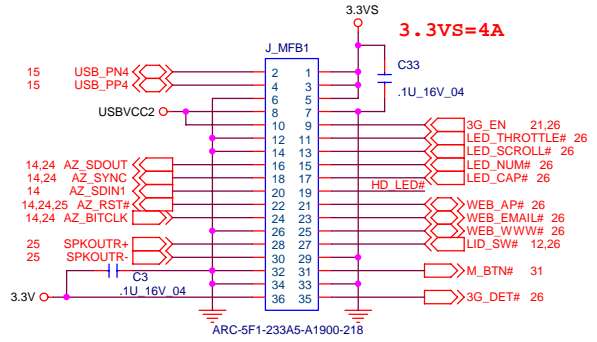
CCD



Bluetooth



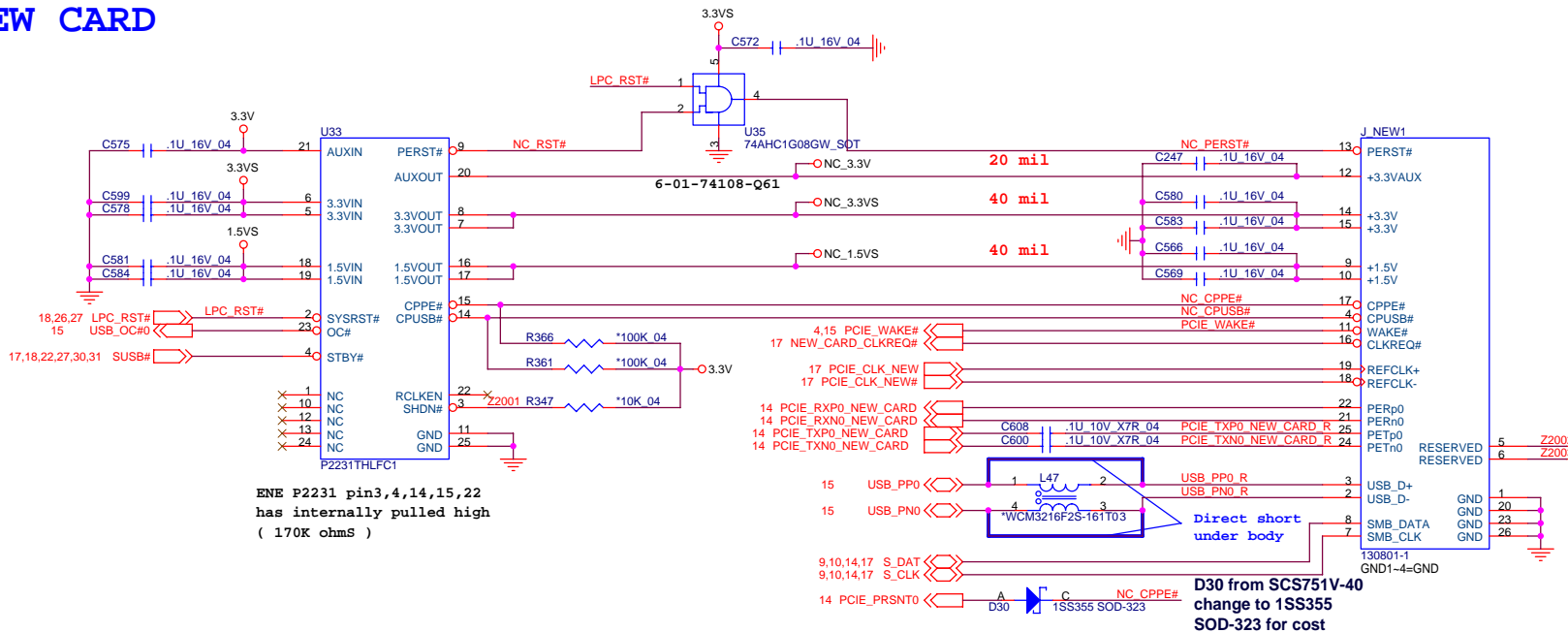
MULTI I/O CONN



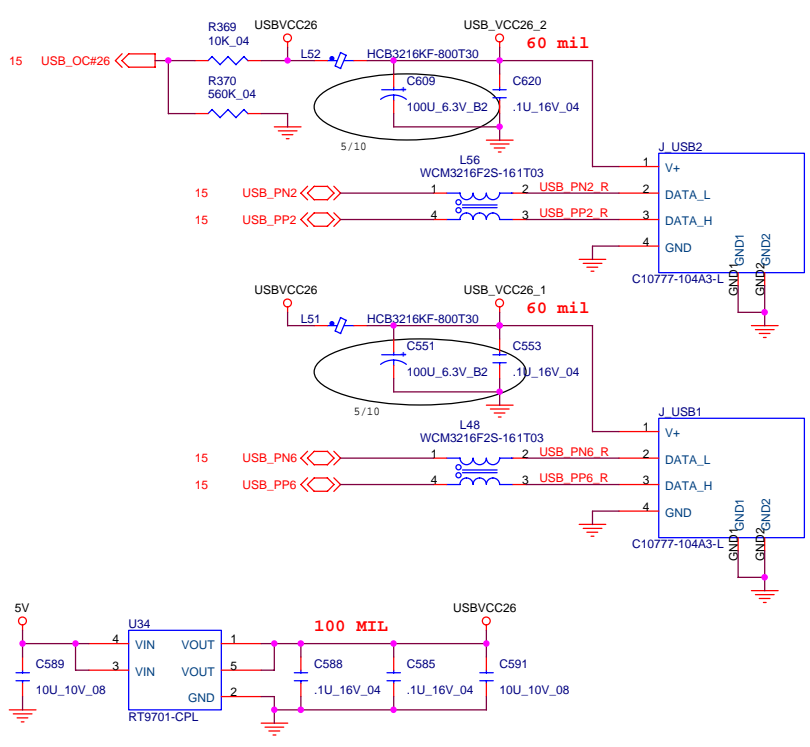
- <<> VDD3 2,15,21,26,27,29,31,32
- <<> 3.3V 2,12,14,15,16,18,20,23,25,27,29,30,31
- <<> 3.3VS 6,10,11,12,13,14,15,16,17,18,20,21,22,24,26,27,28
- <<> 5V 20,21,24,25,28,29,30,31
- <<> 5VS 12,18,21,24,25,27

CLEVO CO. 藍天電腦		
Title [19]MHLTI I/O,ODD,CCD,BT		
Size A3	Document Number M720SR Socket P	Rev 2.0A
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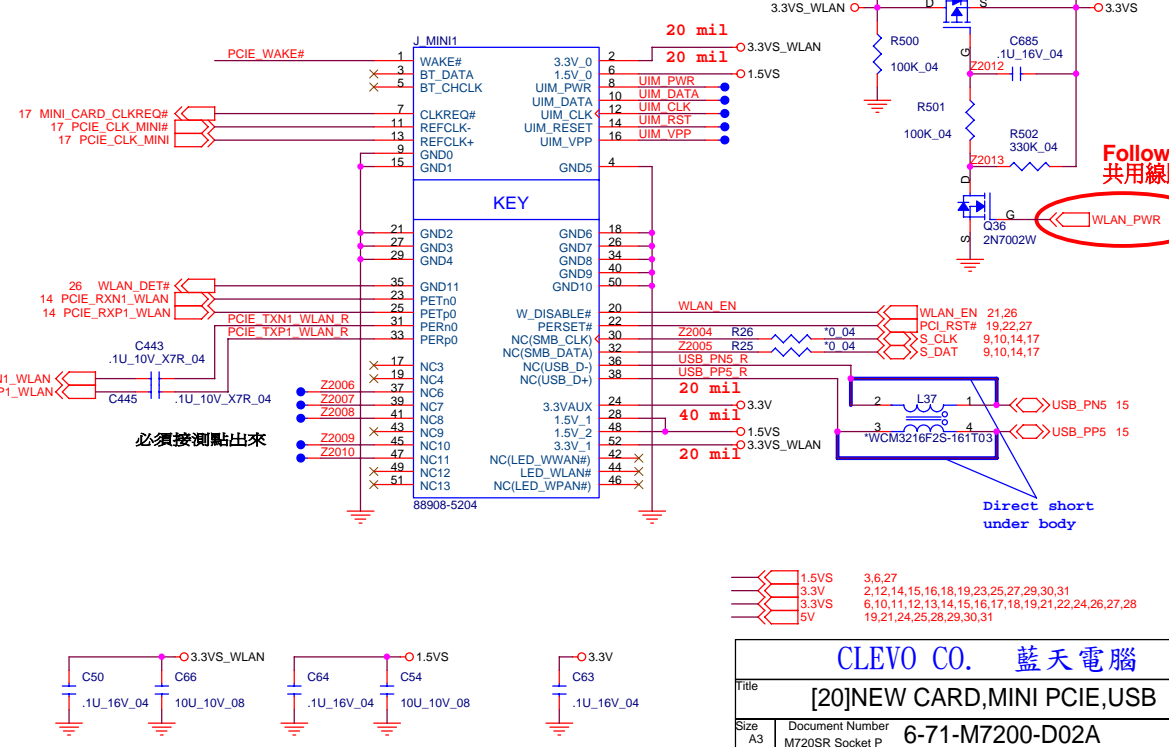
NEW CARD



USB PORT

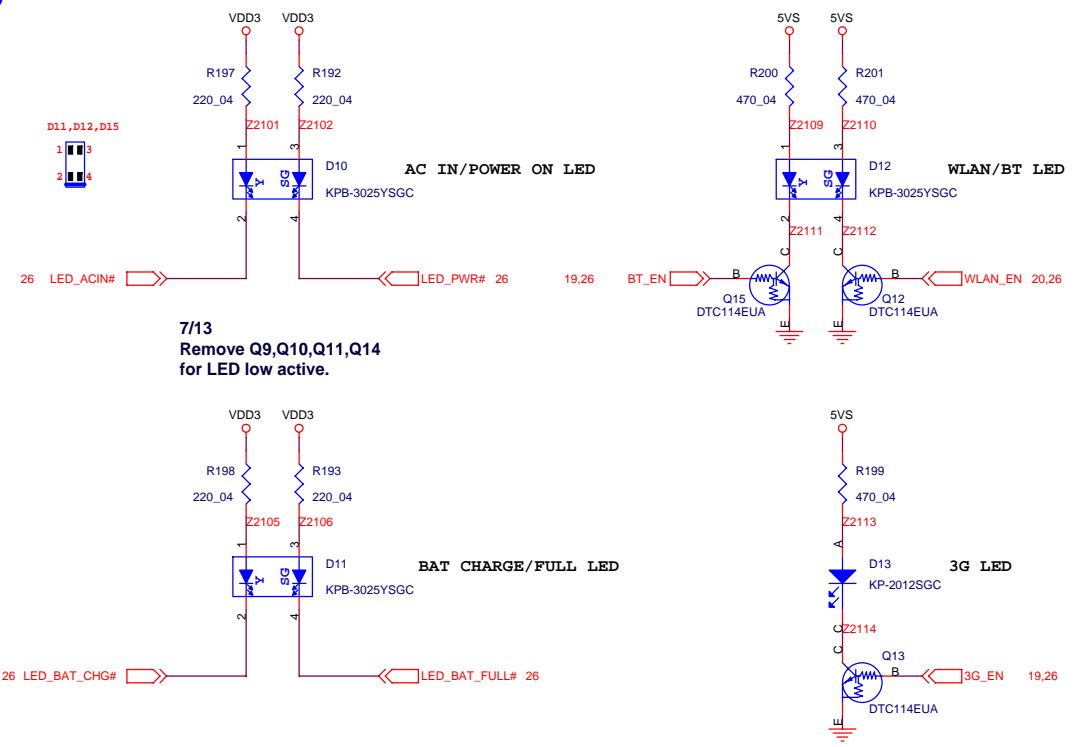


MINI CARD

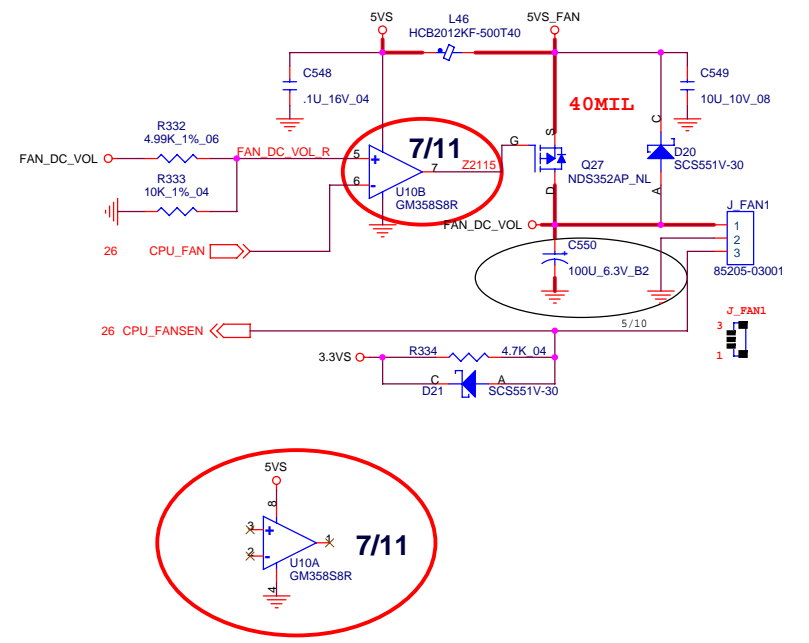


CLEVO CO. 藍天電腦			
Title [20]NEW CARD,MINI PCIE,USB			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
Date: Thursday, September 06, 2007	Sheet 20	of 37	

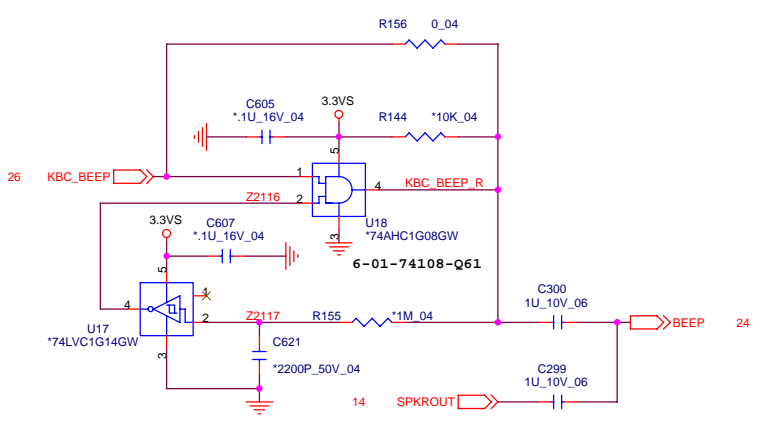
LED



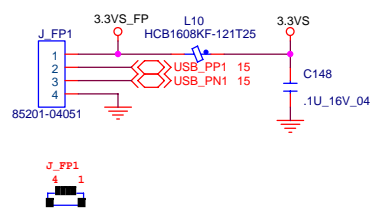
FAN CONTROL



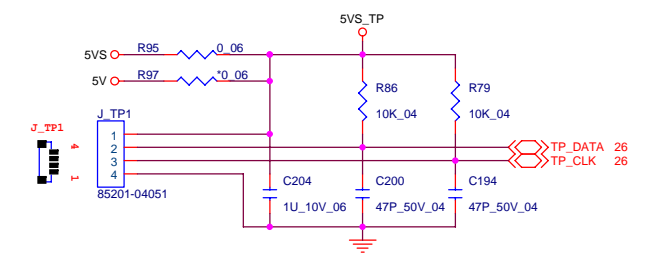
PC BEEP



FP CONN



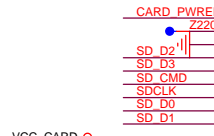
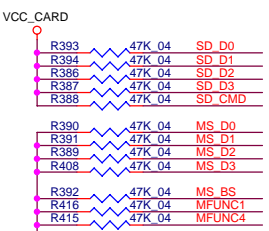
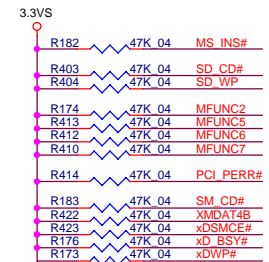
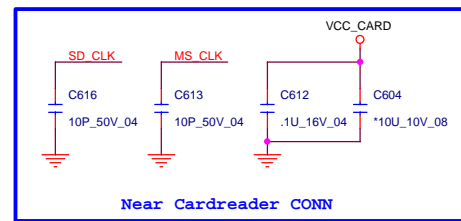
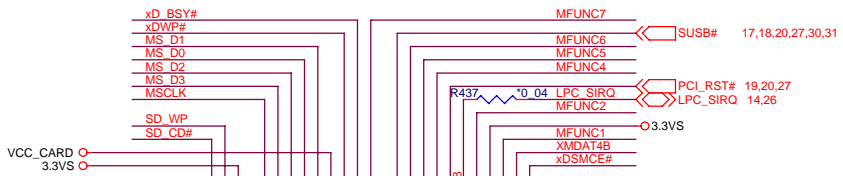
CLICK CONN



- 3.3VS 6,10,11,12,13,14,15,16,17,18,19,20,22,24,26,27,28
- 5V 19,20,24,25,28,29,30,31
- 5VS 12,18,19,24,25,27
- VDD3 2,15,26,27,29,31,32

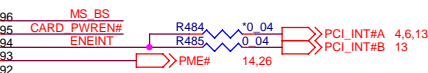
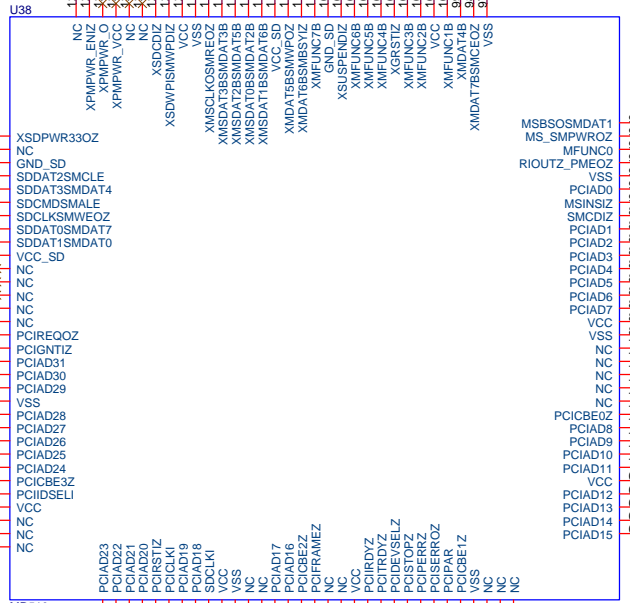
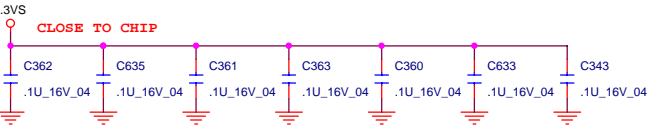
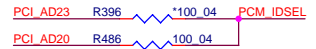
CLEVO CO. 藍天電腦			
Title [21]LED,FAN,PC BEEP,TP,FP			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
Date: Thursday, September 06, 2007	Sheet 21	of 37	

13 PCI_AD[31:0] << PCI_AD[31:0]

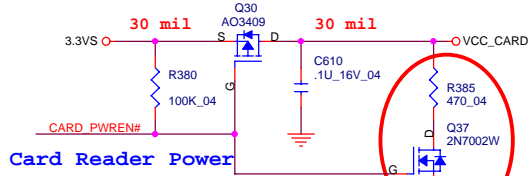
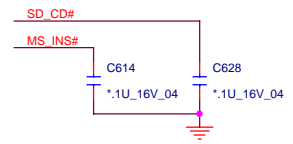
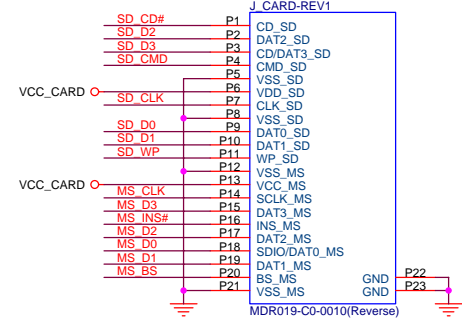


13 PCI_REQ#0 << PCI_GNT#0

13 PCI_C/BE#3 << PCM_IDSEL



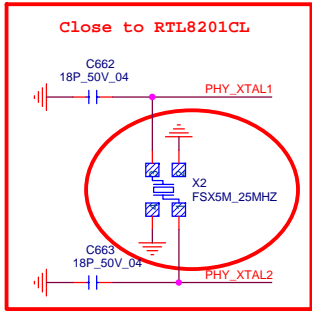
13 PCI_C/BE#0 << PCM_IDSEL



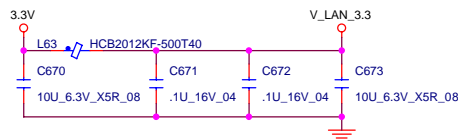
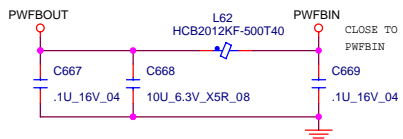
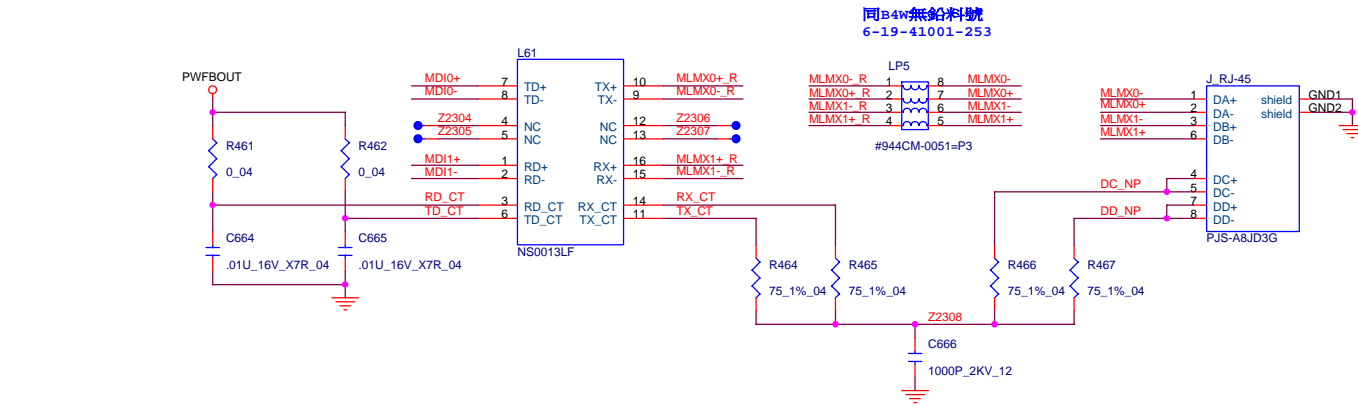
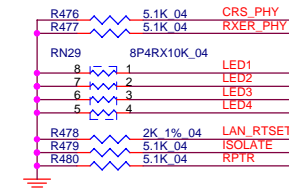
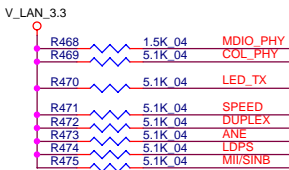
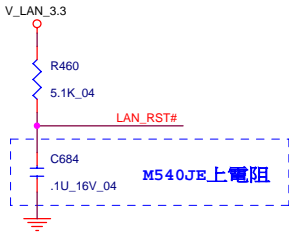
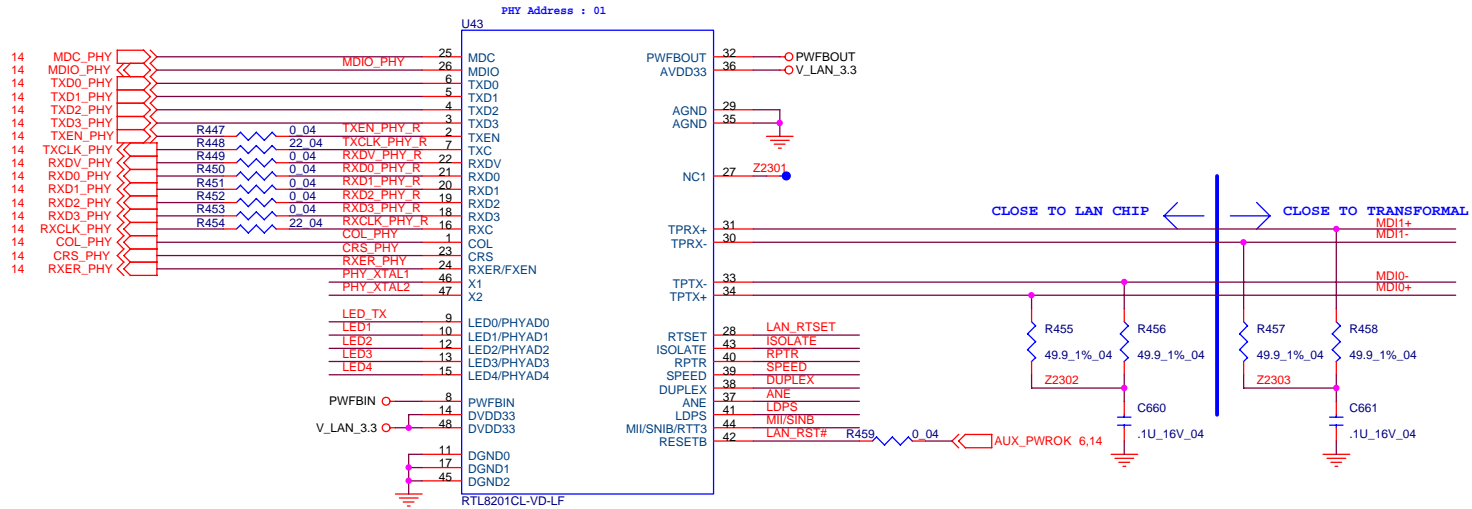
7/13
 Modify R385 from 100K change to 470 ohm and ADD Q37 for remove SD card time > 1ms issue.

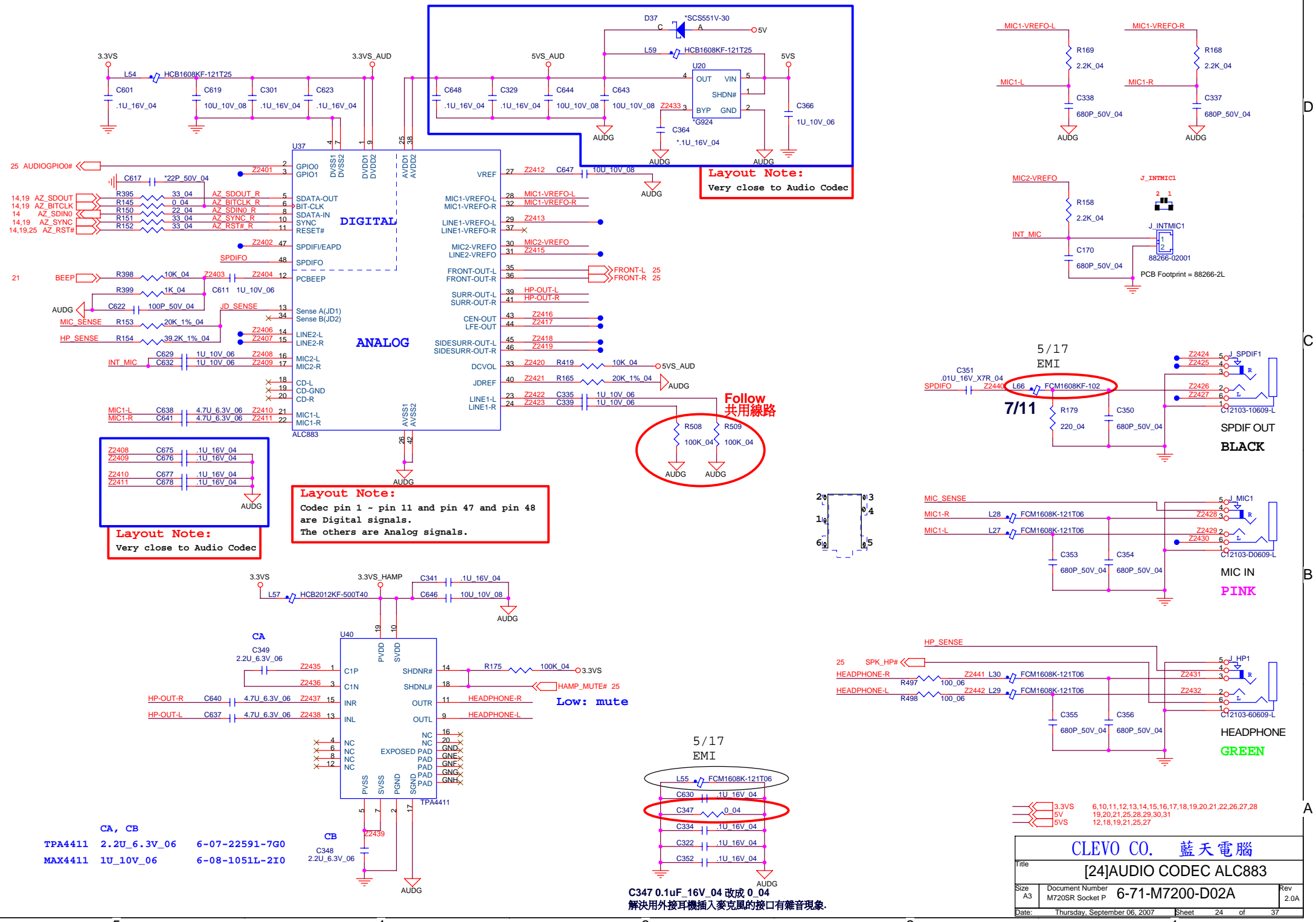
3.3VS 6,10,11,12,13,14,15,16,17,18,19,20,21,24,26,27,28

CLEVO CO. 藍天電腦			
Title [22]ENE MR510,7 IN 1			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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7/13
 Modify C662,C663 from 22PF
 change to 18PF for OSC
 issue.





Layout Note:
 Codec pin 1 ~ pin 11 and pin 47 and pin 48 are Digital signals.
 The others are Analog signals.

Layout Note:
 Very close to Audio Codec

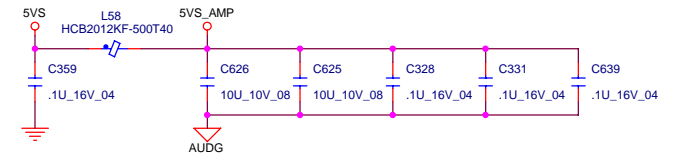
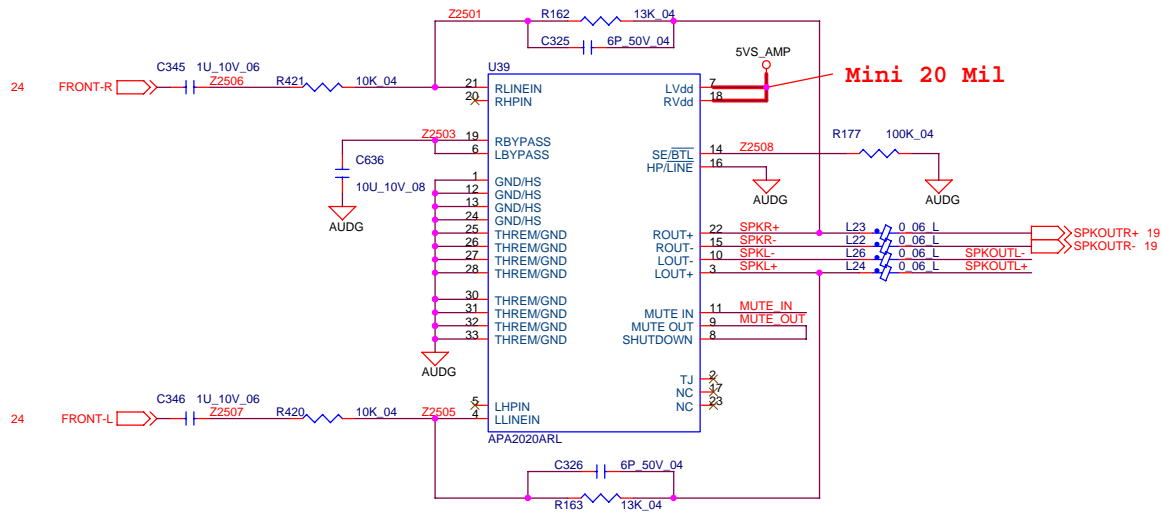
Layout Note:
 Very close to Audio Codec

Follow 共用線路

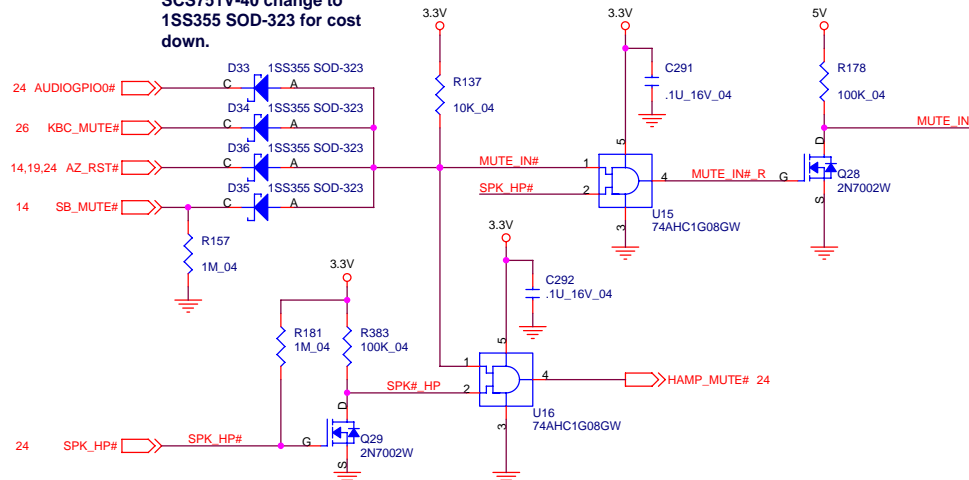
CA, CB
 TPA4411 2.2U_6.3V_06 6-07-22591-7G0
 MAX4411 1U_10V_06 6-08-1051L-2I0

5/17 EMI
 L55 FCM1608K-121T06
 C347 0.1uF_16V_04 改成 0.04
 解決用外接耳機插入麥克風的接口有雜音現象。

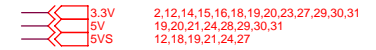
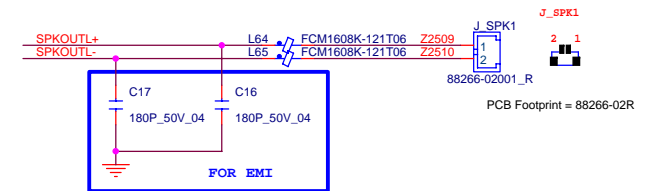
CLEVO CO. 藍天電腦			
Title [24]AUDIO CODEC ALC883			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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D33-D36 from SCS751V-40 change to 1SS355 SOD-323 for cost down.



	Default(L = Mute)
AUDIOGPIO0#	H -> H
KBC_MUTE#	L -> H
AZ_RST#	L -> H
SB_MUTE#	L -> H



CLEVO CO. 藍天電腦		
Title [25]AUDIO AMP		
Size A3	Document Number M720SR Socket P	Rev 2.0A
Date: Thursday, September 06, 2007	Sheet 25 of 37	

EC pin swap follow 共用線路

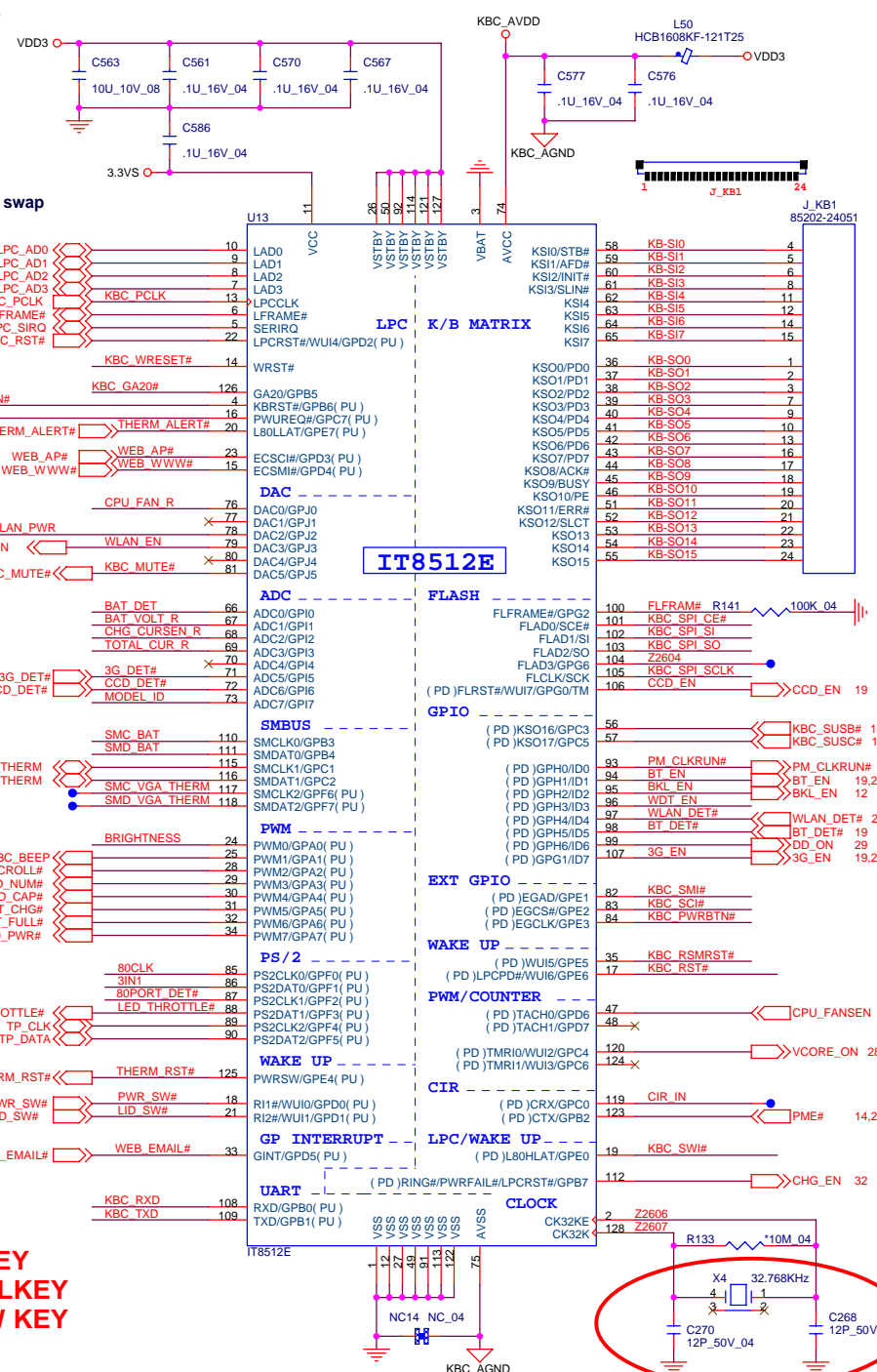
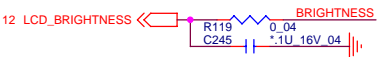
7/13 ITE8512E pin4 and pin16 swap for LED driver circuit.

Follow 共用線路

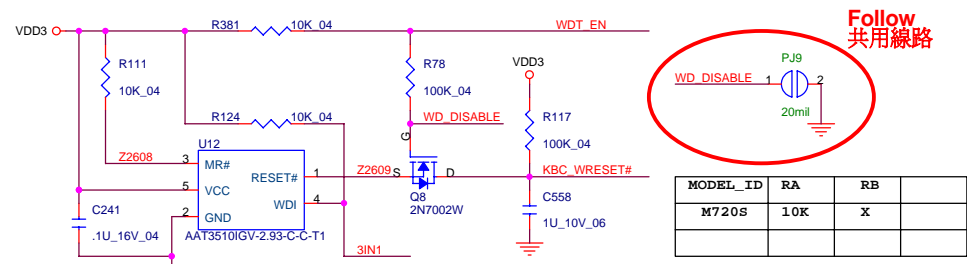
Follow 共用線路

LOW ACTIVE

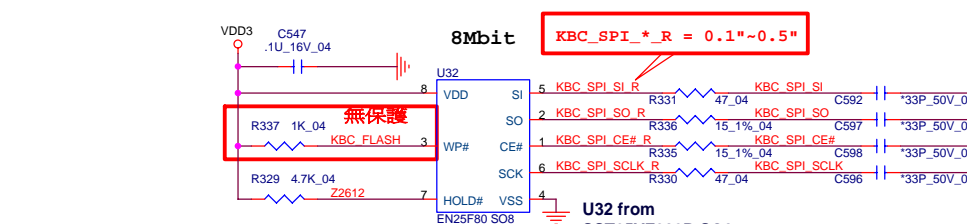
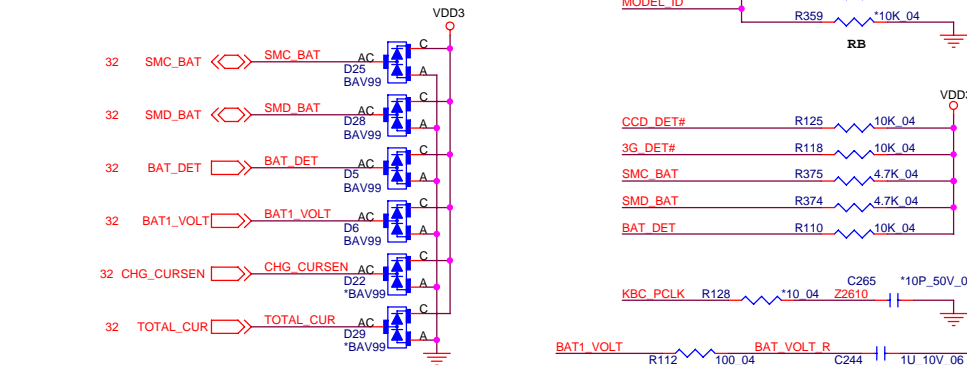
**WEB0---->AP KEY
WEB1---->EMAILKEY
WEB2---->WWW KEY**



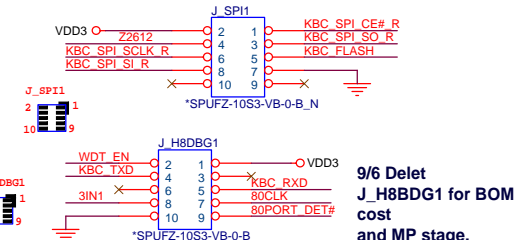
7/13 Modify C268,C270 from 15PF change to 12PF for OSC issue.



MODEL_ID	RA	RB
M720S	10K	X



J_SPI1 software not use and for PVT BOM cost.



J_SPI1 PIN 參考 LV220 JSPIDBG

VDD3	2,15,21,27,29,31,32
3.3V	2,12,14,15,16,18,19,20,23,25,27,29,30,31
3.3VS	6,10,11,12,13,14,15,16,17,18,19,20,21,22,24,27,28

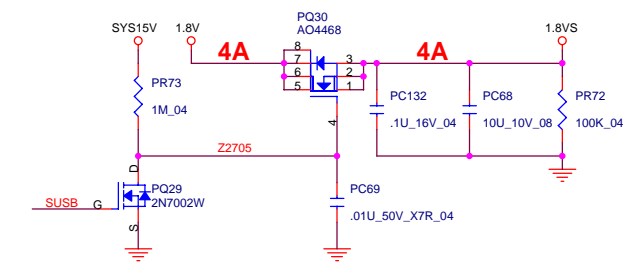
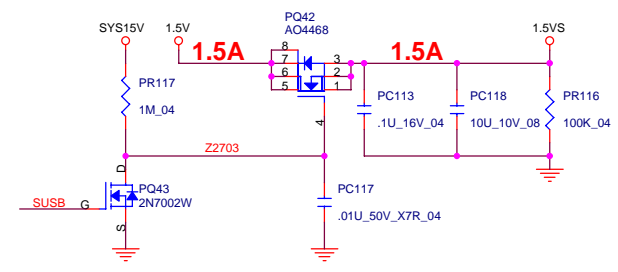
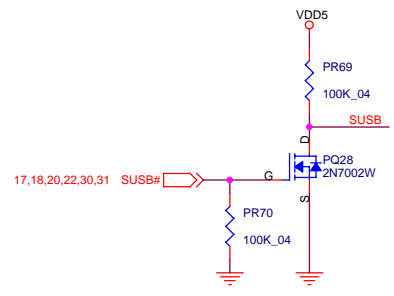
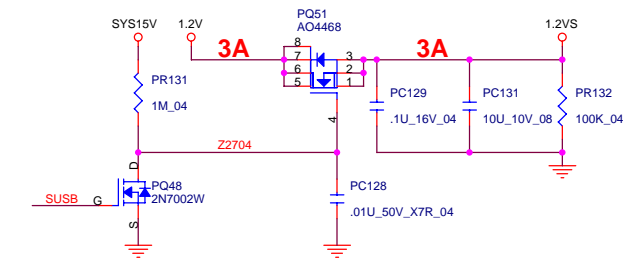
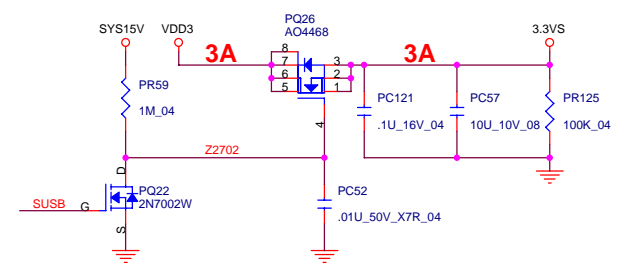
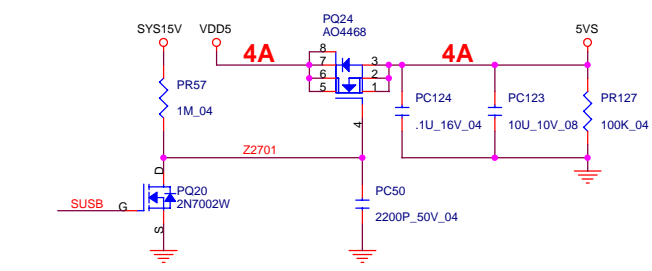
CLEVO CO. 藍天電腦

Title: [26]KBC-ITE IT8512E

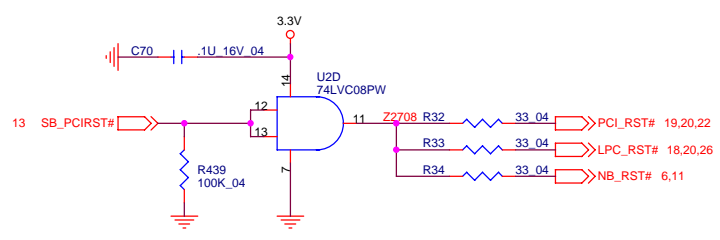
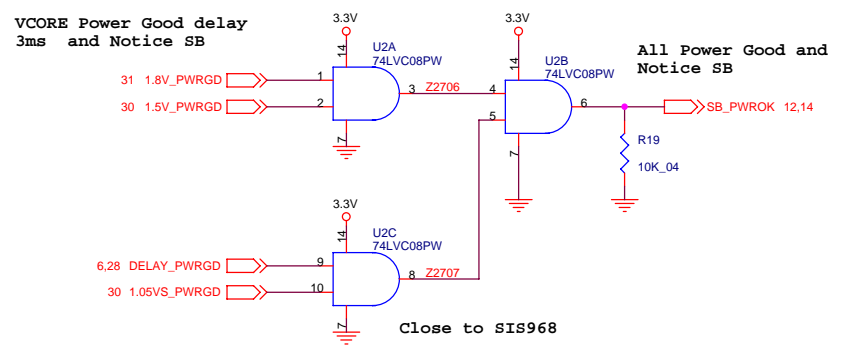
Size A3 Document Number M720SR Socket P 6-71-M7200-D02A Rev 2.0A

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1.2VS, 1.5VS, 1.8VS, 3.3VS, 5VS



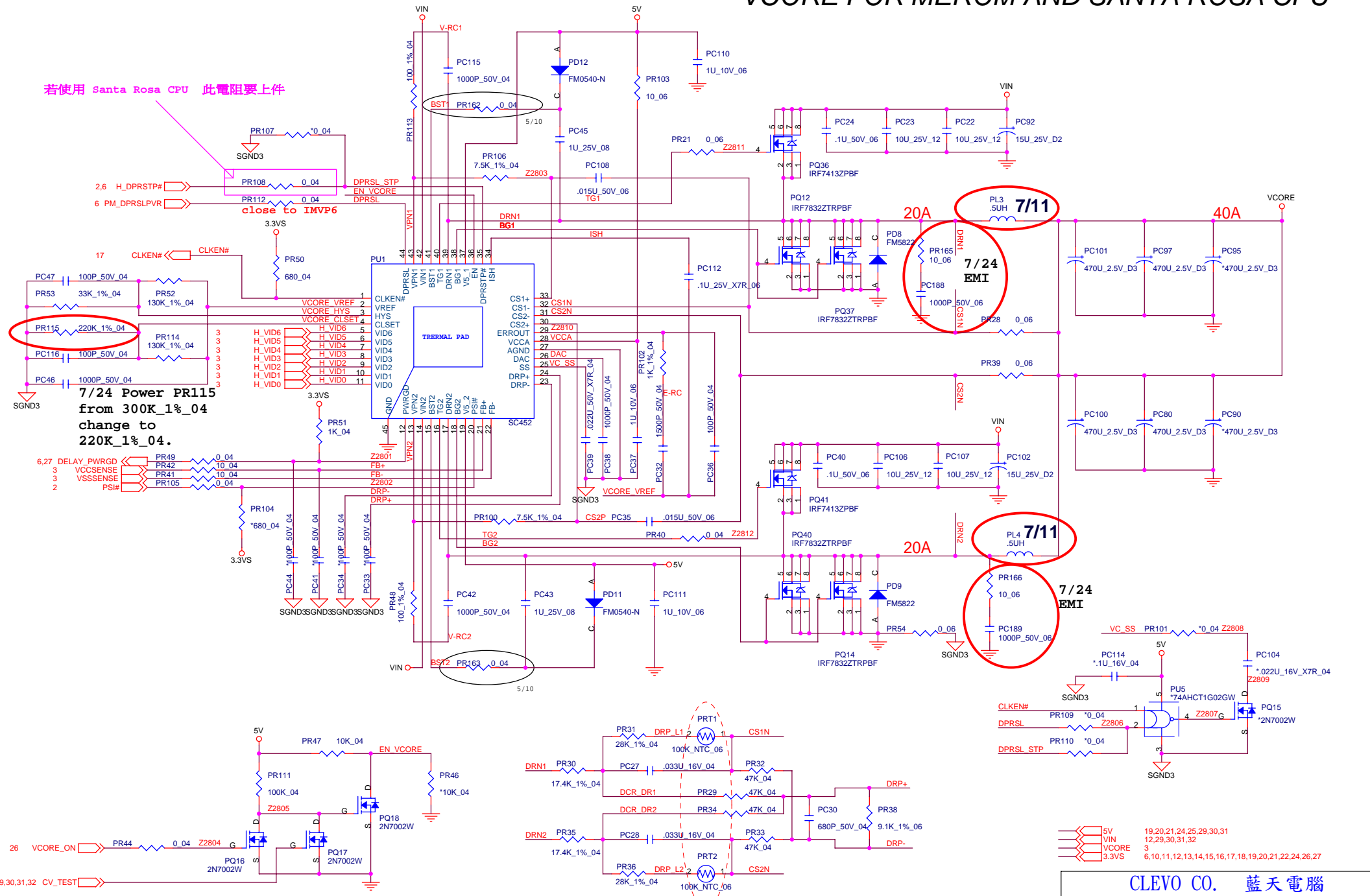
POWER GOOD & RESET



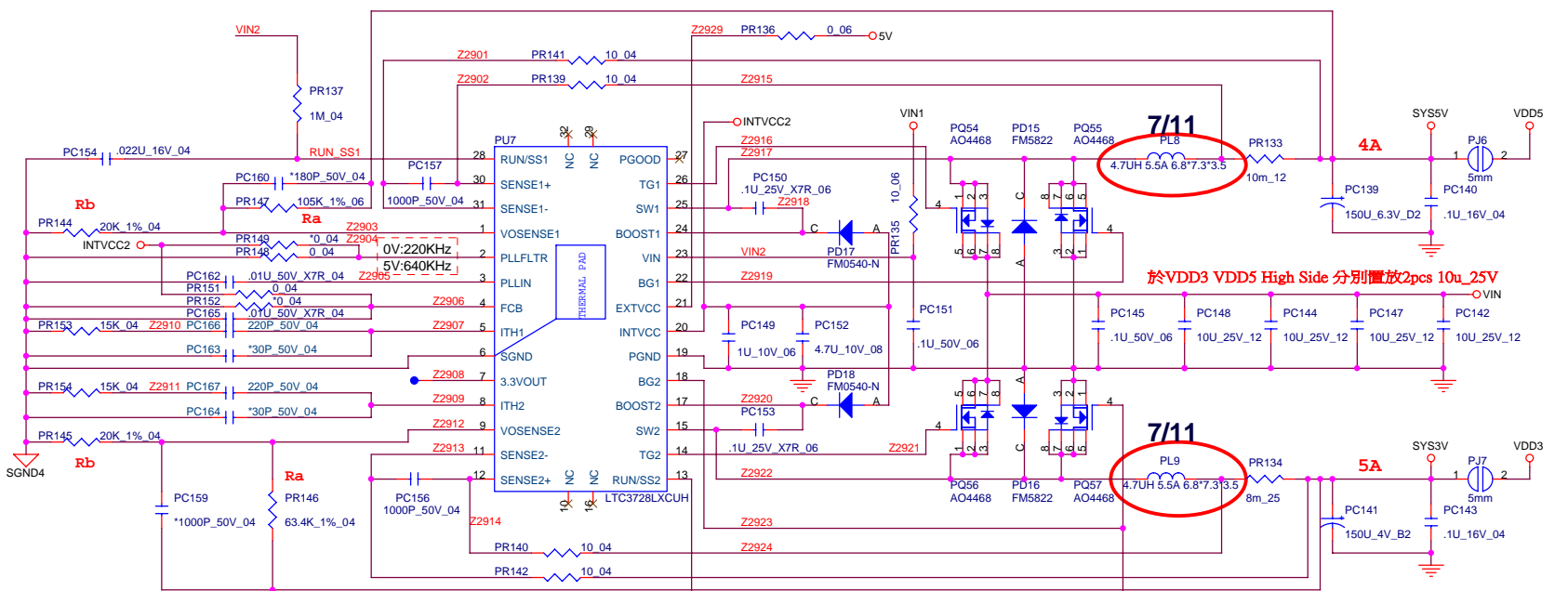
1.2V	7,30
1.2VS	4,7
1.5V	30
1.5VS	3,6,20
1.8V	5,7,9,10,14,15,16,30,31
1.8VS	4,5,6,7,11,12,13,14,15,16,17
3.3V	2,12,14,15,16,18,19,20,23,25,29,30,31
3.3VS	6,10,11,12,13,14,15,16,17,18,19,20,21,22,24,26,28
5V	19,20,21,24,25,28,29,30,31
5VS	12,18,19,21,24,25
SYS15V	12,29
VDD3	2,15,21,26,29,31,32
VDD5	29

CLEVO CO. 藍天電腦			
Title [27]SYSTEM POWER			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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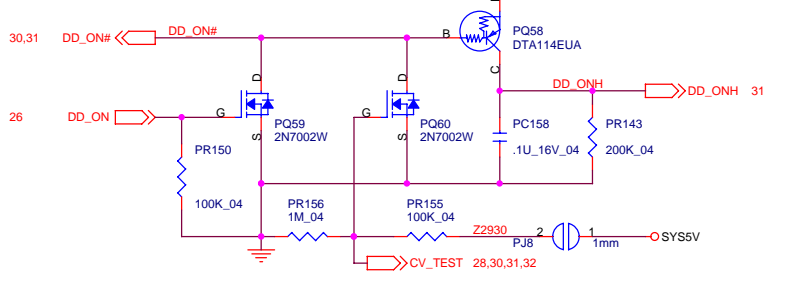
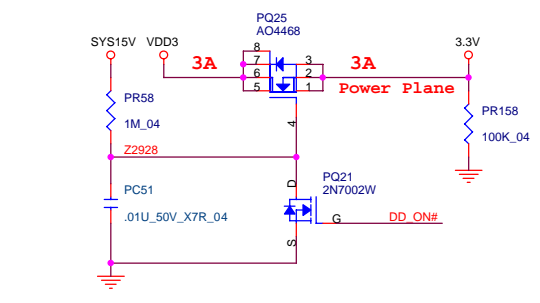
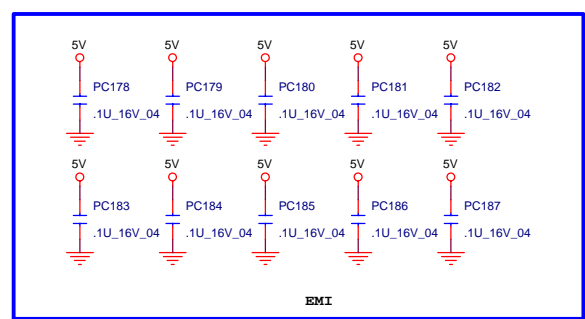
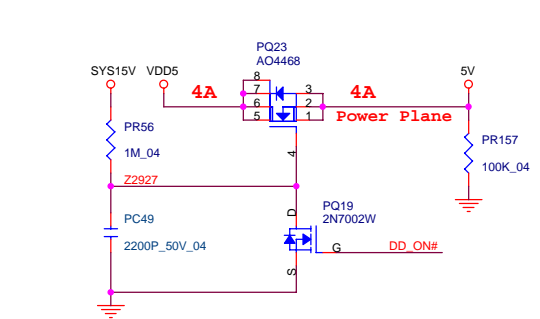
VCORE FOR MEROM AND SANTA ROSA CPU



CLEVO CO. 藍天電腦			
Title [28]VCORE			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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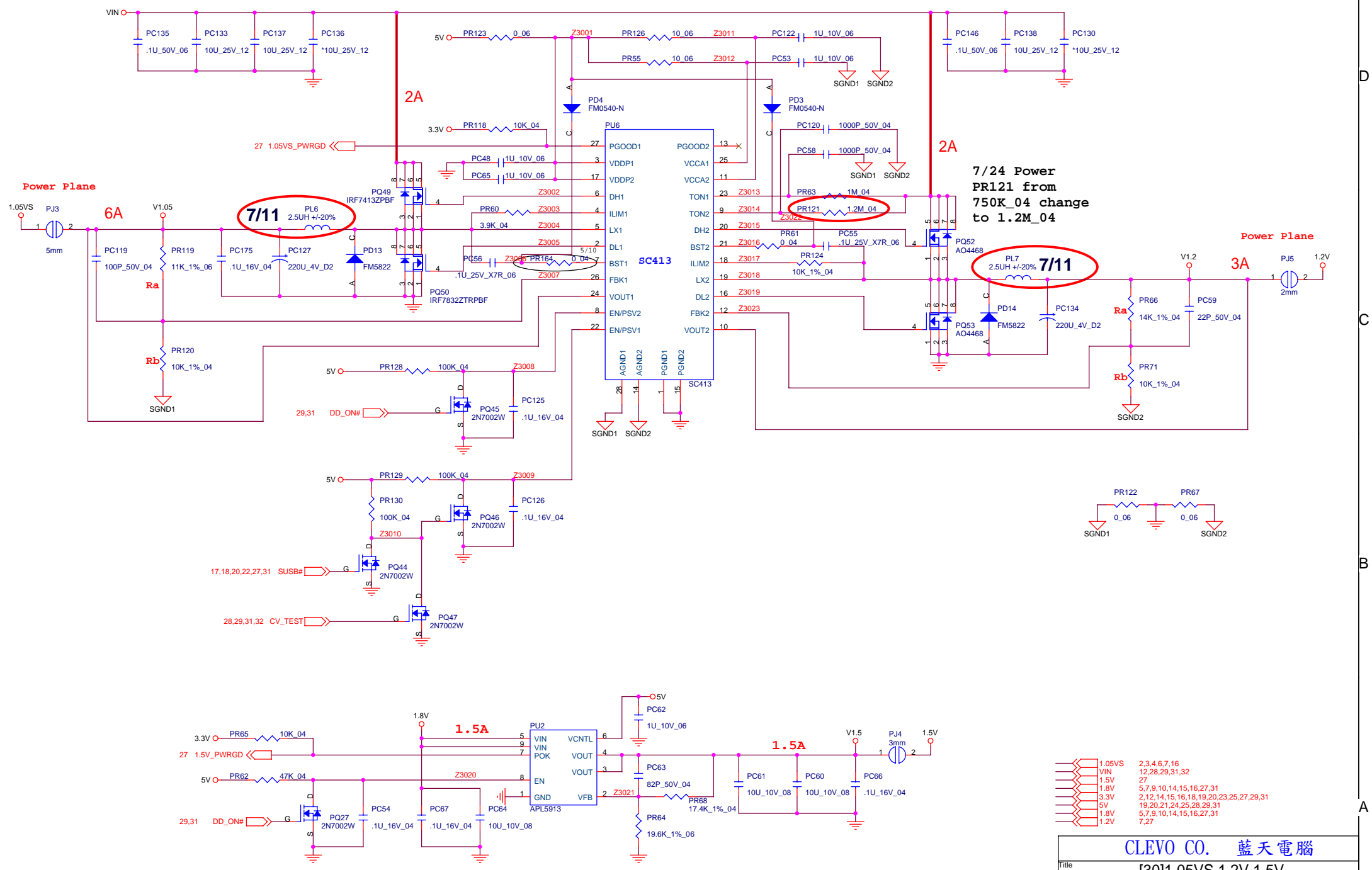


於VDD3 VDD5 High Side 分別置放2pcs 10u_25V



- VIN1 31
- SYS15V 12,27
- 3.3V 2,12,14,15,16,18,19,20,23,25,27,30,31
- VDD3 2,15,21,26,27,31,32
- 5V 19,20,21,24,25,28,30,31
- VDD5 27
- SYS5V 32
- VIN 12,28,30,31,32

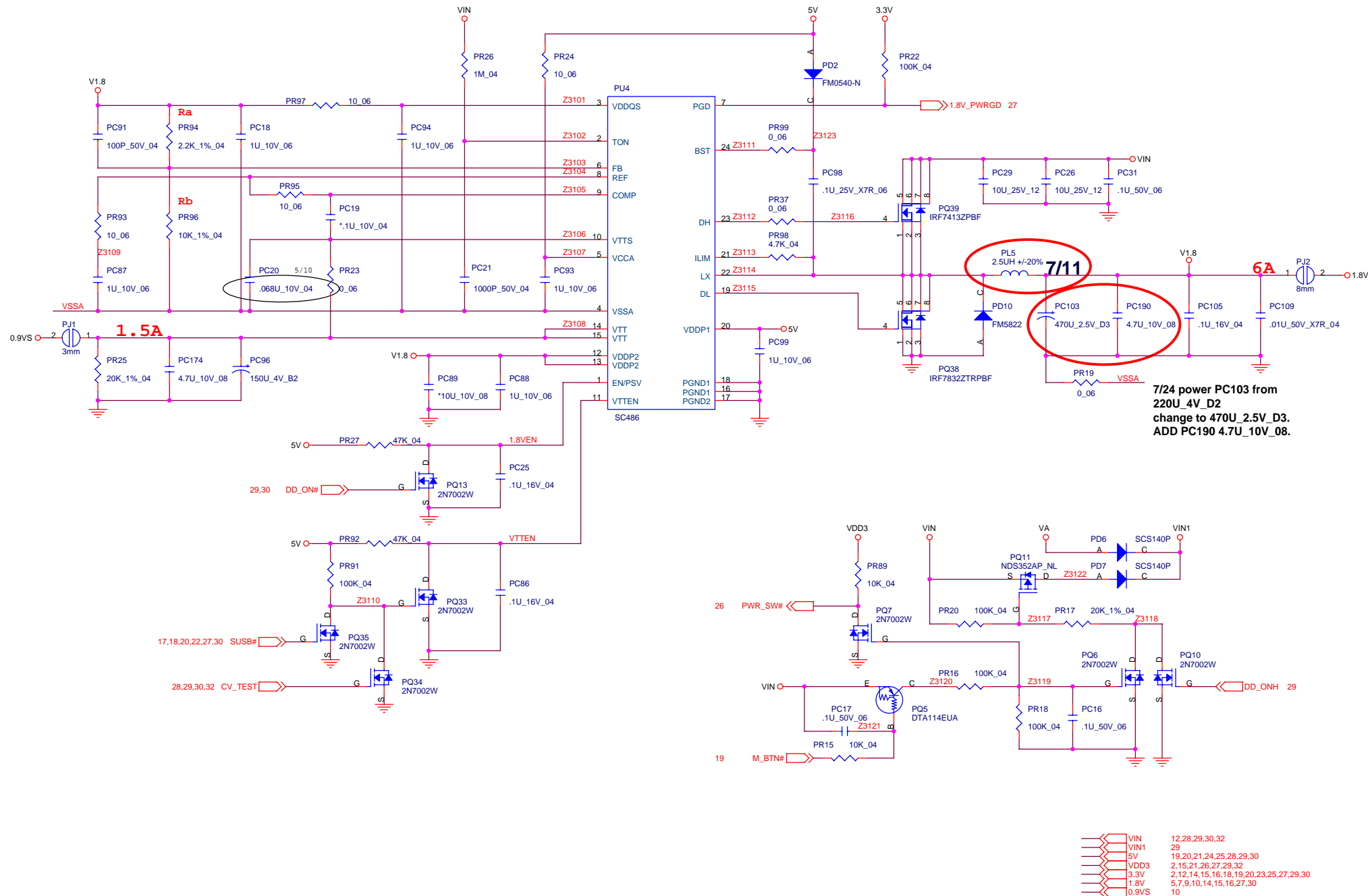
CLEVO CO. 藍天電腦			
Title [29]VDD3,VDD5			
Size A3	Document Number M720SR Socket P	6-71-M720-D02A	Rev 2.0A
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7/24 Power
PR121 from
750K_04 change
to 1.2M_04

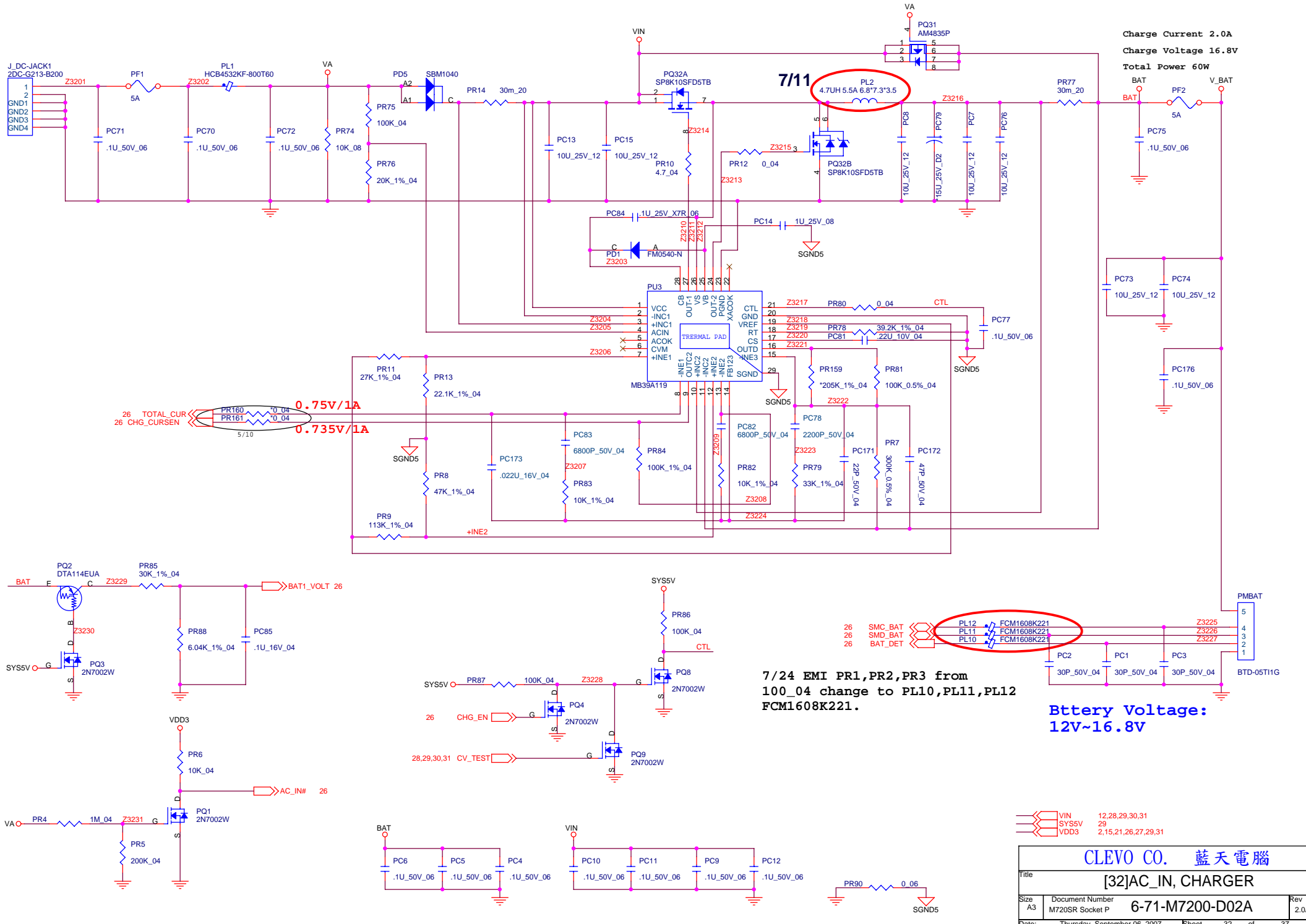
- 1.05VS 2,3,4,6,7,16
- VIN 12,28,29,31,32
- 1.5V 27
- 1.8V 5,7,9,10,14,15,16,27,31
- 3.3V 2,12,14,15,16,18,19,20,23,25,27,29,31
- 5V 19,20,21,24,25,28,29,31
- 1.8V 5,7,9,10,14,15,16,27,31
- 1.2V 7,27

CLEVO CO. 藍天電腦			
Title [30]1.05VS,1.2V,1.5V			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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- VIN 12,28,29,30,32
- VIN1 29
- 5V 19,20,21,24,25,28,29,30
- VDD3 2,15,21,26,27,29,32
- 3.3V 2,12,14,15,16,18,19,20,23,25,27,29,30
- 1.8V 5,7,9,10,14,15,16,27,30
- 0.9VS 10

CLEVO CO. 藍天電腦			
Title [31]1.8V,0.9VS			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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Charge Current 2.0A
 Charge Voltage 16.8V
 Total Power 60W

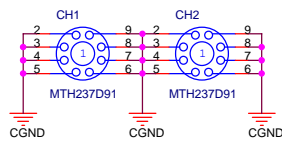
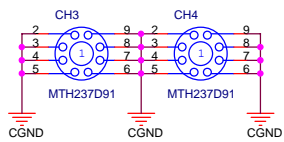
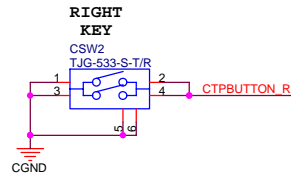
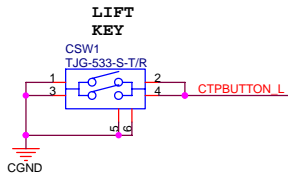
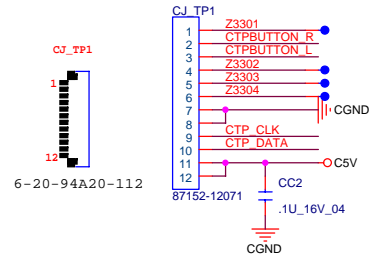
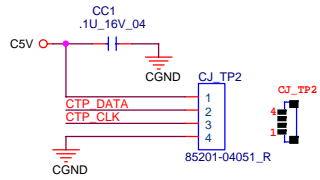
26 TOTAL_CUR
 26 CHG_CURSEN
 0.75V/1A
 0.735V/1A

7/24 EMI PR1,PR2,PR3 from 100_04 change to PL10,PL11,PL12 FCM1608K221.

Battery Voltage: 12V~16.8V

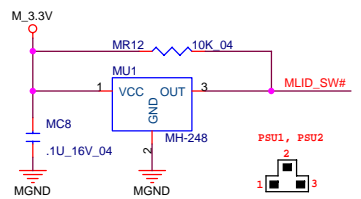
- VIN 12,28,29,30,31
- SYSSV 29
- VDD3 2,15,21,26,27,29,31

CLEVO CO. 藍天電腦			
Title [32]AC_IN, CHARGER			
Size A3	Document Number M720SR Socket P	6-71-M7200-D02A	Rev 2.0A
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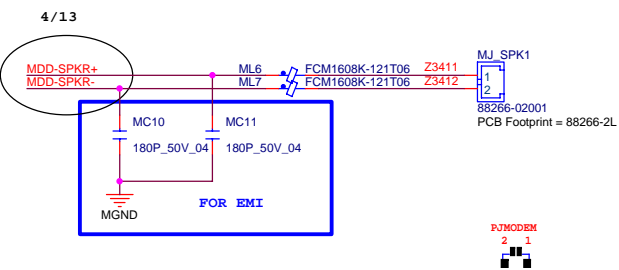


CLEVO CO. 藍天電腦		
Title [33]CLICK BOARD		
Size A3	Document Number 6-71-M72S2-D03	Rev 3.0
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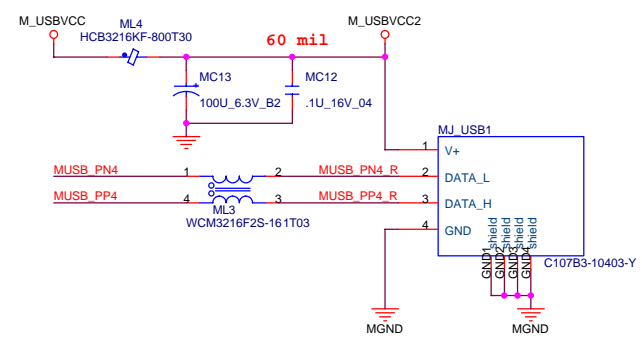
LID SWITCH IC



SPEAKER CONNECTOR

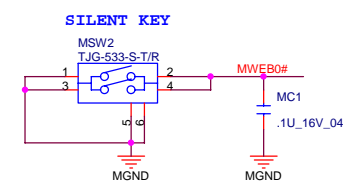
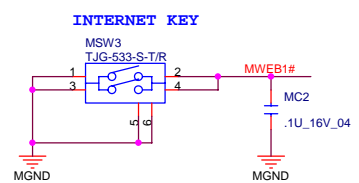
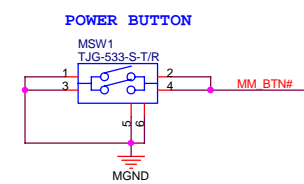
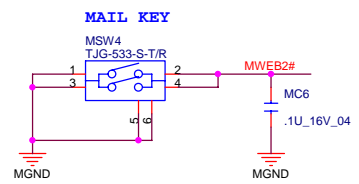
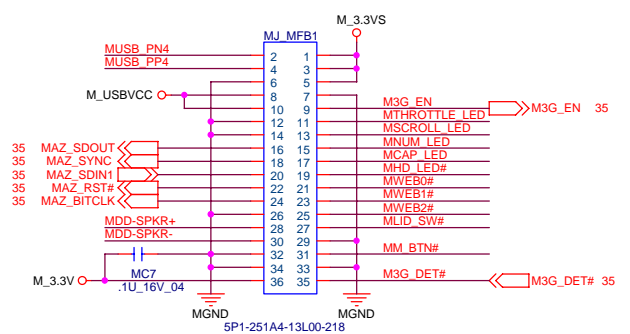


USB PORT

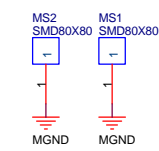
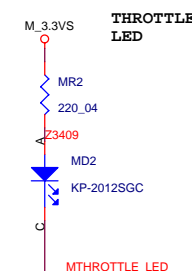
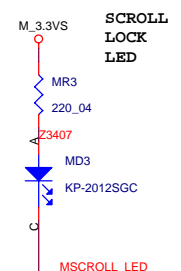
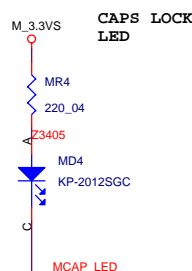
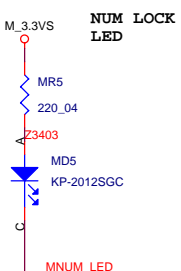
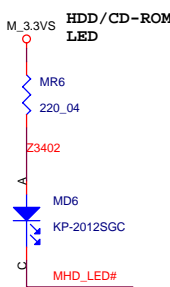
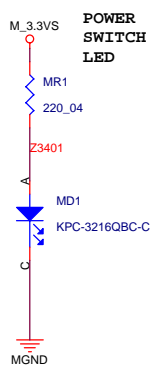


HOT KEY & POWER SW

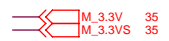
M_3.3VS NEED 4A



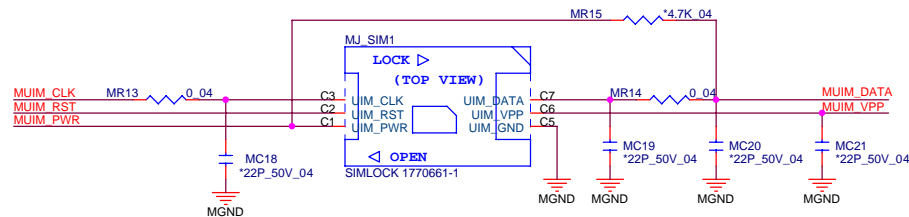
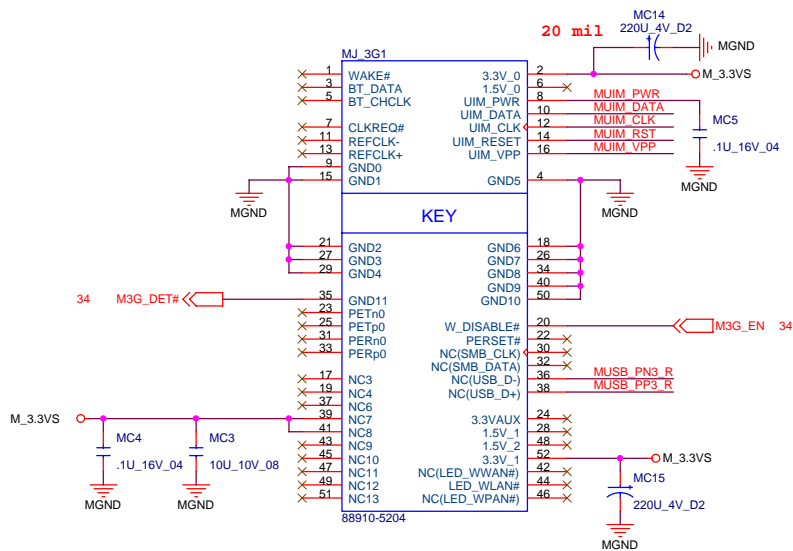
LED



7/13
Remove
MQ1, MQ2, MQ3, MQ4
for LED low active.

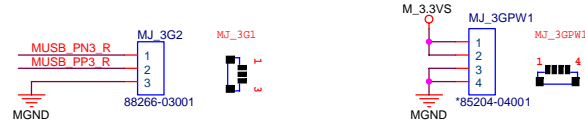


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Title [34]MULTI I/O BOARD 1/2		
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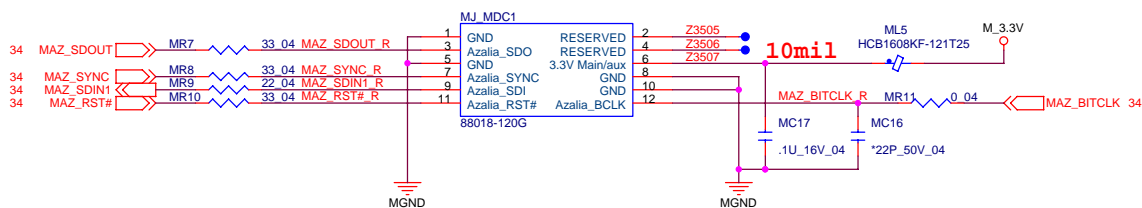
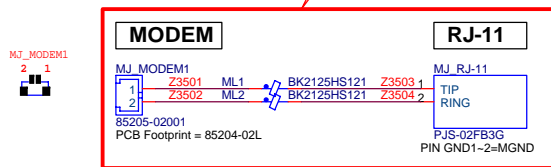


Layout時

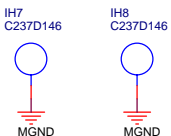
1. SIM之所有信號線加粗(10mil)
2. 所有信號線之間加GND
3. SIM hold 本體四周加GND圍繞
4. SIM CONN 靠近 MINI CARD CONN



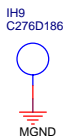
須與其他零件或線路
淨空 2.5mm 以上



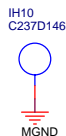
IH4, IH5
銅柱 6-34-D90T0-010



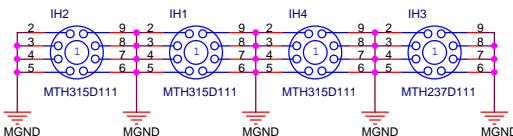
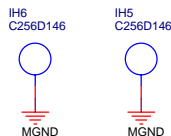
IH6 (A1)
銅柱 6-34-M72SS-020



IH7 (A2)
銅柱 6-34M72SS-010



IH8, IH9
銅柱 6-34-M56AS-011



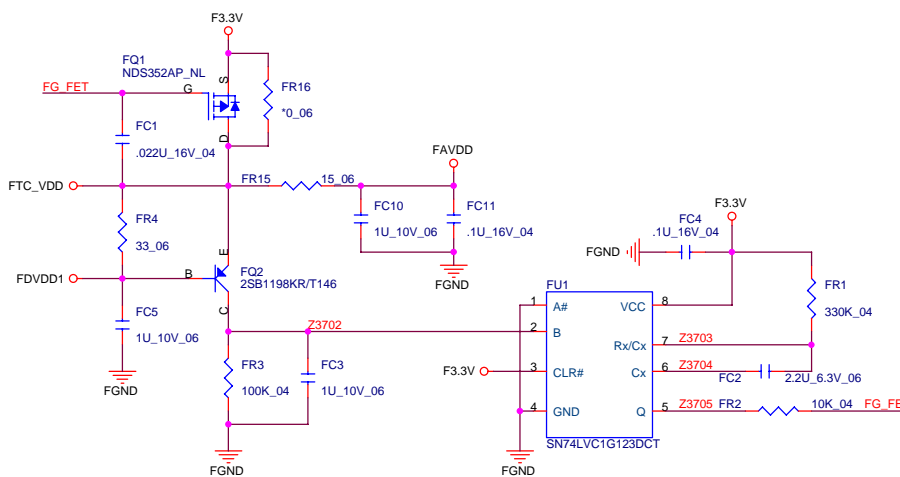
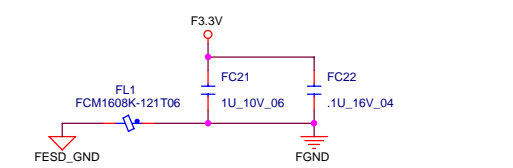
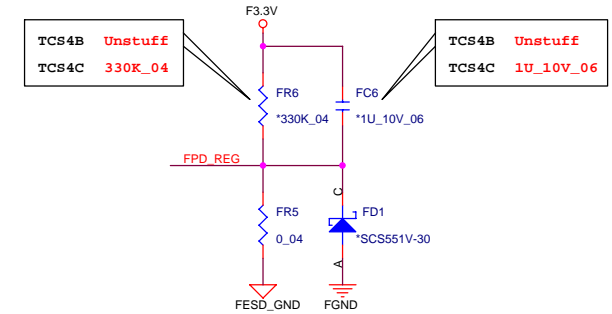
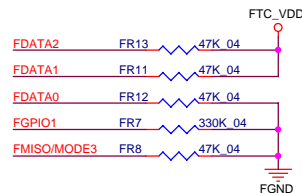
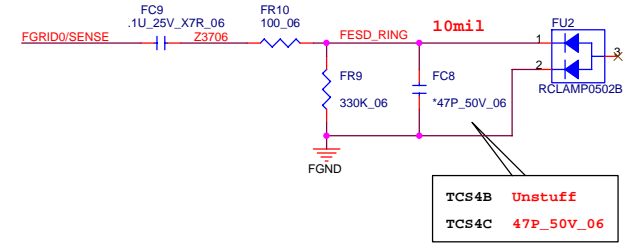
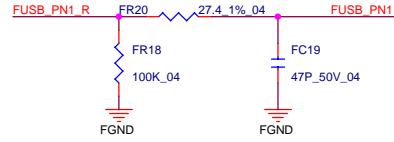
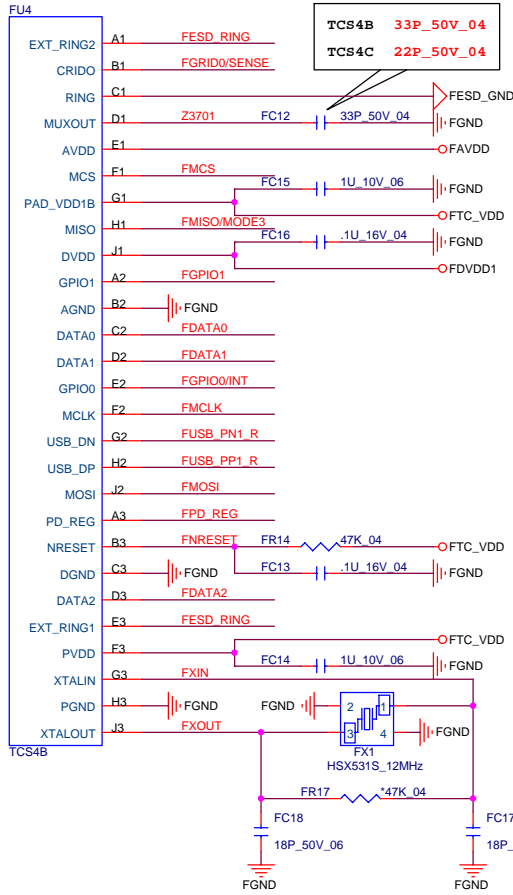
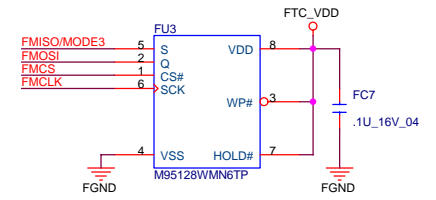
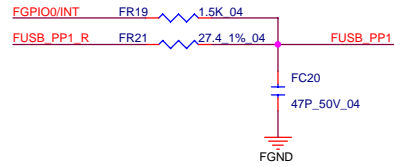
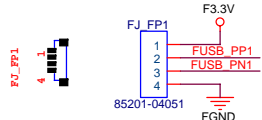
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9/6 Delet BIOS board circuit for MP stage.

Normal Low

LPC_FLASH	Flash Mode
HI	Enable
LO	Protected

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Title [36]BIOS BOARD		
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Title [37]FINGER PRINTER BOARD			
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