

1 2 3 4 5 6 7 8

A

A

B

B

C

C

D

D

E

E

BERLIN 10G

M96/M92/Madison/Park

MP Build

2010.03.18

DATE	CHANGE NO.	REV

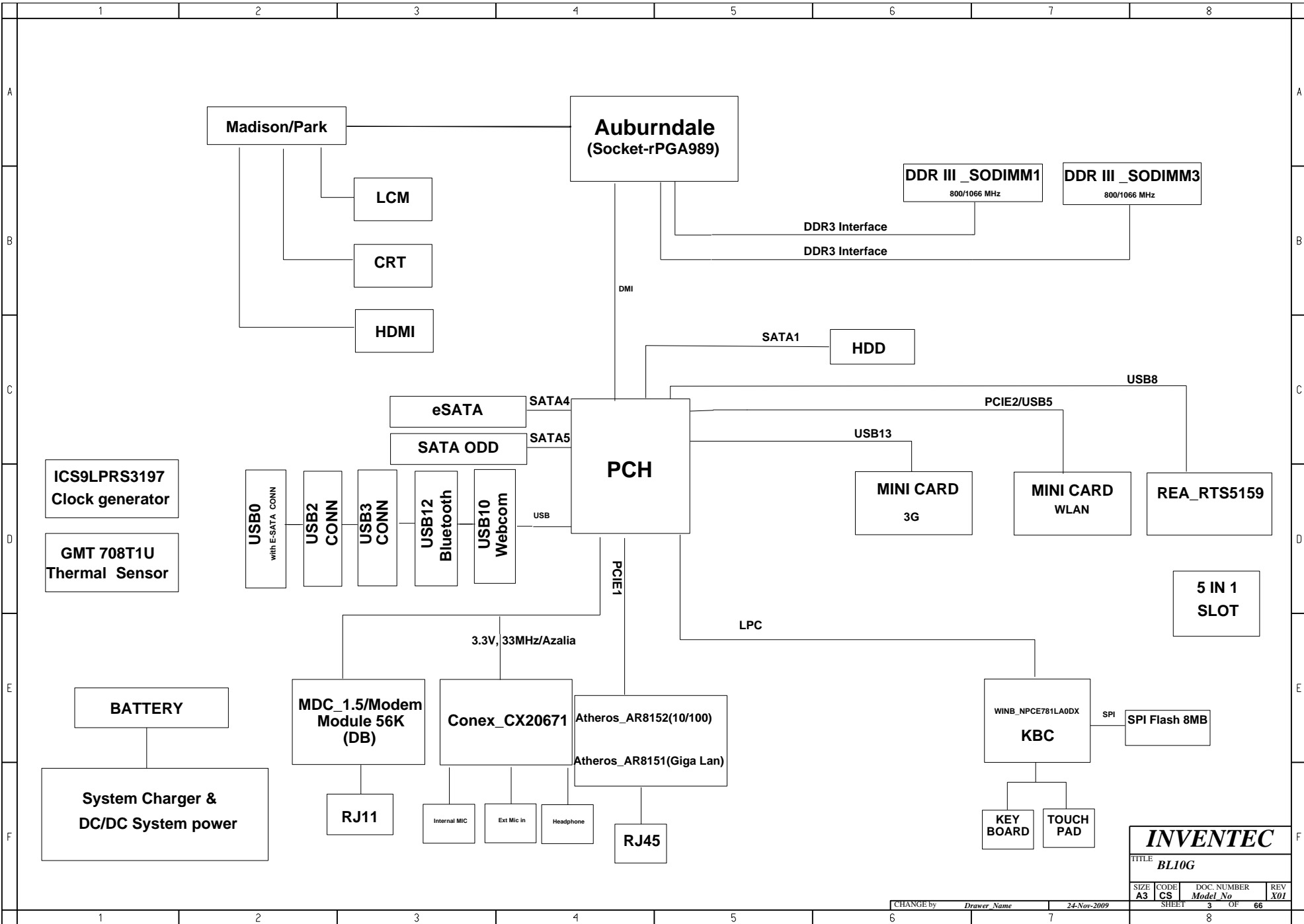
DRAWER	EE	DATE	POWER	DATE	INVENTEC			
DESIGN								
CHECK								
RESPONSIBLE								
TITLE					BL10G			
SIZE =	3	VER :						
FILE NAME :	XXXX-XXXXXX-XX				SIZE	CODE	DOC. NUMBER	REV
P/N	XXXXXXXXXXXX				A3	CS	Model No	X01
					SHEET	1	OF	66

1 2 3 4 5 6 7 8

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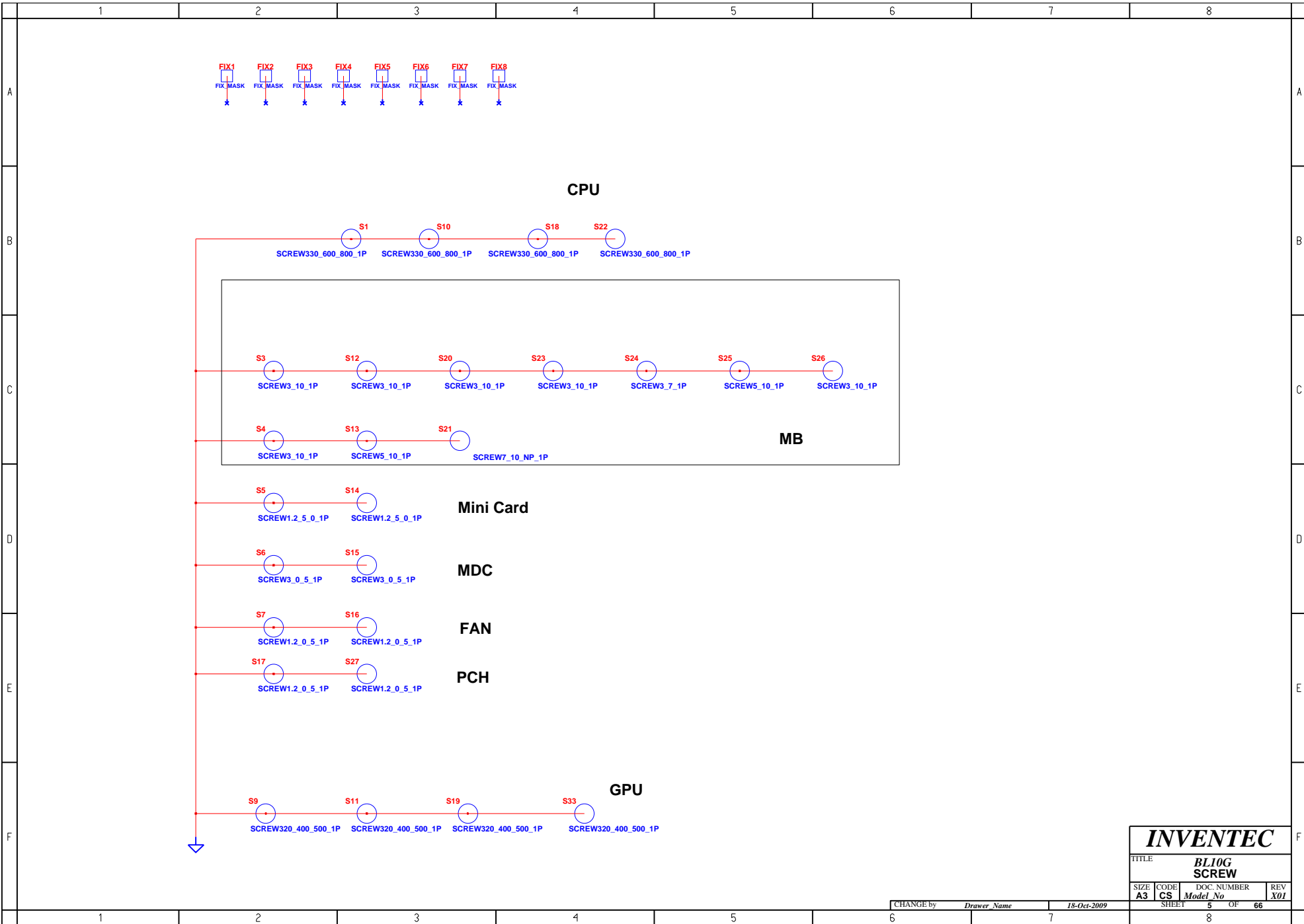
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TITLE <i>BL10G</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	
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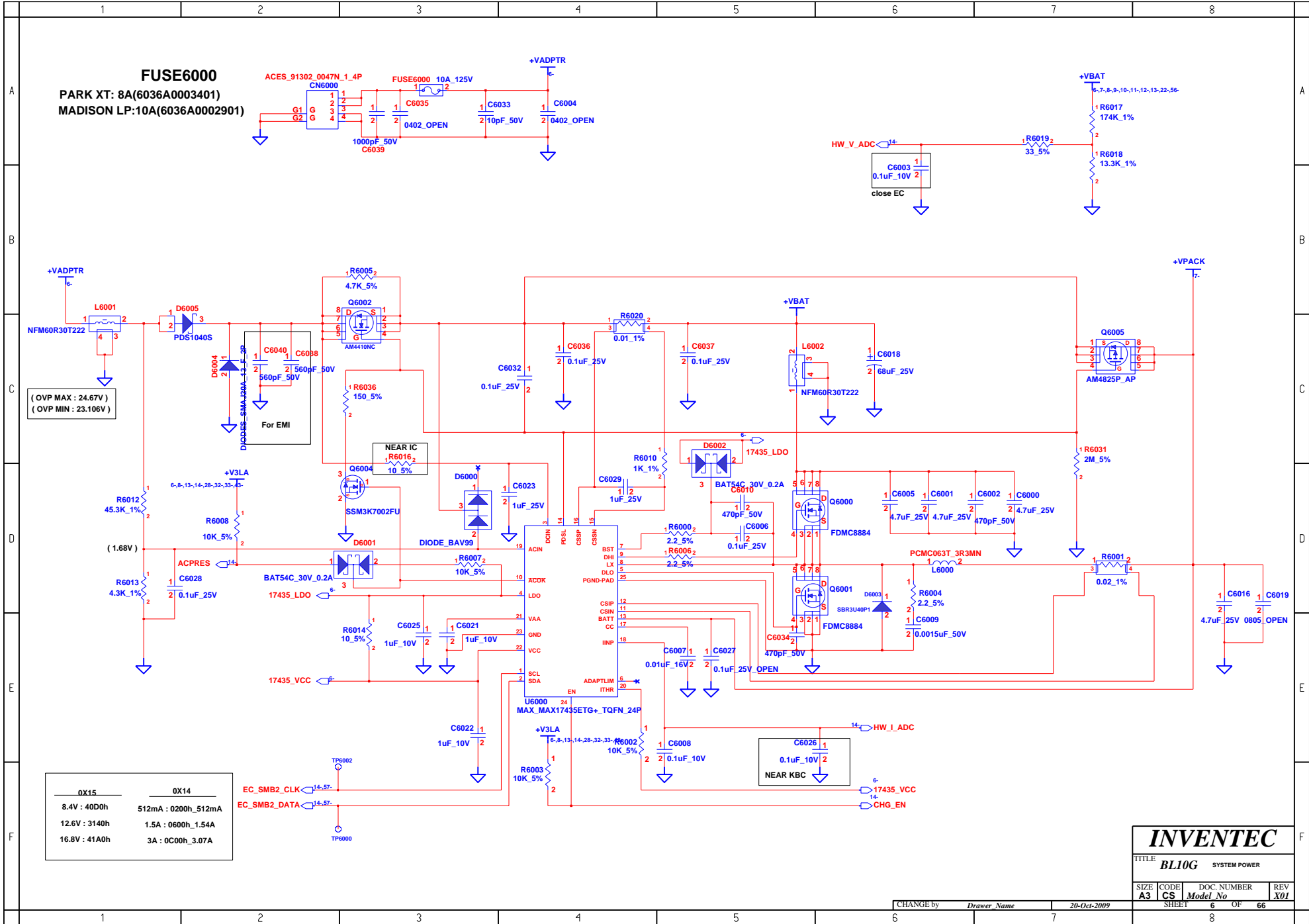
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TITLE BL10G			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
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3		66	

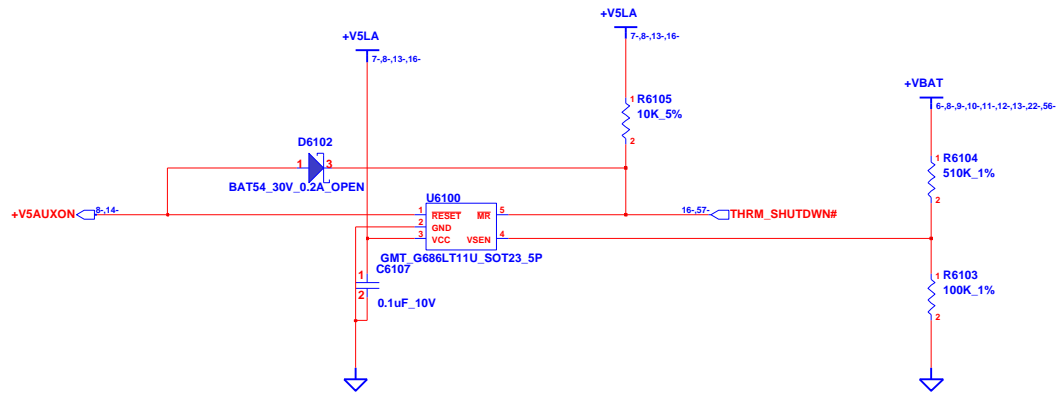
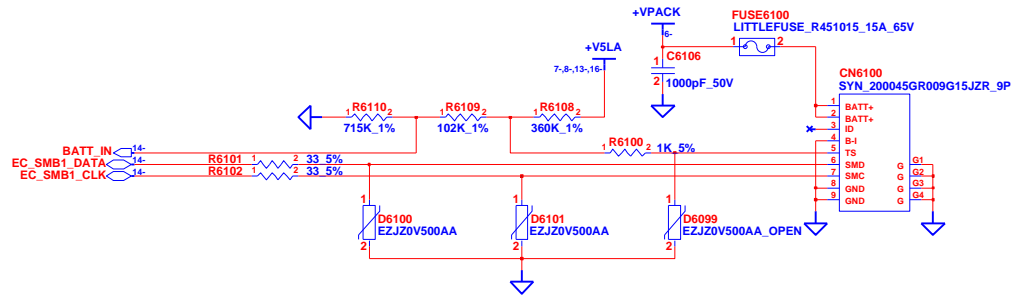
	1	2	3	4	5	6	7	8	
A	<p>Need to update power map.</p>								A
B									
C									
D									
E									
F									
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE BL10G			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No.</i>	REV X01
CHANGE by <i>Drawer Name</i>		23-Nov-2009	SHEET 4 OF 66



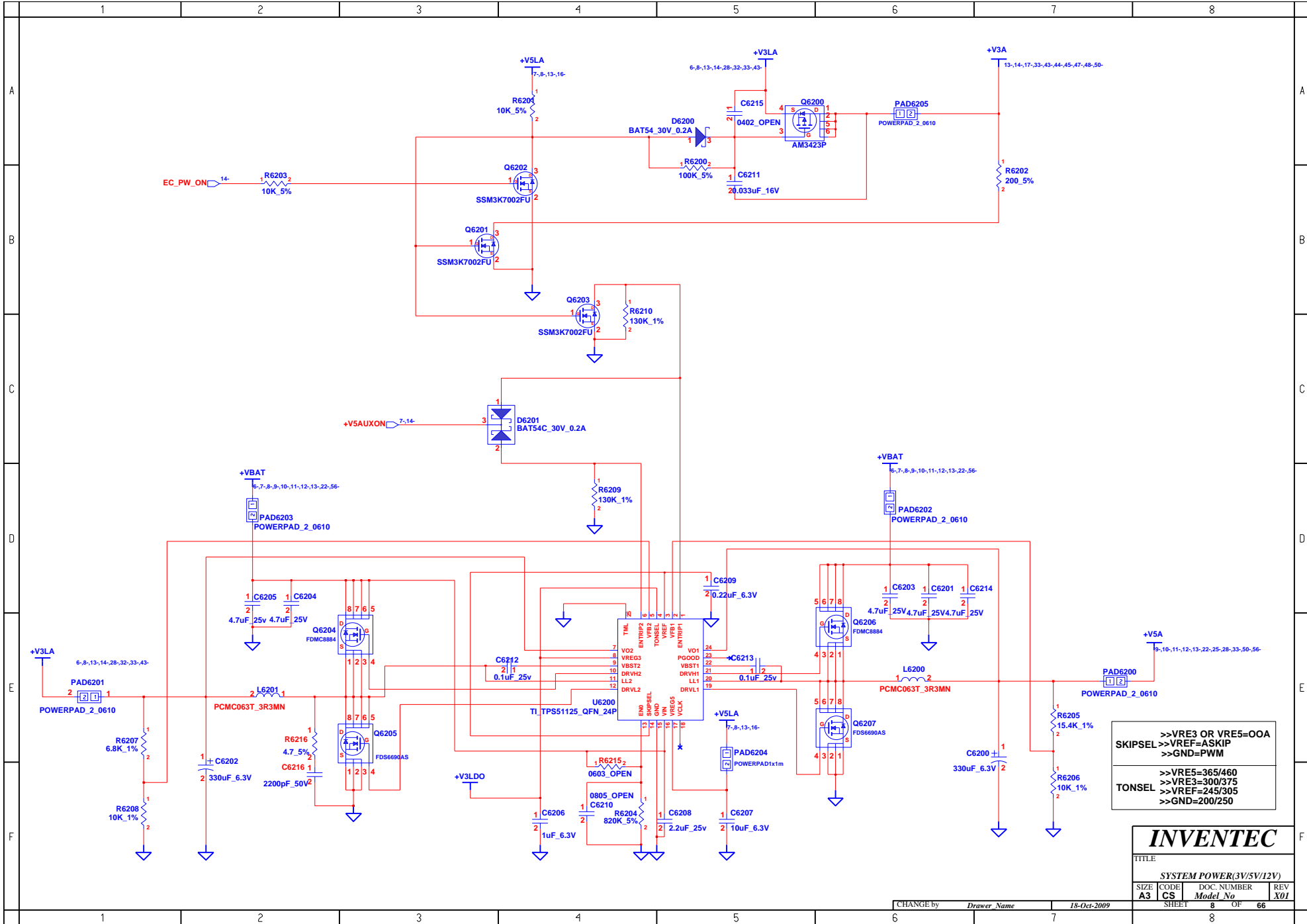
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TITLE BL10G SCREW			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	
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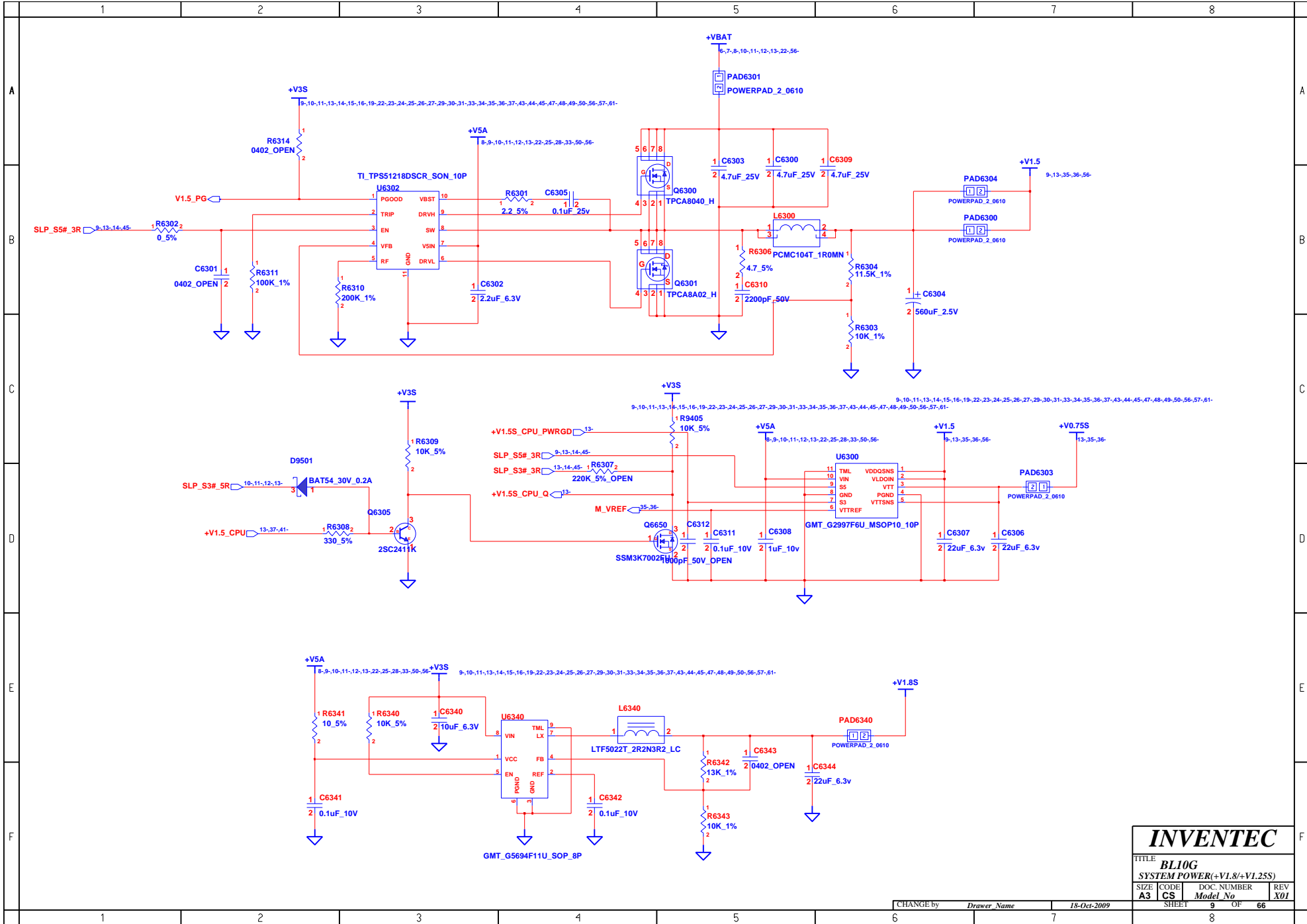




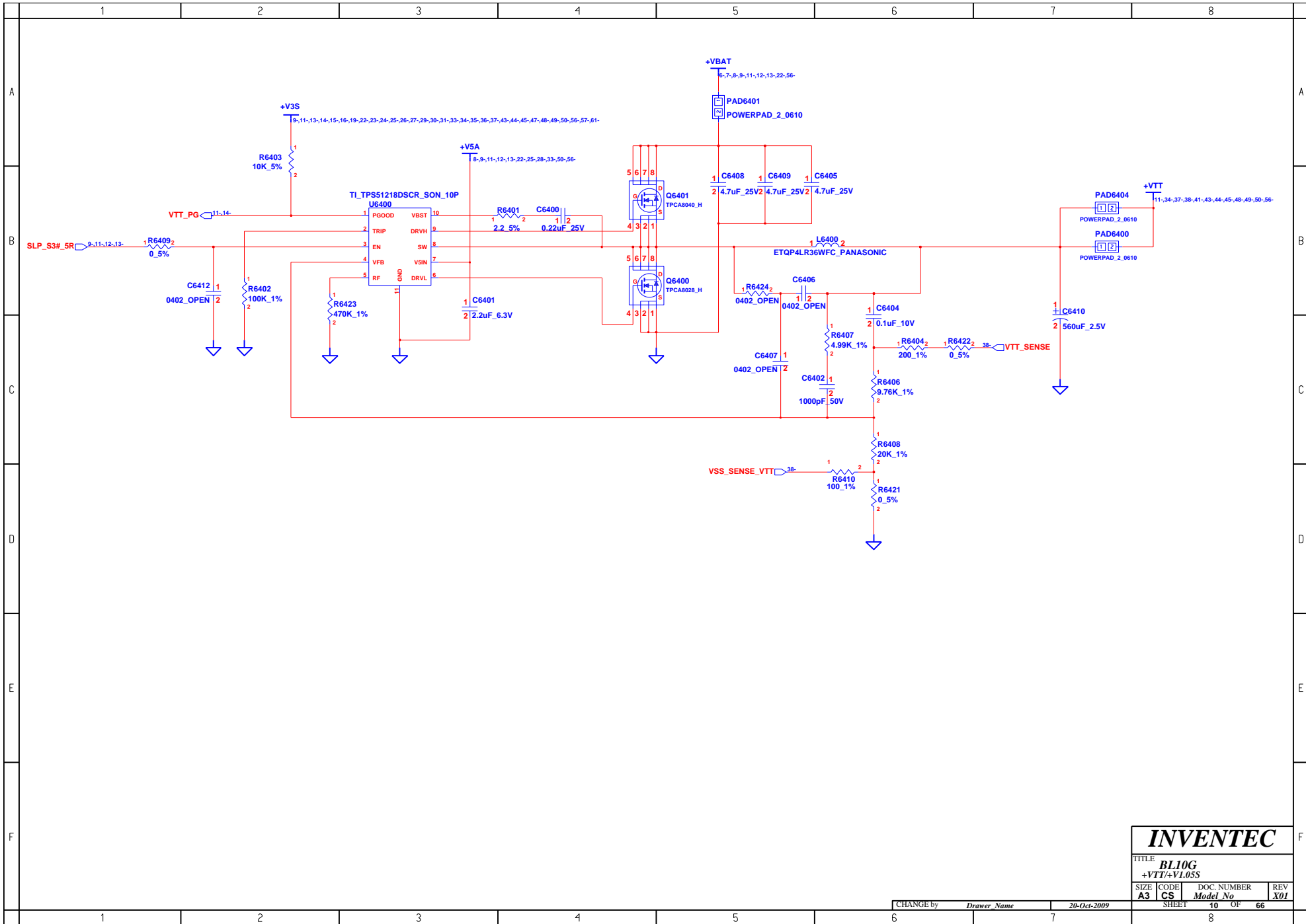
INVENTEC

TITLE			
BL10G			
SELECT & BATTERY CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model_No	X01
CHANGE by		Drawer Name	18-Oct-2009
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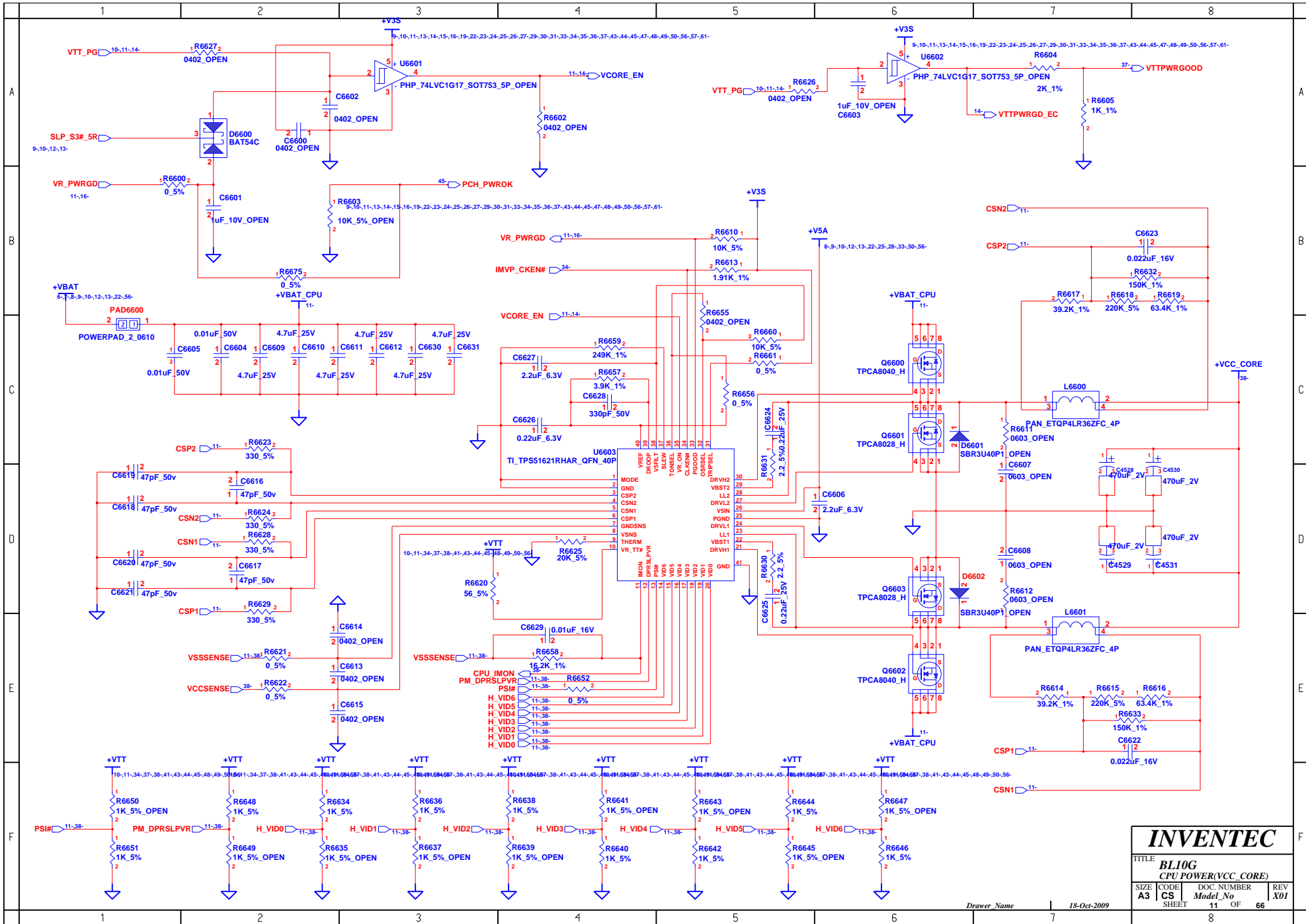




INVENTEC			
TITLE BL10G			
SYSTEM POWER(+V1.8/+V1.25S)			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	
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INVENTEC			
TITLE BL10G			
+VTT/+V1.05S			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
CHANGE by <i>Drawer Name</i>		20-Oct-2009	SHEET 10 OF 66

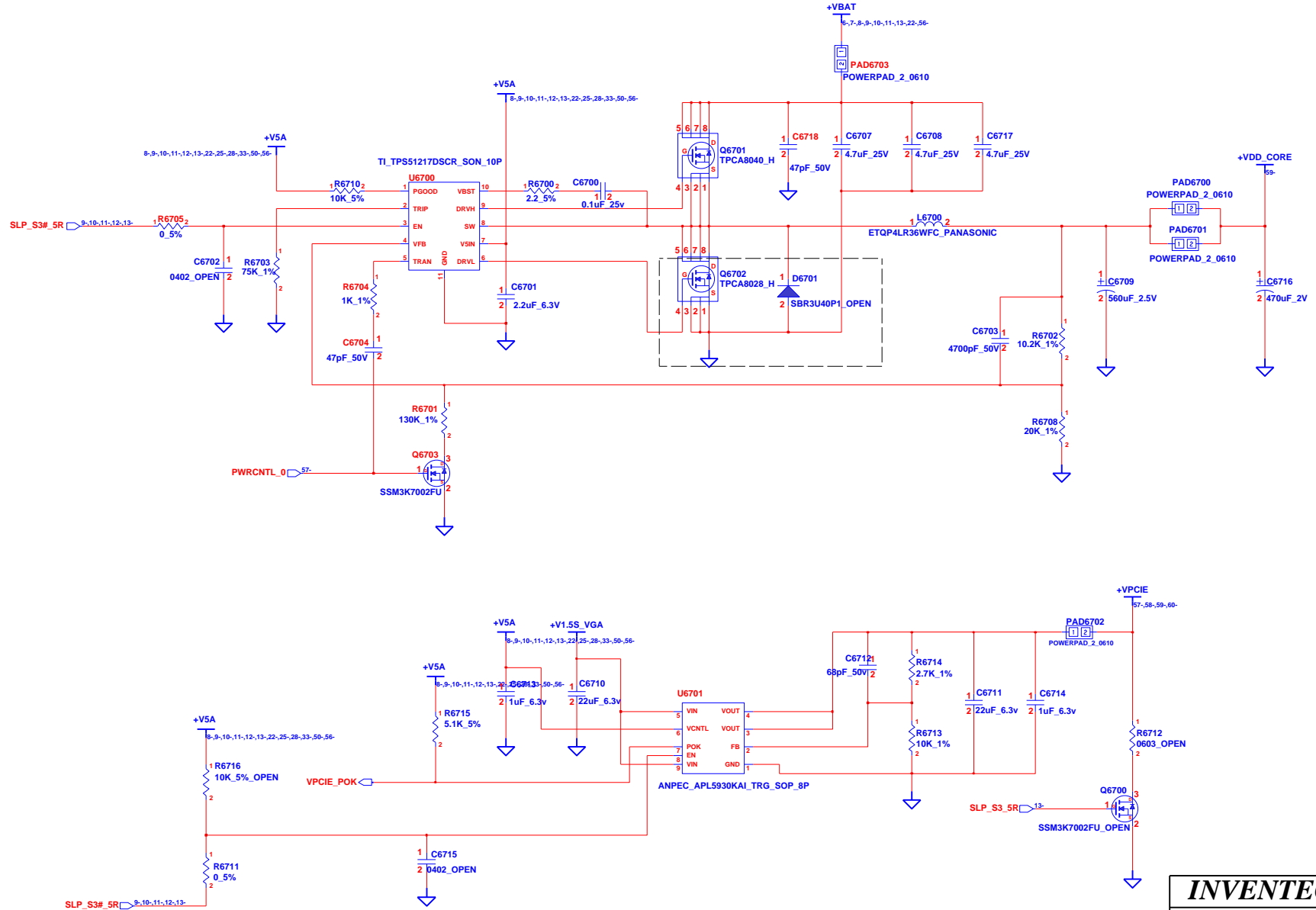


INVENTEC

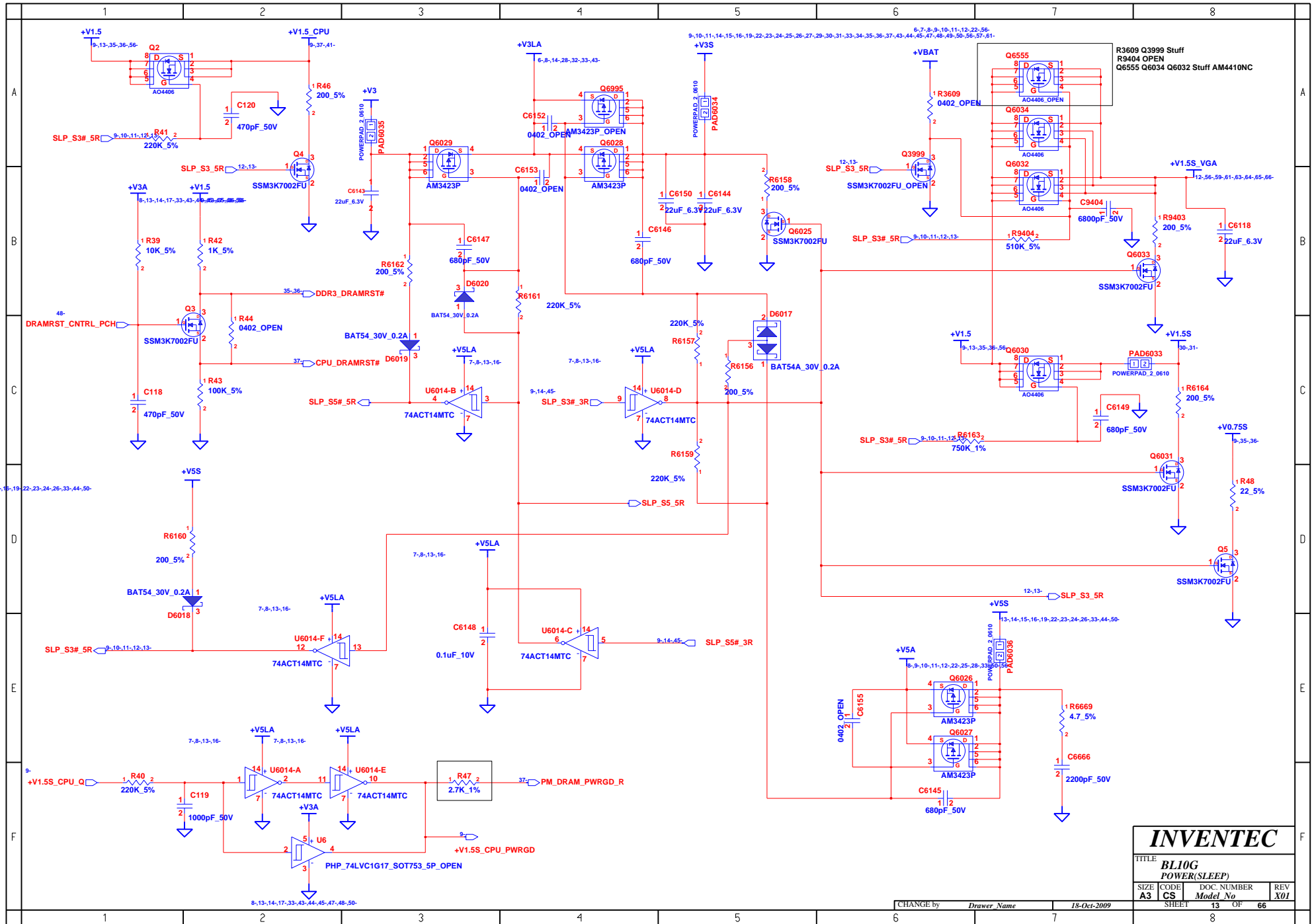
TITLE
BLI0G
 CPU POWER(VCC_CORE)

SIZE	CODE	DOC NUMBER	REV
A3	CS	Model_No	X01

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INVENTEC			
TITLE BL10G			
VGA POWER			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
CHANGE by Drawer Name		18-Oct-2009	SHEET 12 OF 66



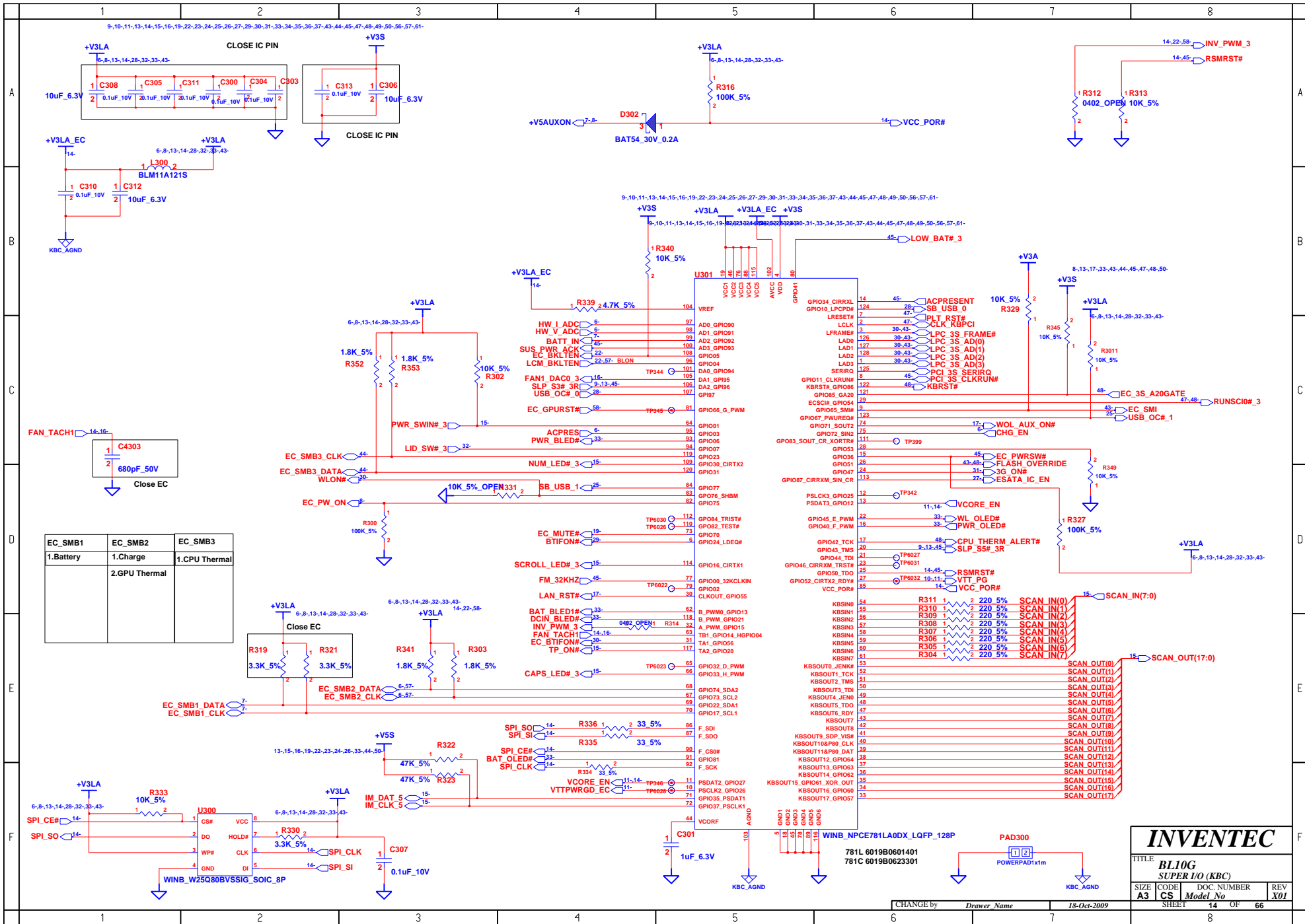
INVENTEC

TITLE
**BL10G
POWER(SLEEP)**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

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CHANGE by *Drawer Name* 18-Oct-2009



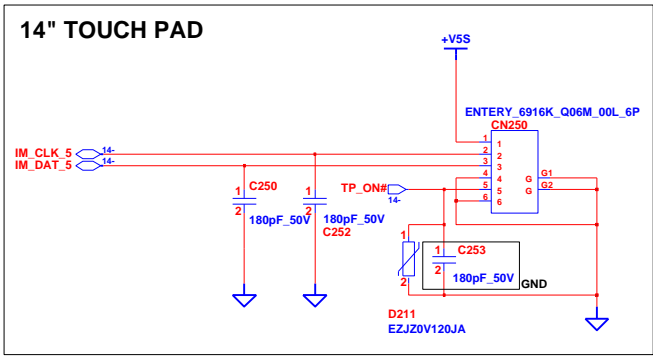
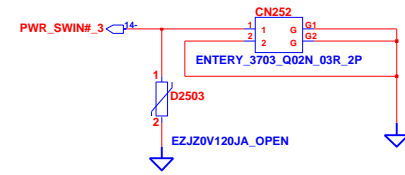
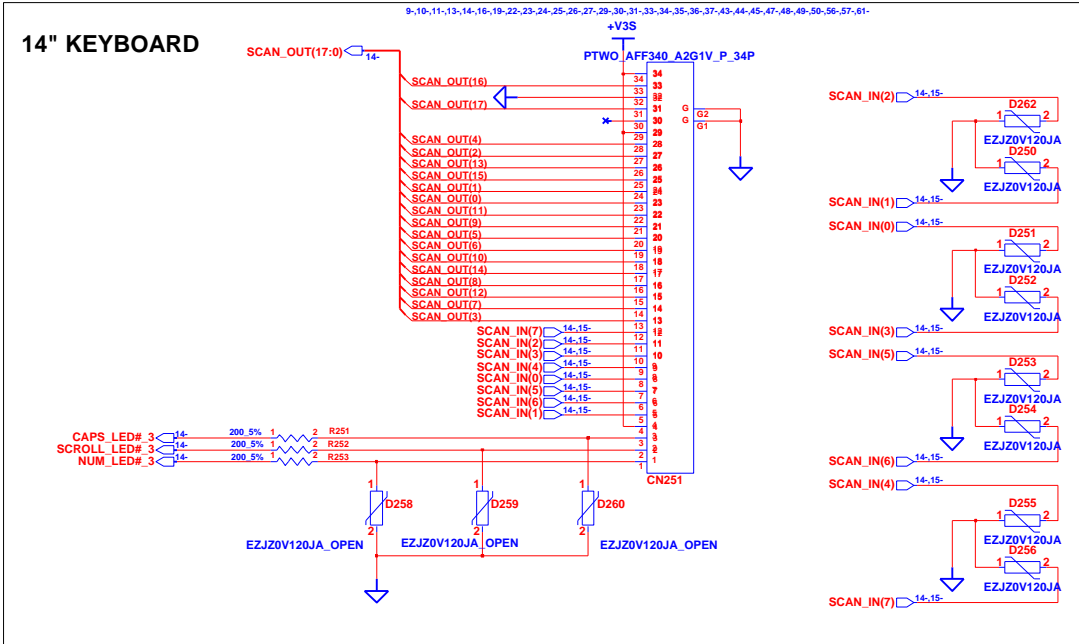
EC_SMB1	EC_SMB2	EC_SMB3
1.Battery	1.Charge	1.CPU Thermal
	2.GPU Thermal	

INVENTEC

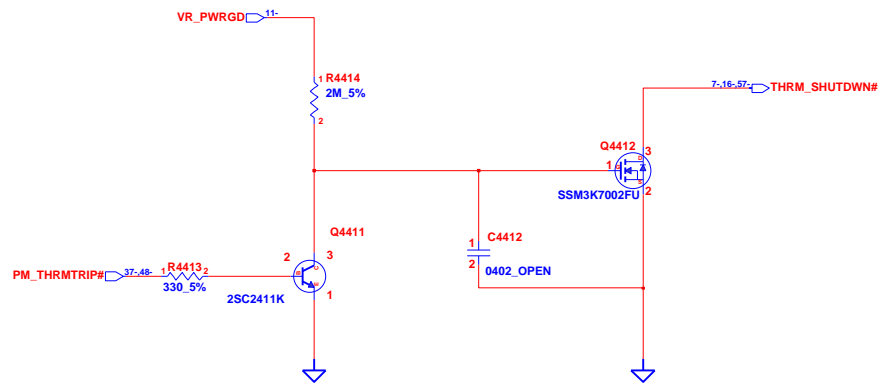
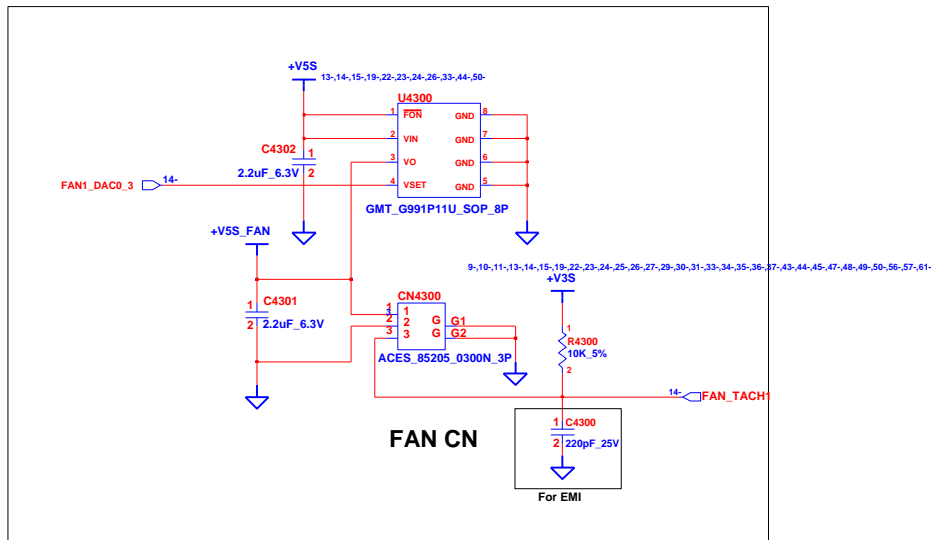
TITLE
**BLI0G
 SUPER I/O (KBC)**

SIZE A3	CODE CS	DOC NUMBER Model No	REV X01
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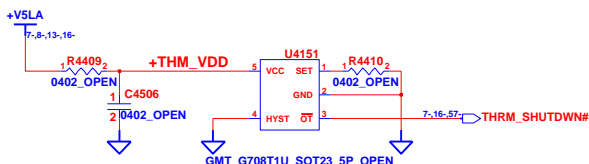
SHEET 14 OF 66



INVENTEC			
TITLE BL10G			
K/B & TP/B CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
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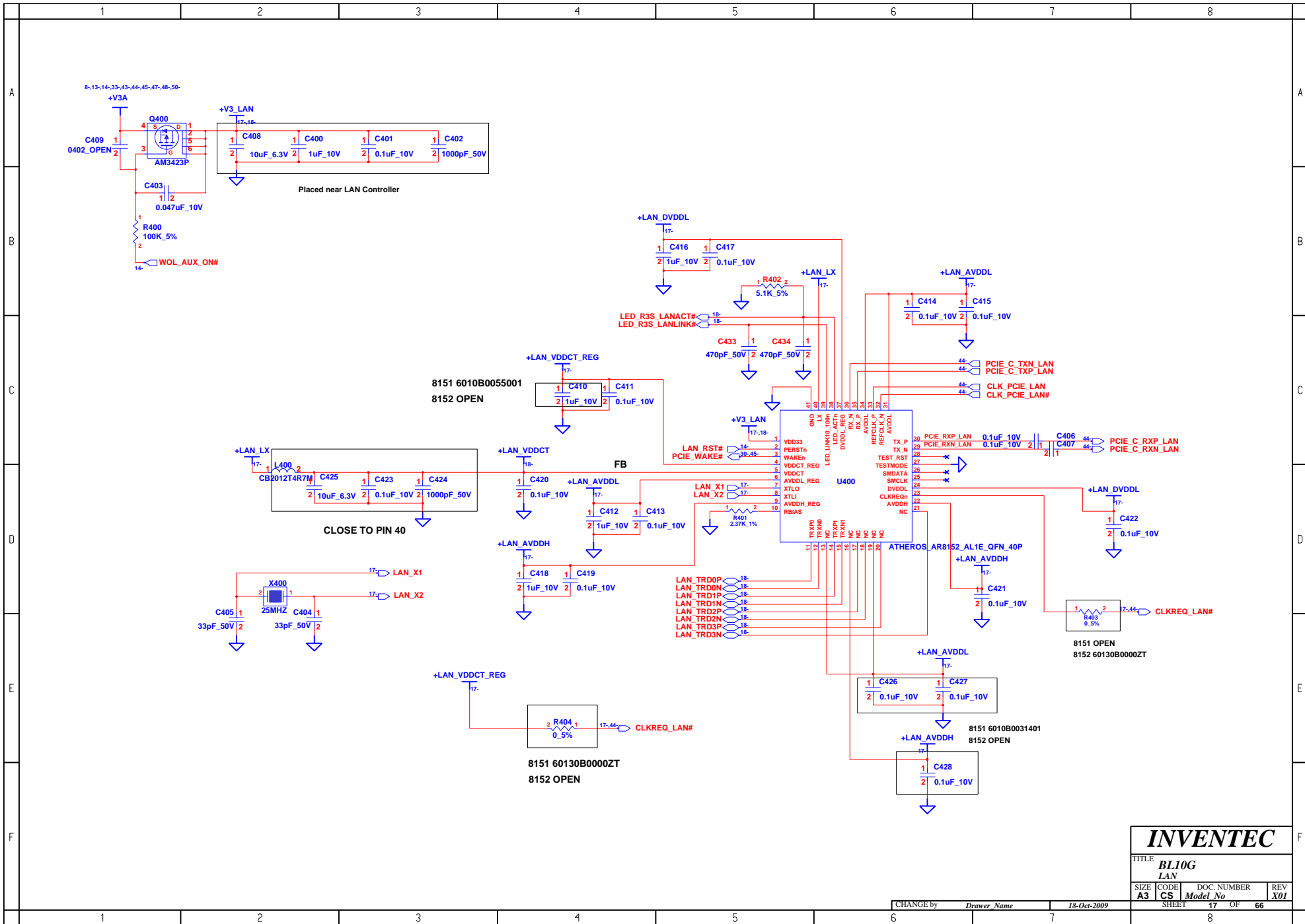


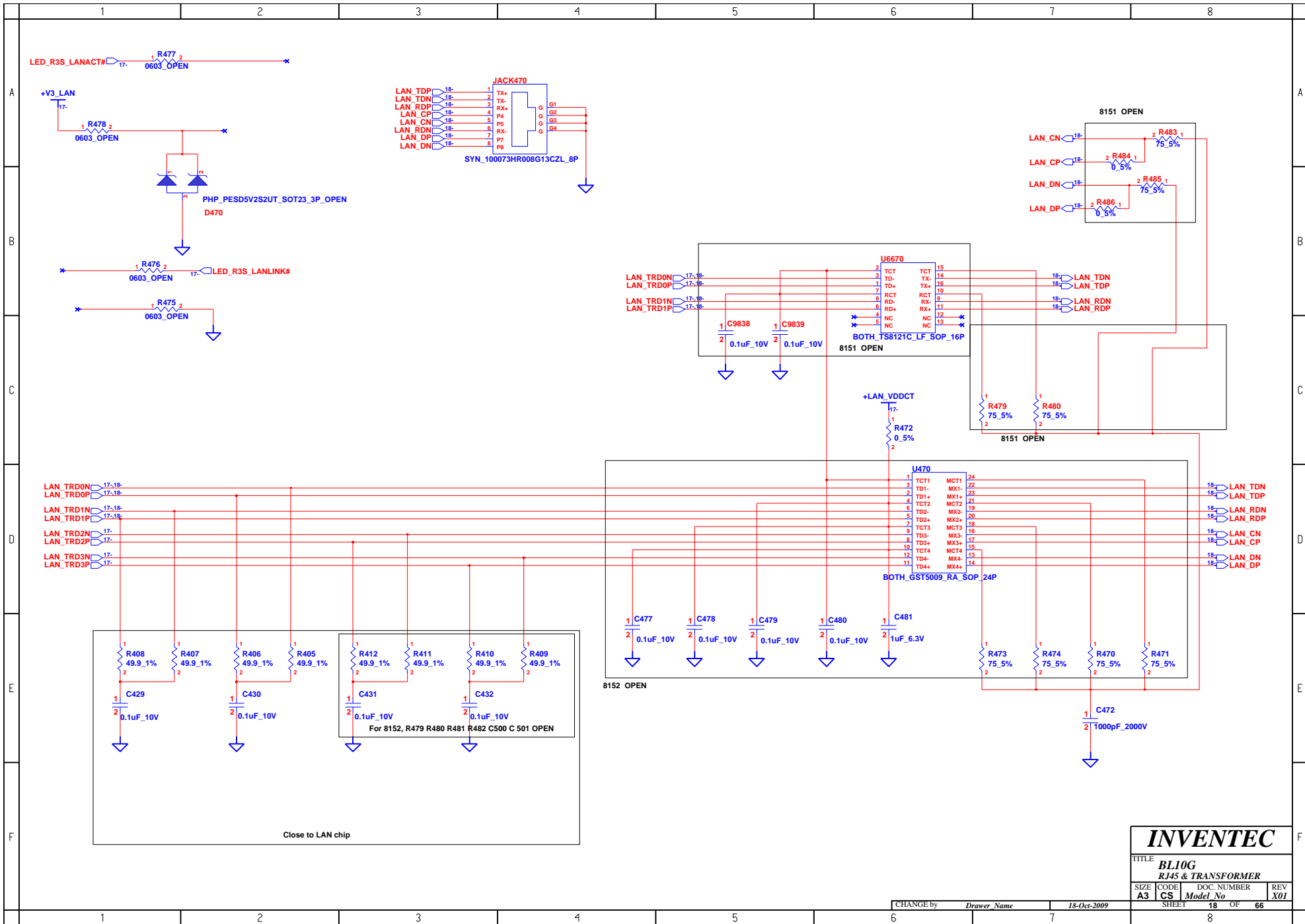
GM Thermal shutdown at 80.8°C +/-3°C from 60°C to 100°C
 PM Thermal shutdown at 86°C +/-3°C from 60°C to 100°C
 $RSET=0.0012*T^2 - 0.9308*T+96.147$
 Hysteresis is 30C



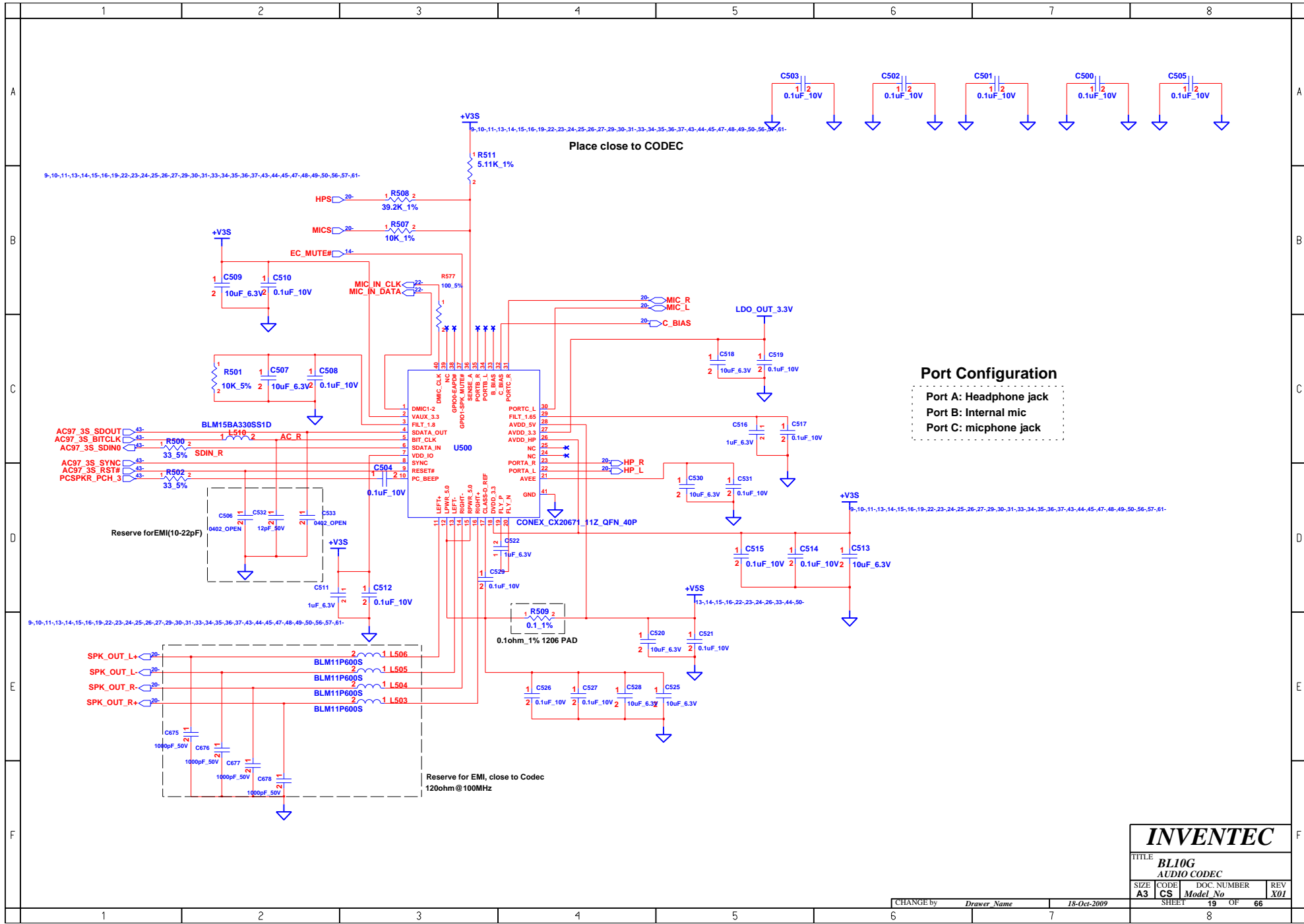
INVENTEC

TITLE			
BLI0G			
FAN,Thermal controller			
SIZE	CODE	DOC NUMBER	REV
A3	CS	Model No	X01
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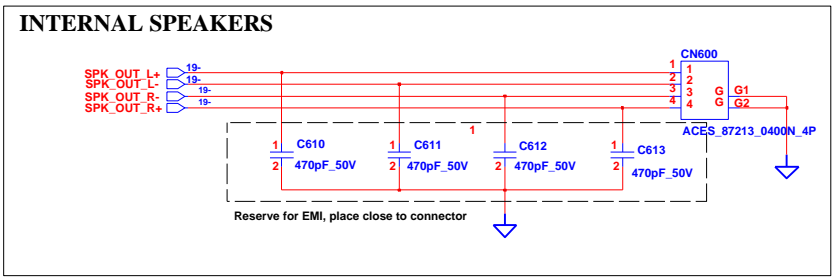


INVENTEC			
TITLE BL10G RJ45 & TRANSFORMER			
SIZE A3	CODE CS	DOC NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 18 OF 66

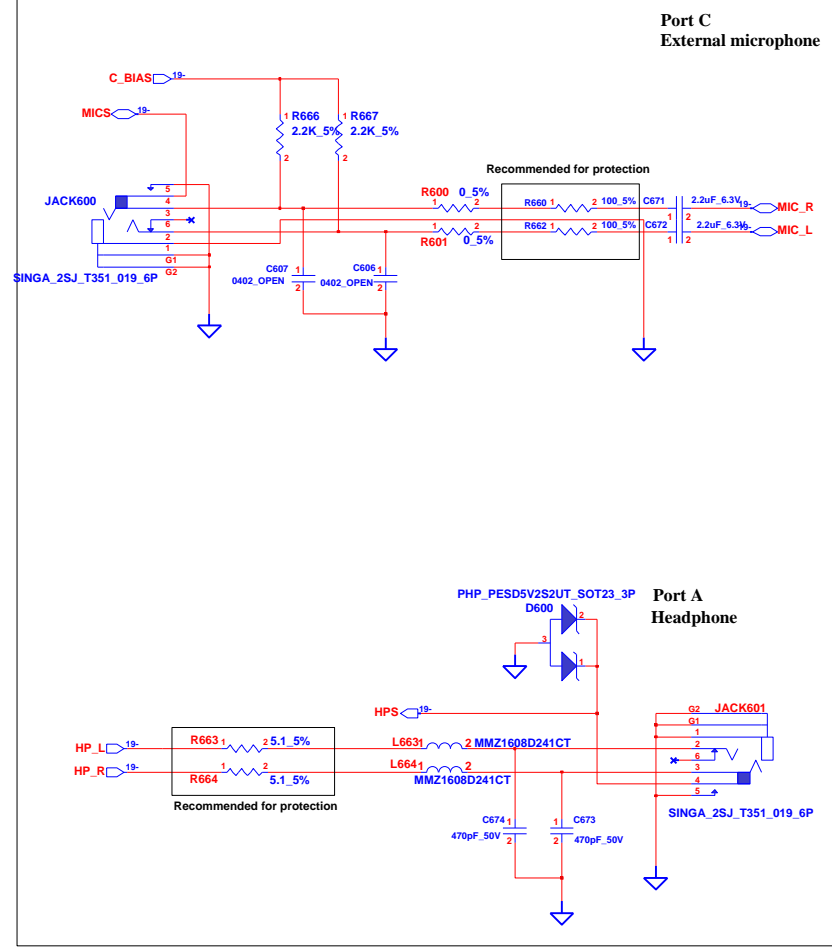


Port Configuration
 Port A: Headphone jack
 Port B: Internal mic
 Port C: microphone jack

INVENTEC			
TITLE BL10G AUDIO CODEC			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 19 OF 66



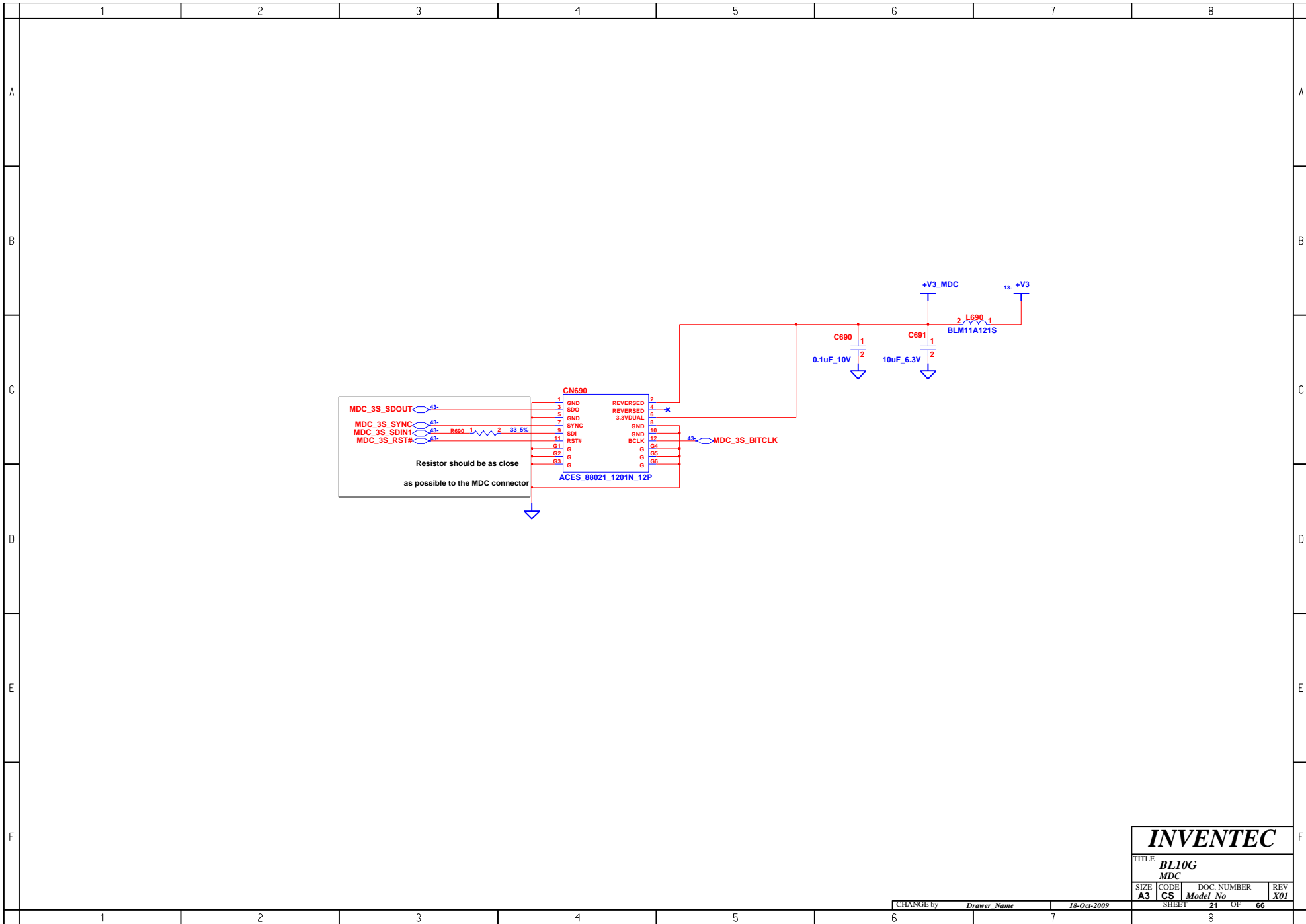
AUDIO JACKS



INVENTEC

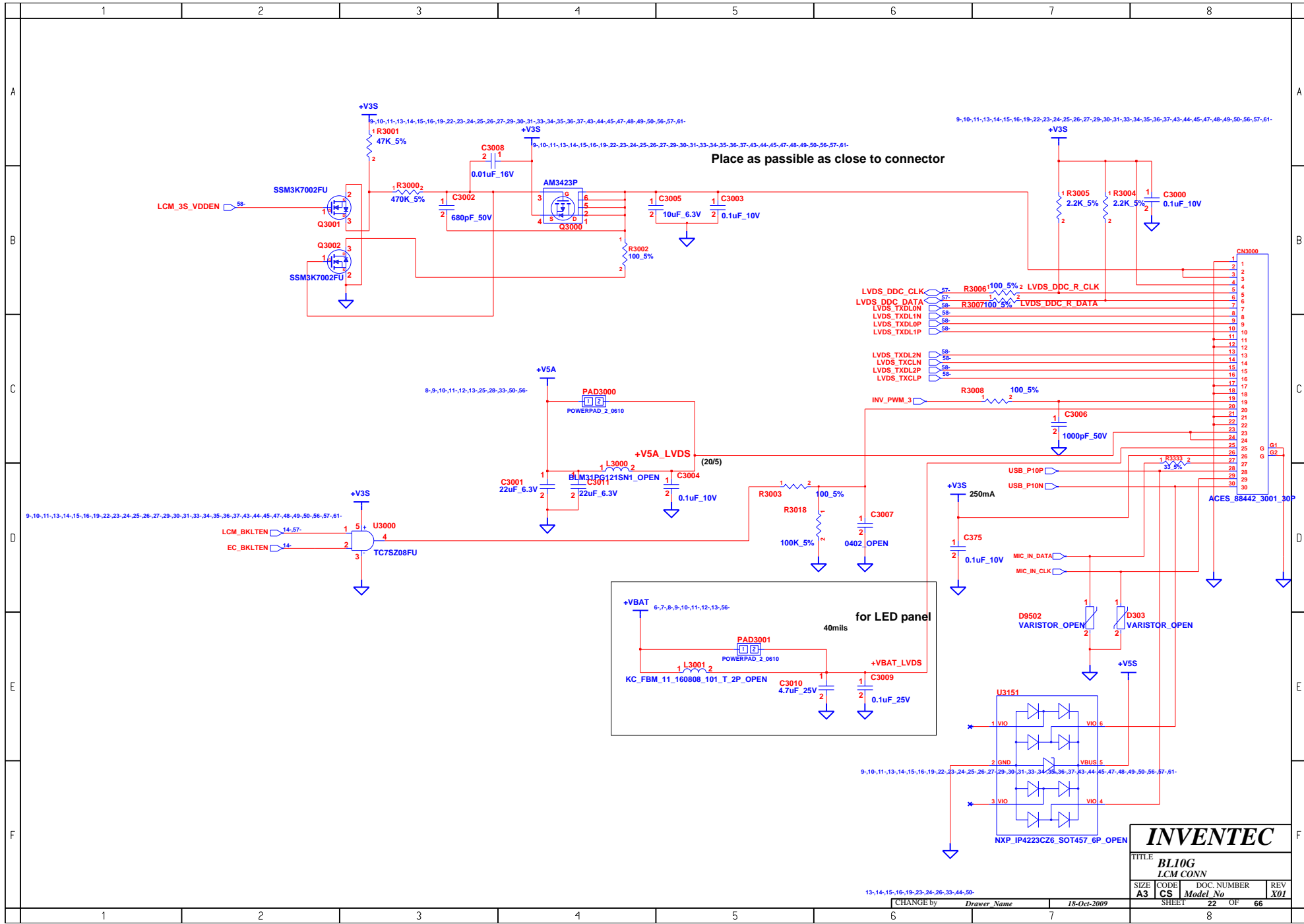
TITLE			
BLI0G			
AUDIO AMP&MIC&SPEAKER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	Model No	X01

CHANGE by	Drawer Name	18-Oct-2009	SHEET	20	OF	66
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INVENTEC

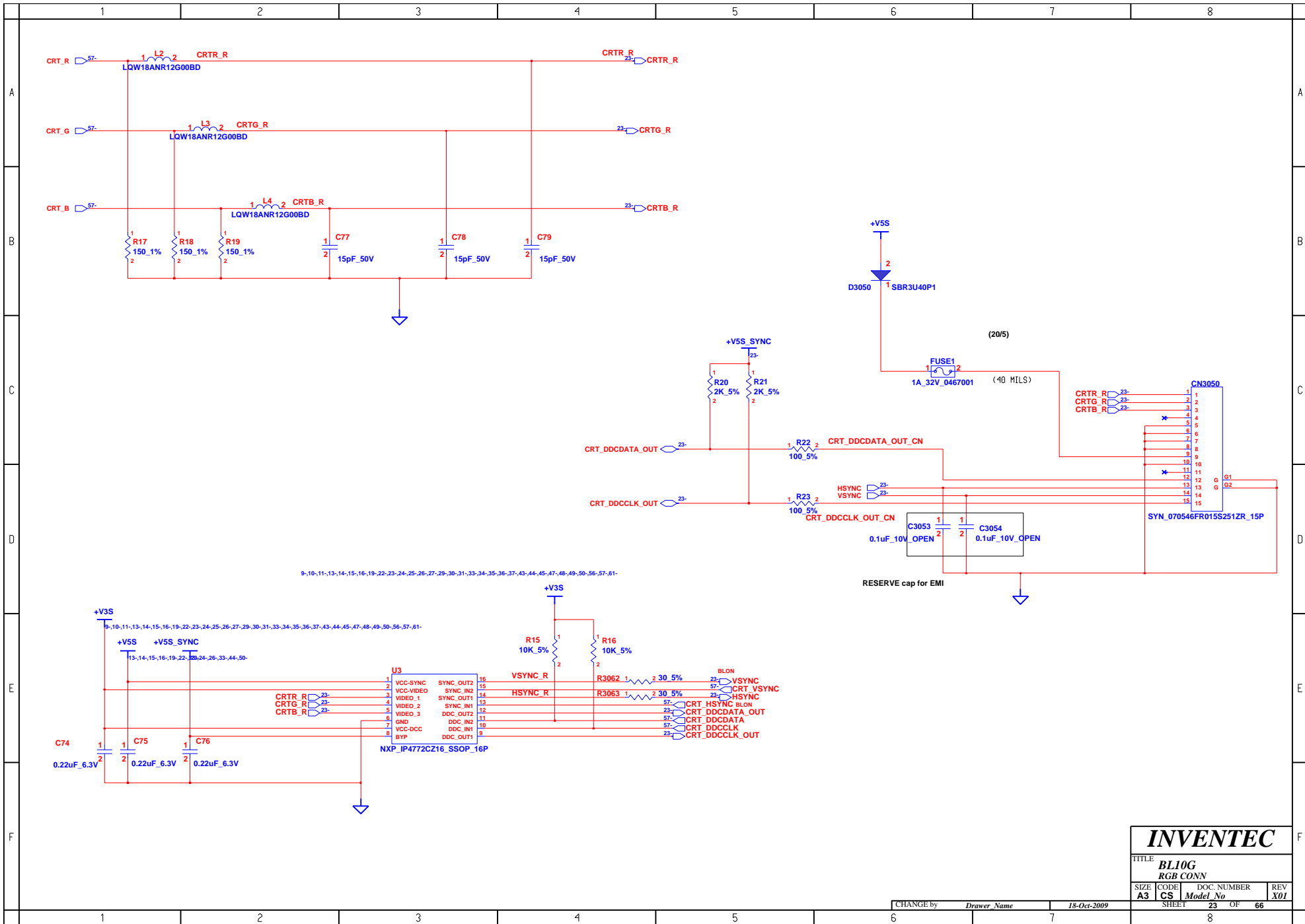
TITLE			
BL10G			
MDC			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	<i>Model_No</i>	X01



INVENTEC

TITLE			
BLI0G			
LCM CONN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	Model No	X01
CHANGE by		Drawer Name	18-Oct-2009
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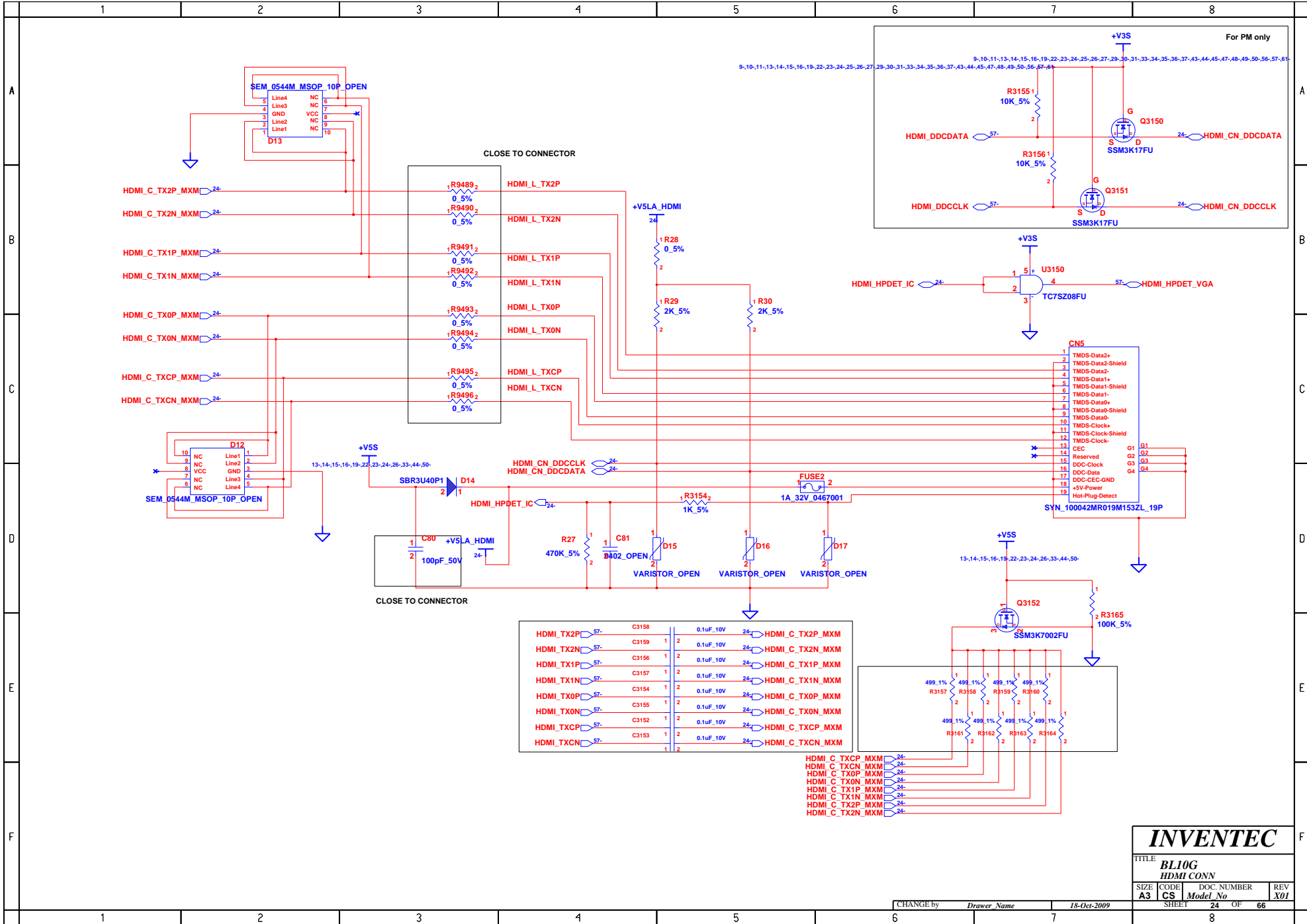
13,14,15,16,19,23,24,26,33,44,50-



INVENTEC

TITLE			
BLI0G			
RGB CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
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CHANGE by *Drawer Name* 18-Oct-2009



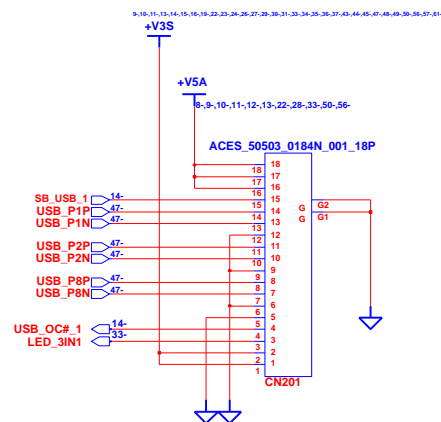
INVENTEC

TITLE
BL10G
HDMI CONN

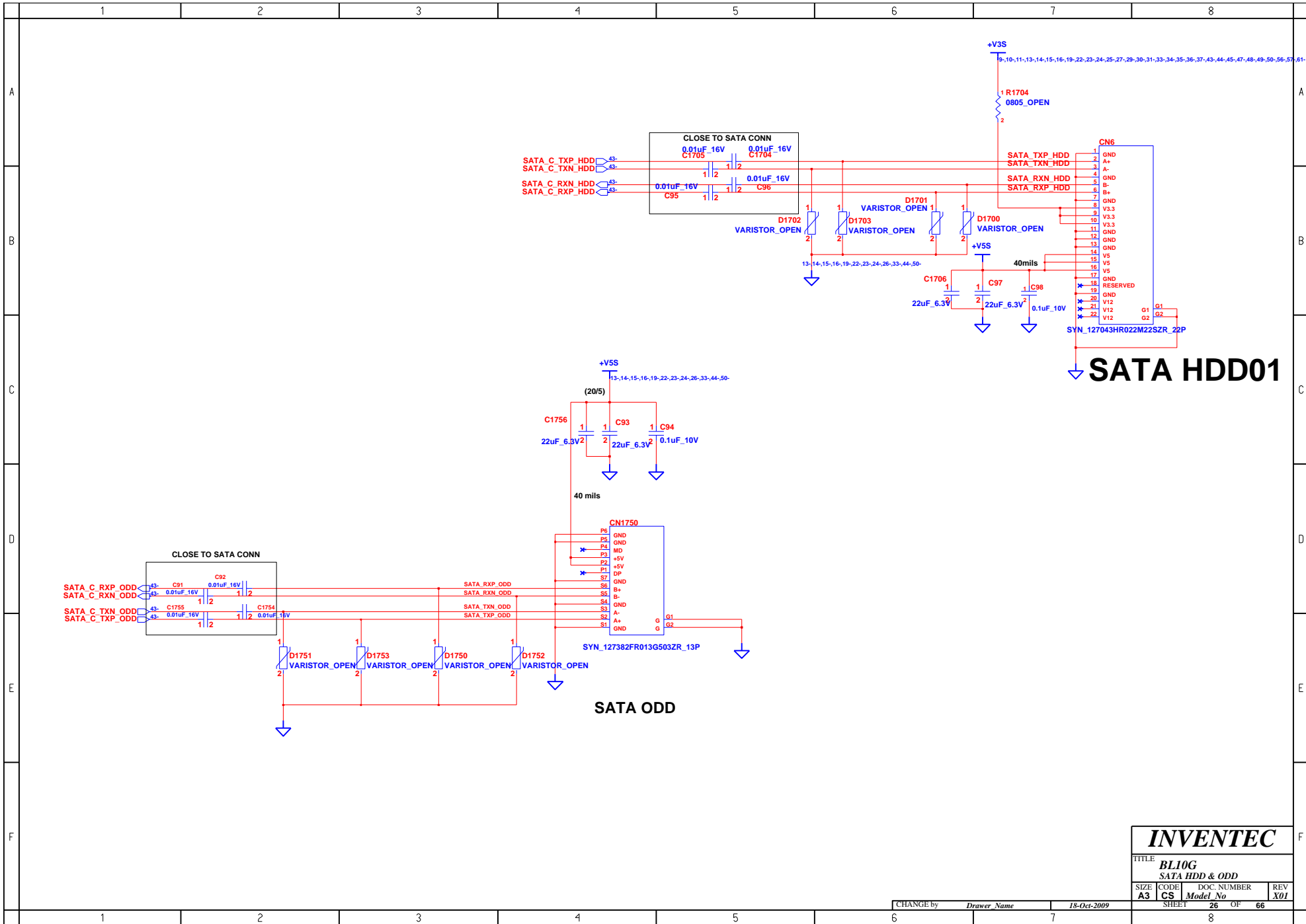
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by *Drawer Name* 18-Oct-2009
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CardReader/USB Connector



INVENTEC			
TITLE BL10G Cardreader			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
CHANGE by Drawer Name		18-Oct-2009	SHEET 25 OF 66
1	2	3	8



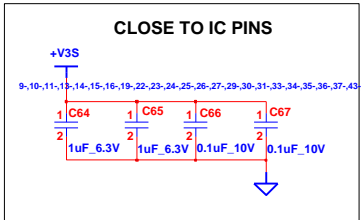
SATA HDD01

SATA ODD

INVENTEC

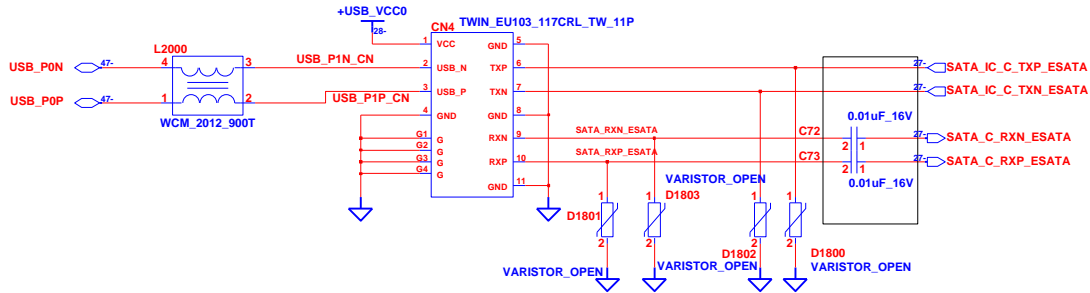
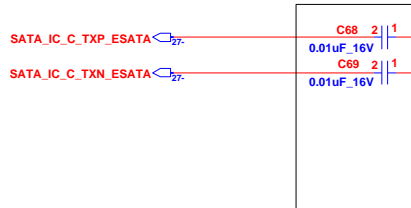
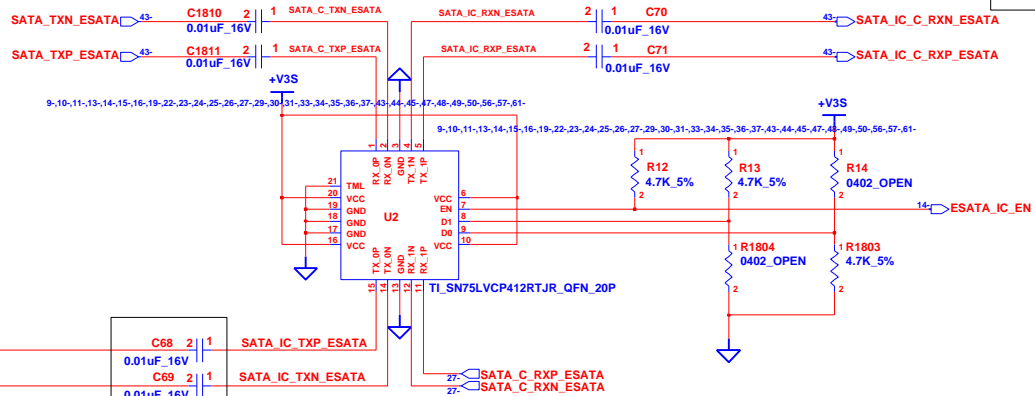
TITLE			
BLI0G			
SATA HDD & ODD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by	Drawer Name	18-Oct-2009	SHEET	26	OF	66
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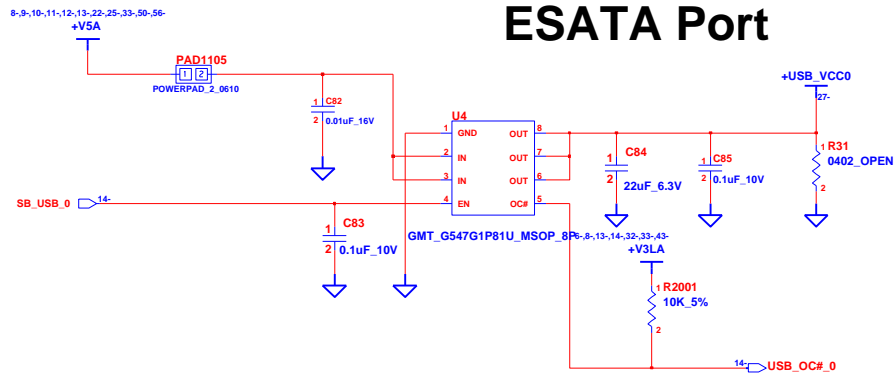
E-SATA

EN	D0	D1	FUNCTION
0	X	X	STANDBY
1	0	0	DEFAULT
1	1	0	CH0->5DB
1	0	1	CH1->5DB
1	1	1	CH0,1->5DB



INVENTEC			
TITLE BL10G E-SATA			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
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ESATA Port

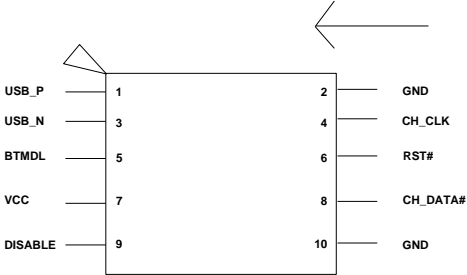
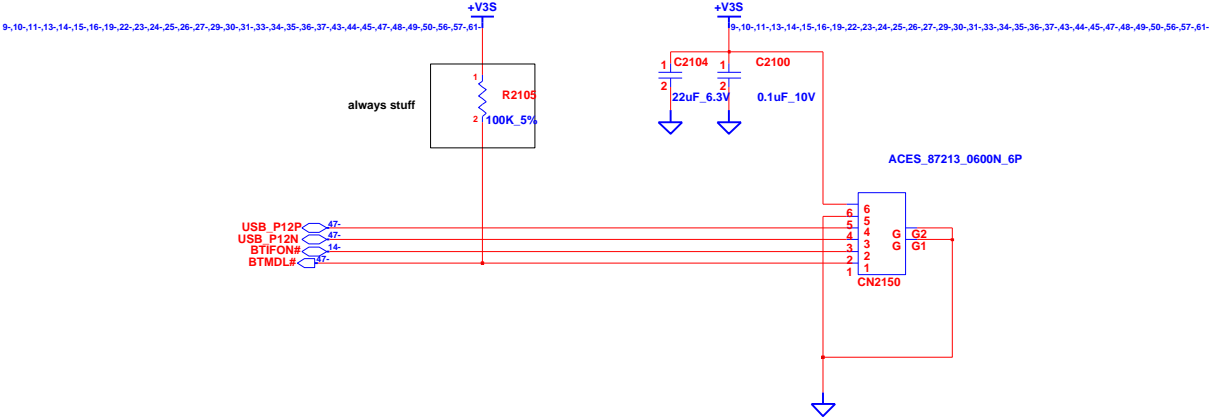


INVENTEC

TITLE			
BL10G			
USB CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
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CHANGE by *Drawer Name* 18-Oct-2009

BLUETOOTH



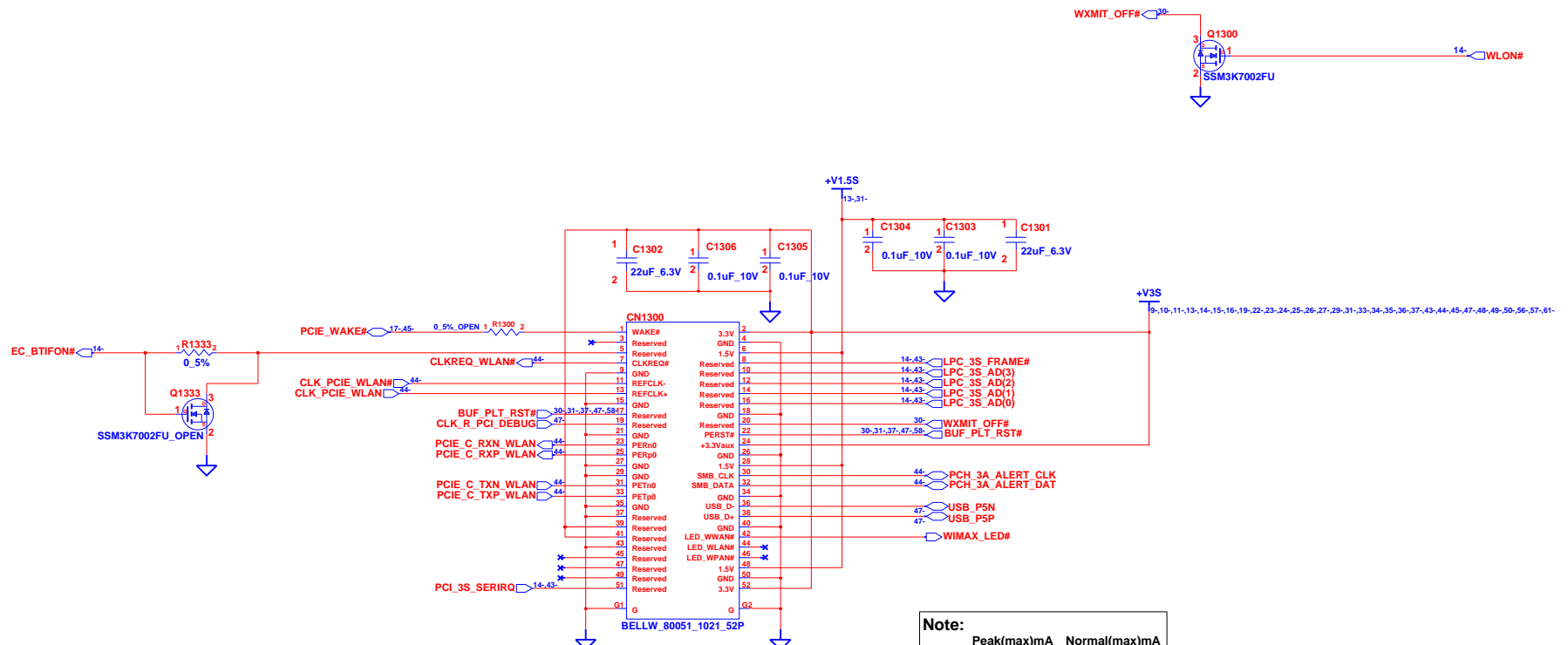
INVENTEC

TITLE
BL10G
Bluetooth

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
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CHANGE by *Drawer Name* 18-Oct-2009 SHEET 29 OF 66

Wireless & Debug card



Note:

	Peak(max)mA	Normal(max)mA
3.3V	2,750mA	1,100mA
1.5V	500mA	375mA

MINI CARD 1

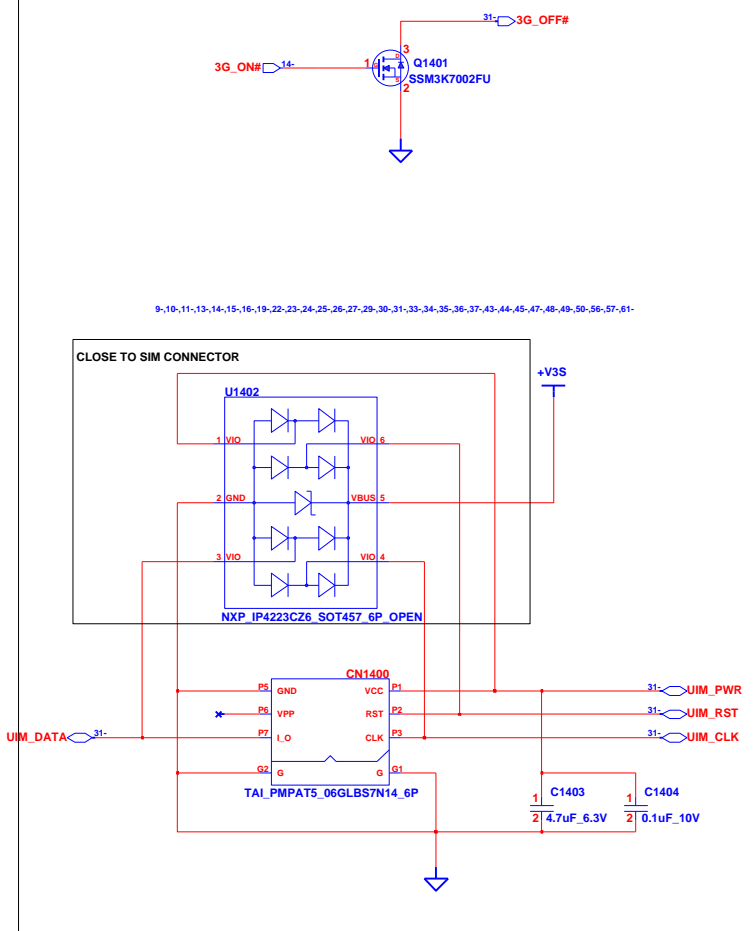
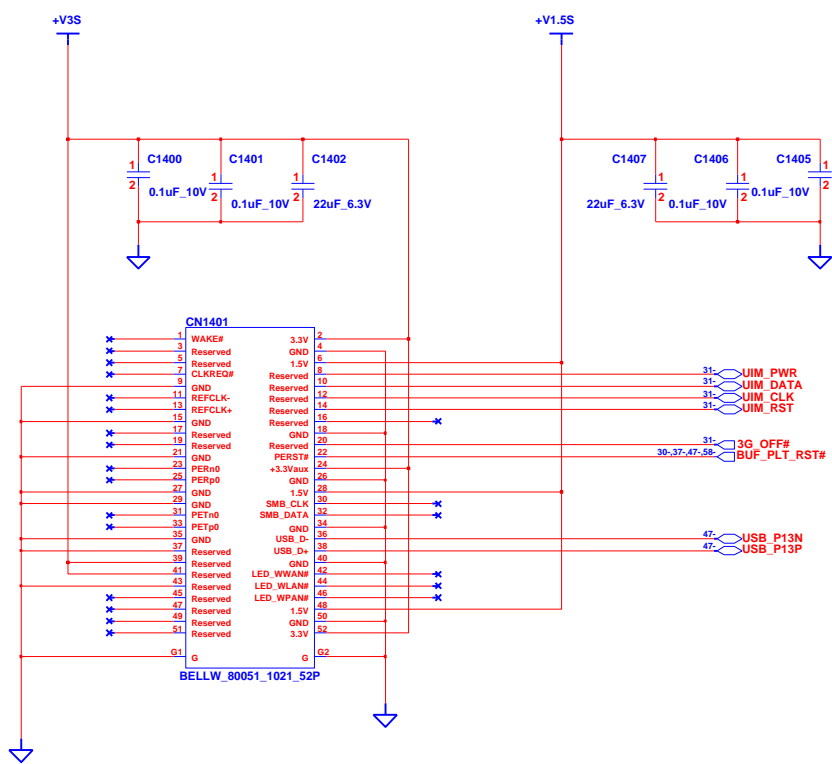
INVENTEC

TITLE			
BL10G WLAN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
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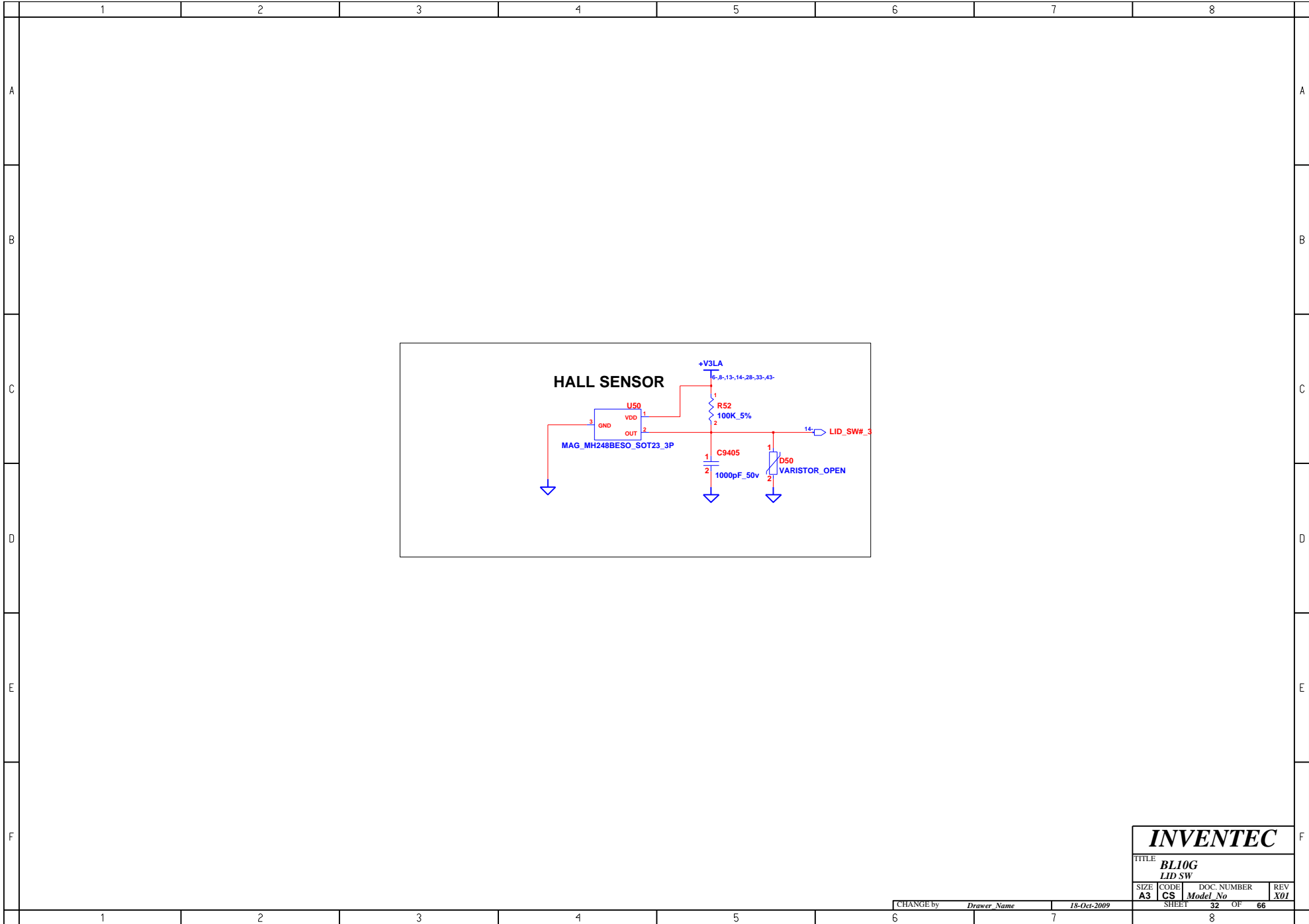
3G/GPS

9-10-11-13-14-15-16-19-22-23-24-25-26-27-29-30-31-33-34-35-36-37-43-44-45-47-48-49-50-56-57-61- 13-30



MINI CARD 2

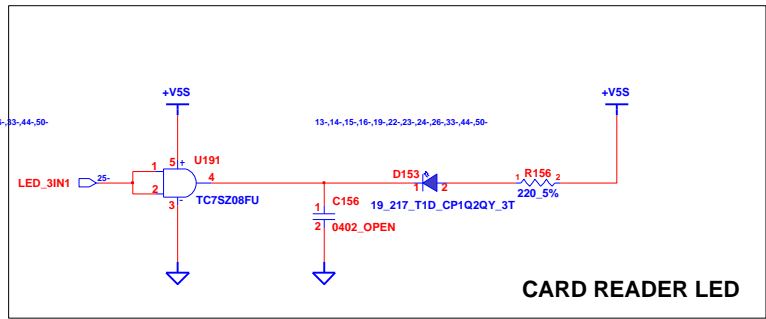
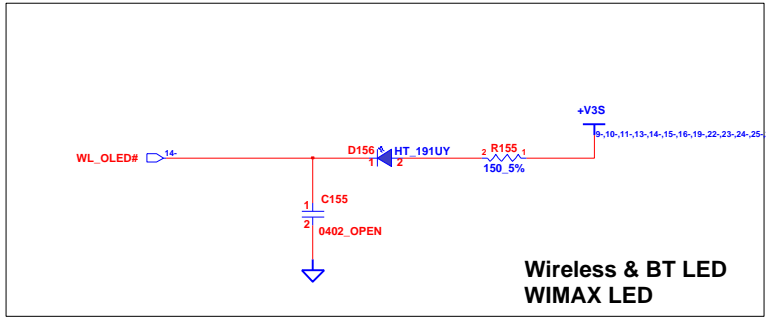
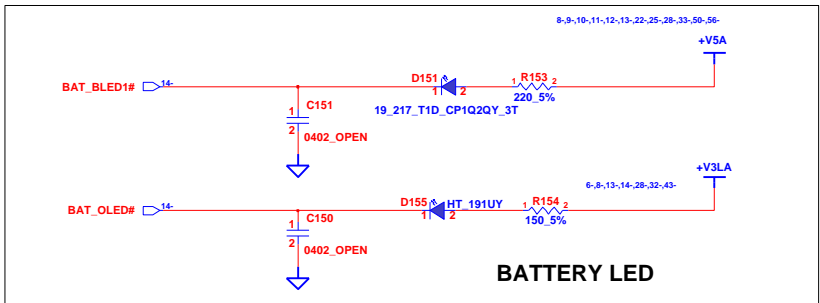
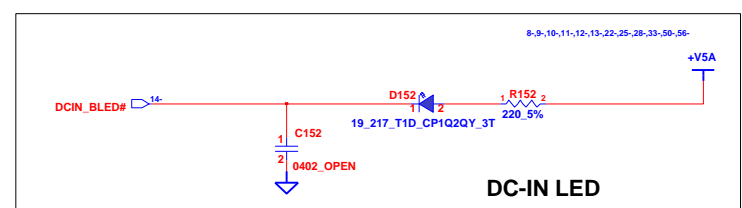
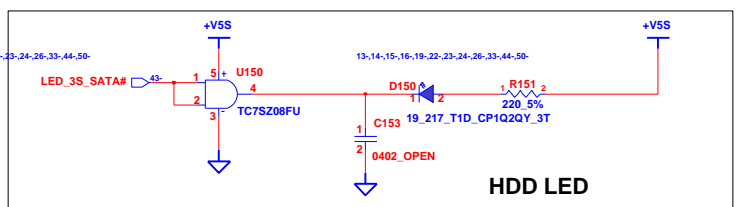
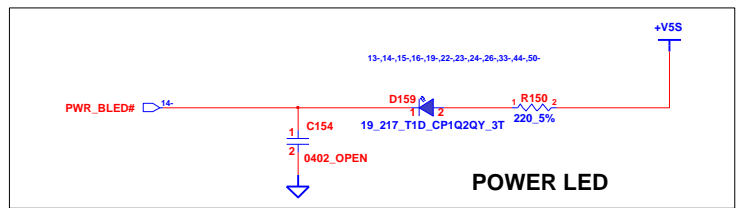
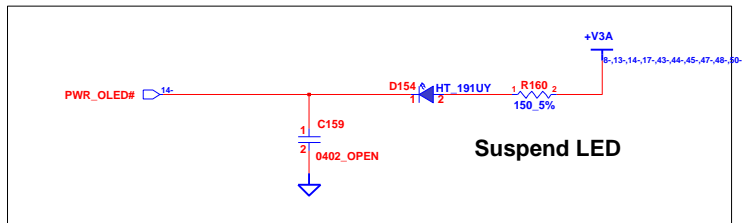
INVENTEC			
TITLE BL10G			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 31 OF 66



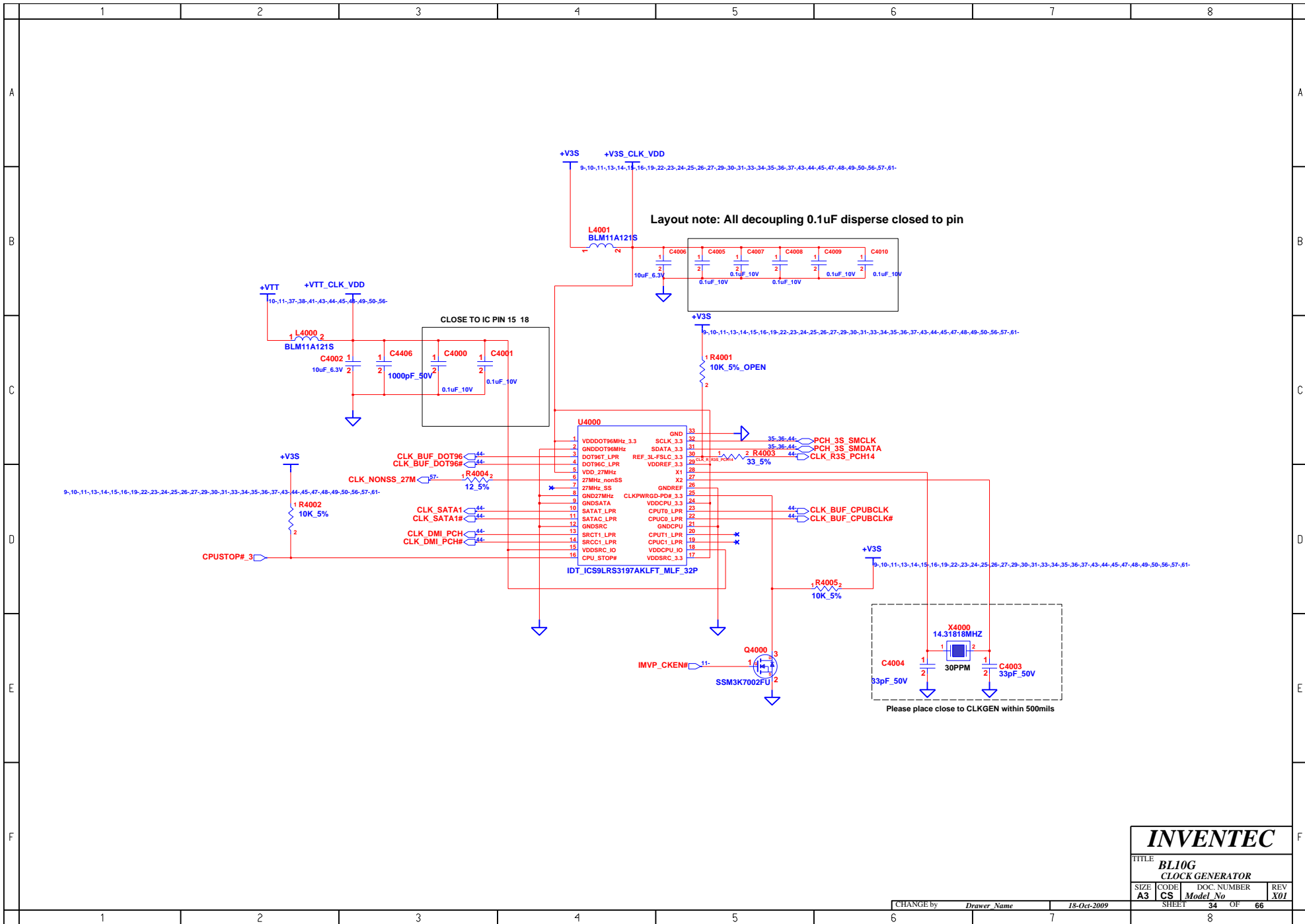
INVENTEC

TITLE			
BL10G			
LID SW			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
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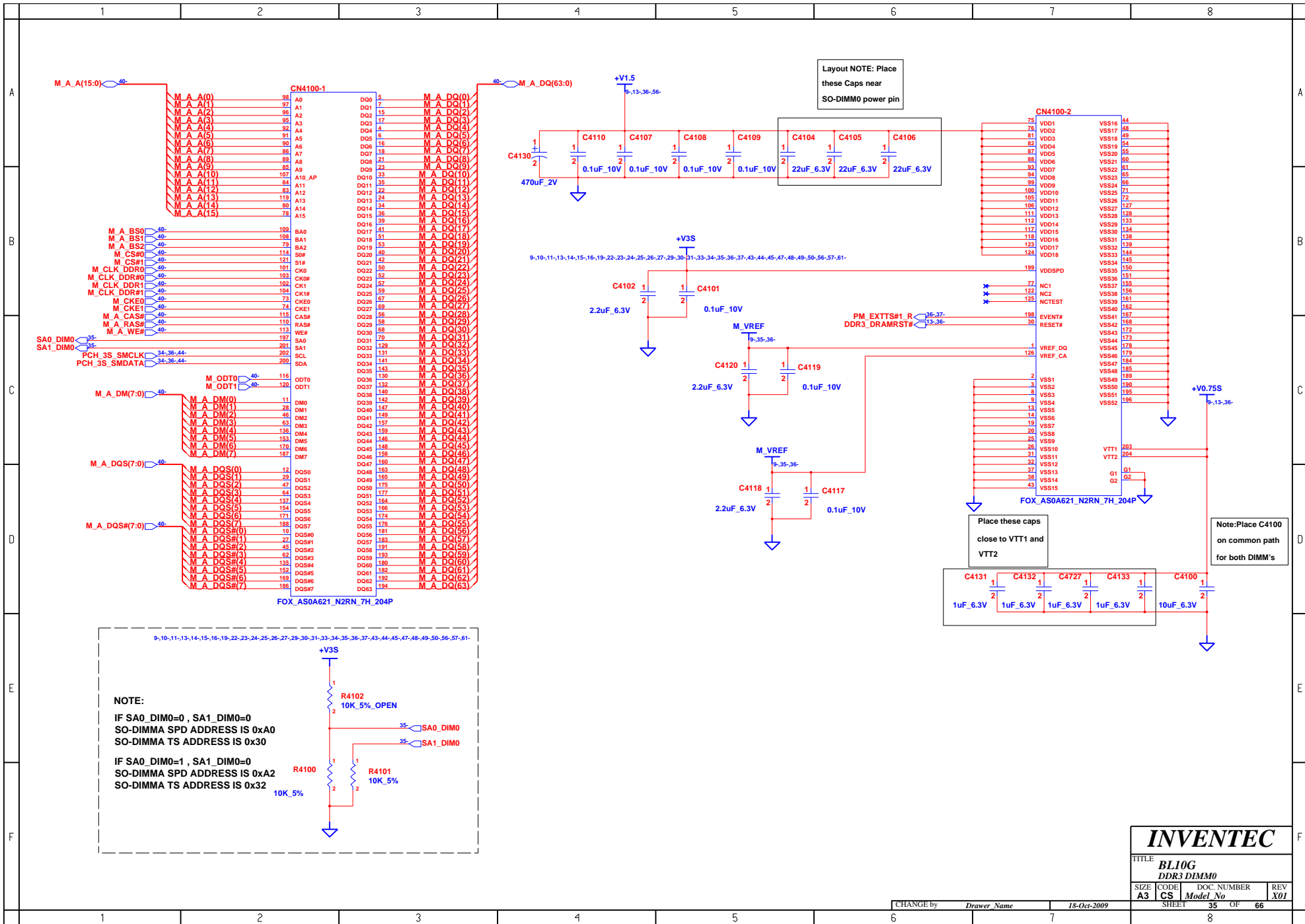
CHANGE by *Drawer Name* 18-Oct-2009



INVENTEC			
TITLE BL10G			
LED			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
CHANGE by		Drawer Name	18-Oct-2009
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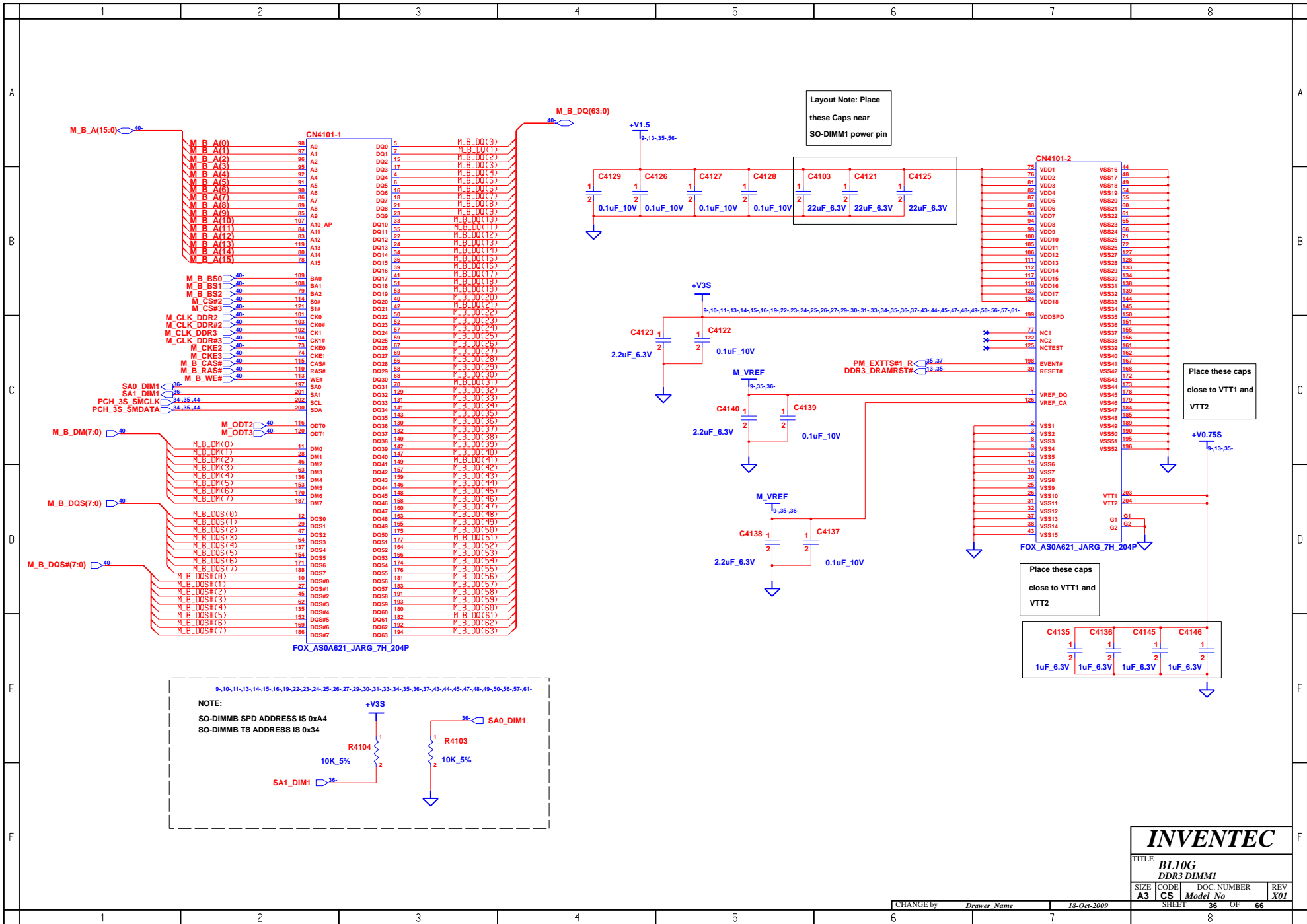


INVENTEC			
TITLE BLI0G			
CLOCK GENERATOR			
SIZE A3	CODE CS	DOC NUMBER <i>Model No</i>	REV X01
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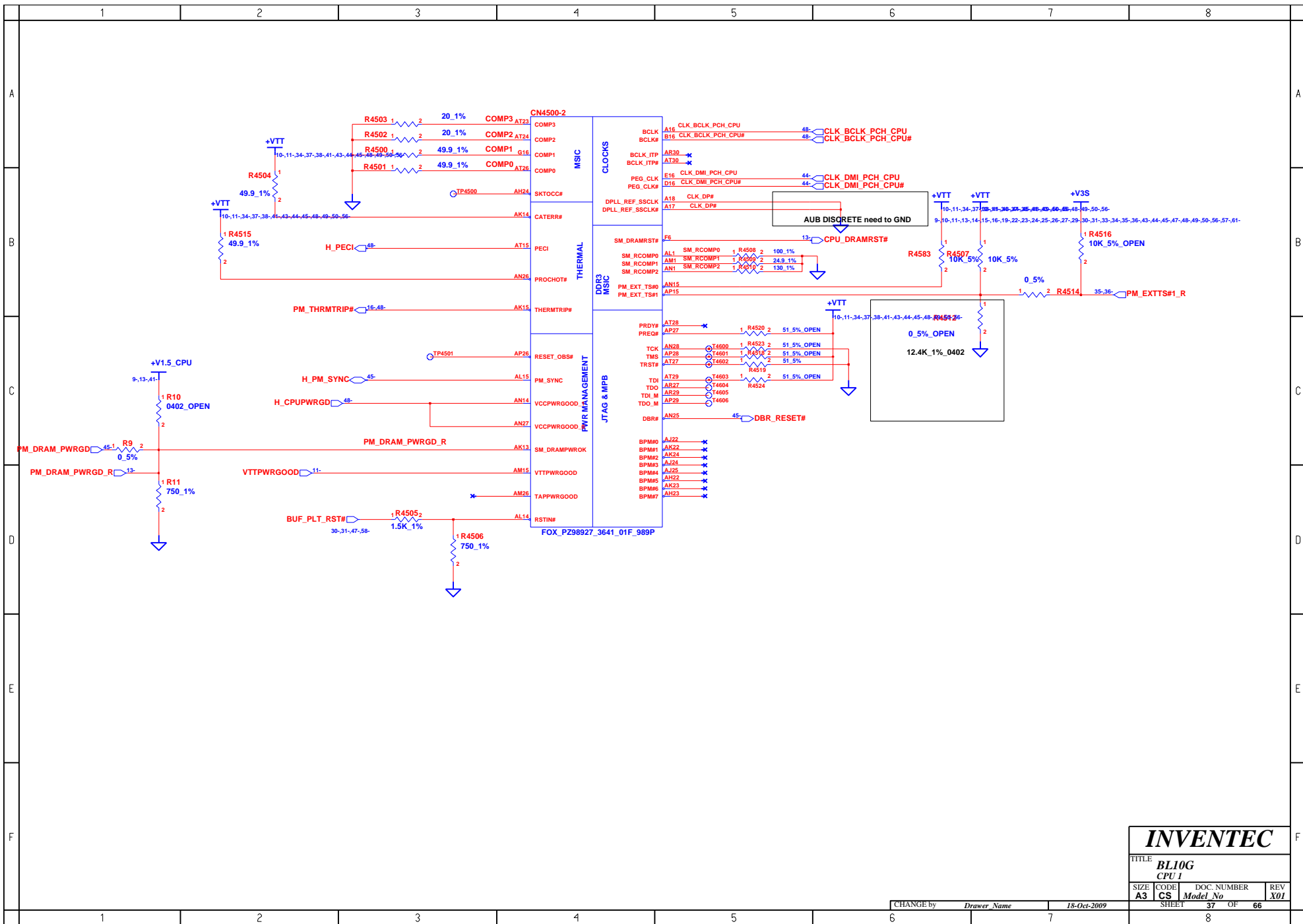


INVENTEC

TITLE			
BLI0G			
DDR3 DIMM0			
SIZE	CODE	DOC. NUMBER	REV
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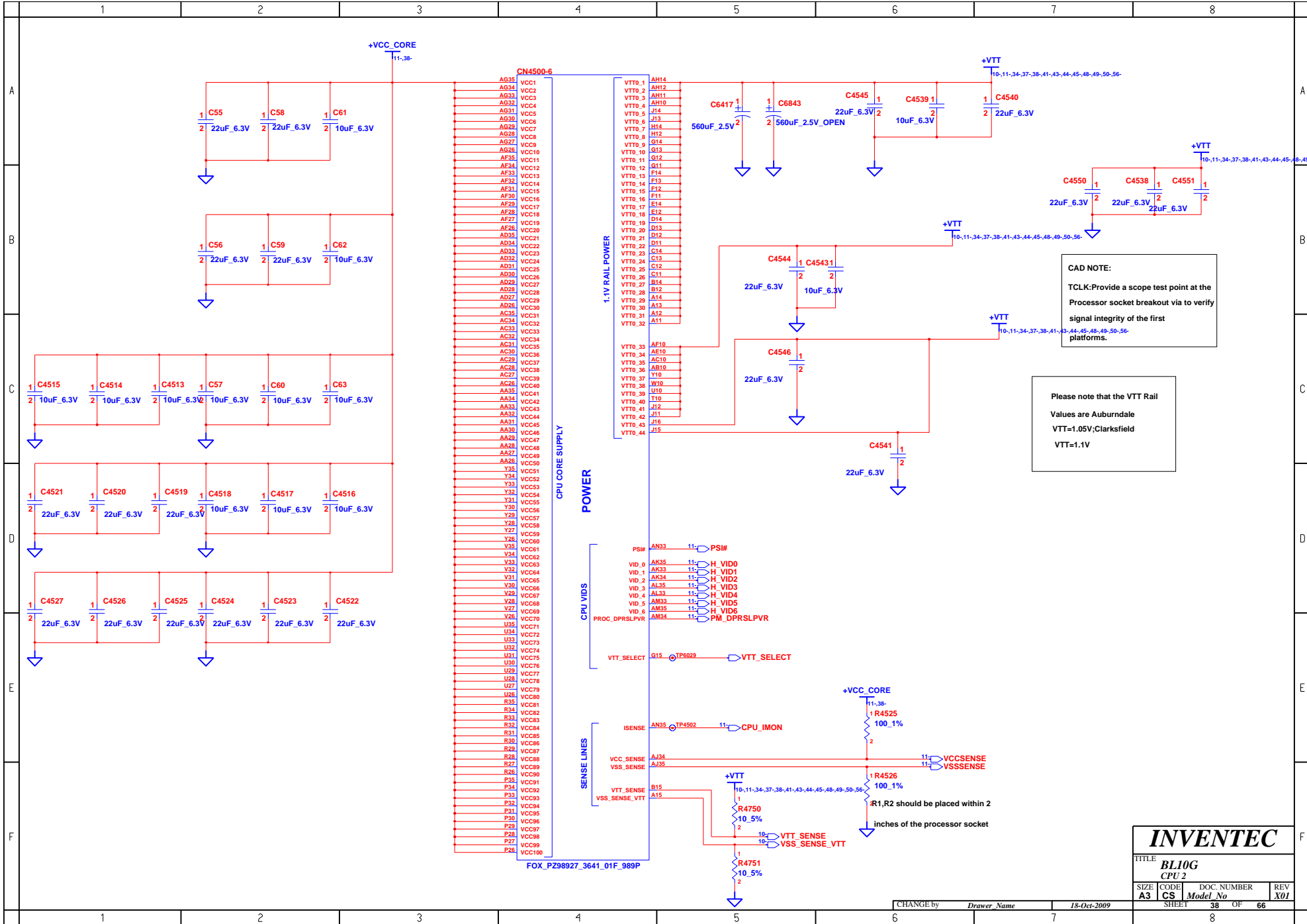
INVENTEC			
TITLE BLI0G DDR3 DIMM1			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	
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INVENTEC

TITLE			
BL10G CPU 1			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

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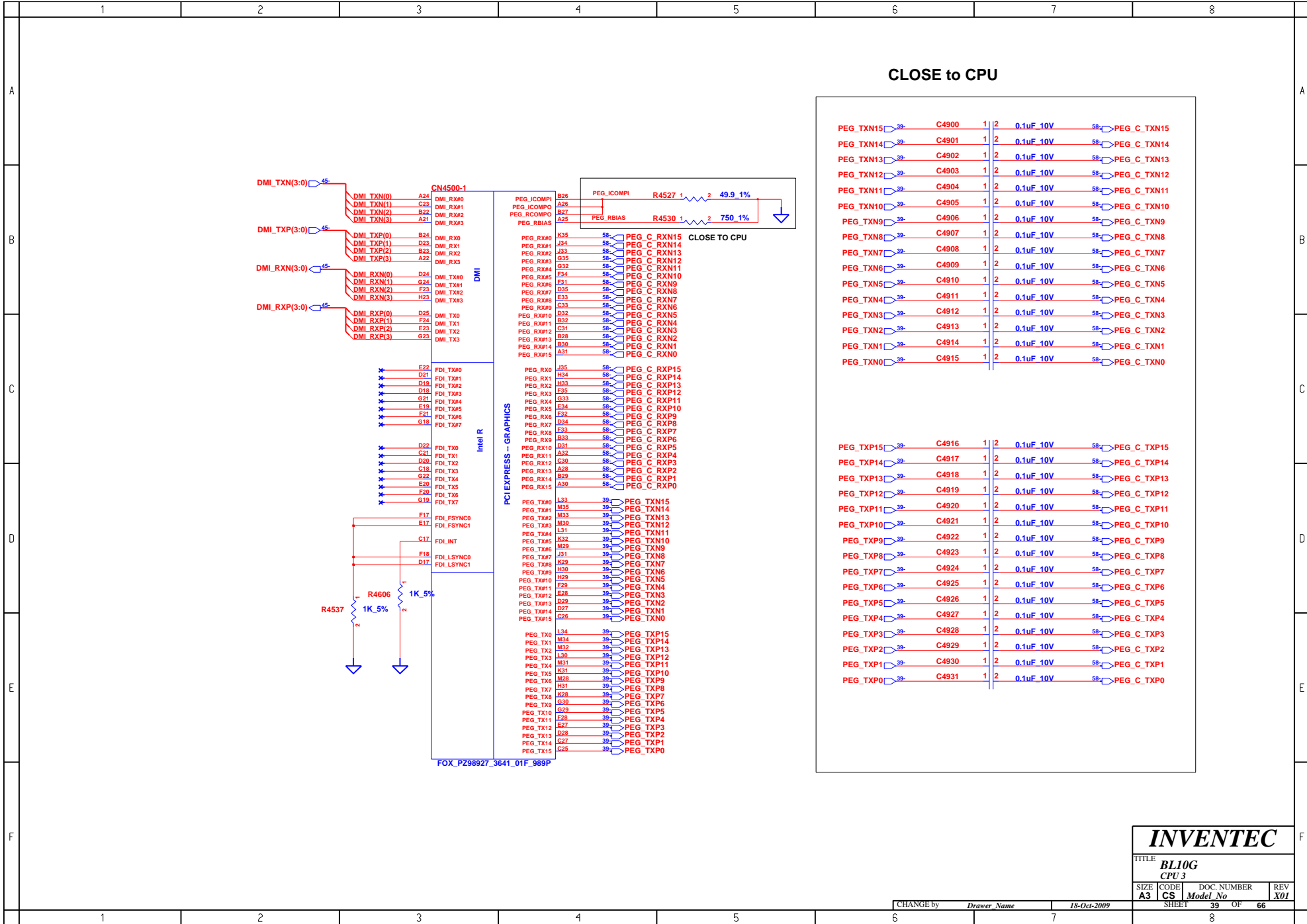
CAD NOTE:
 TCLK: Provide a scope test point at the Processor socket breakout via to verify signal integrity of the first platforms.

Please note that the VTT Rail Values are Auburndale
 VTT=1.05V; Clarksfield
 VTT=1.1V

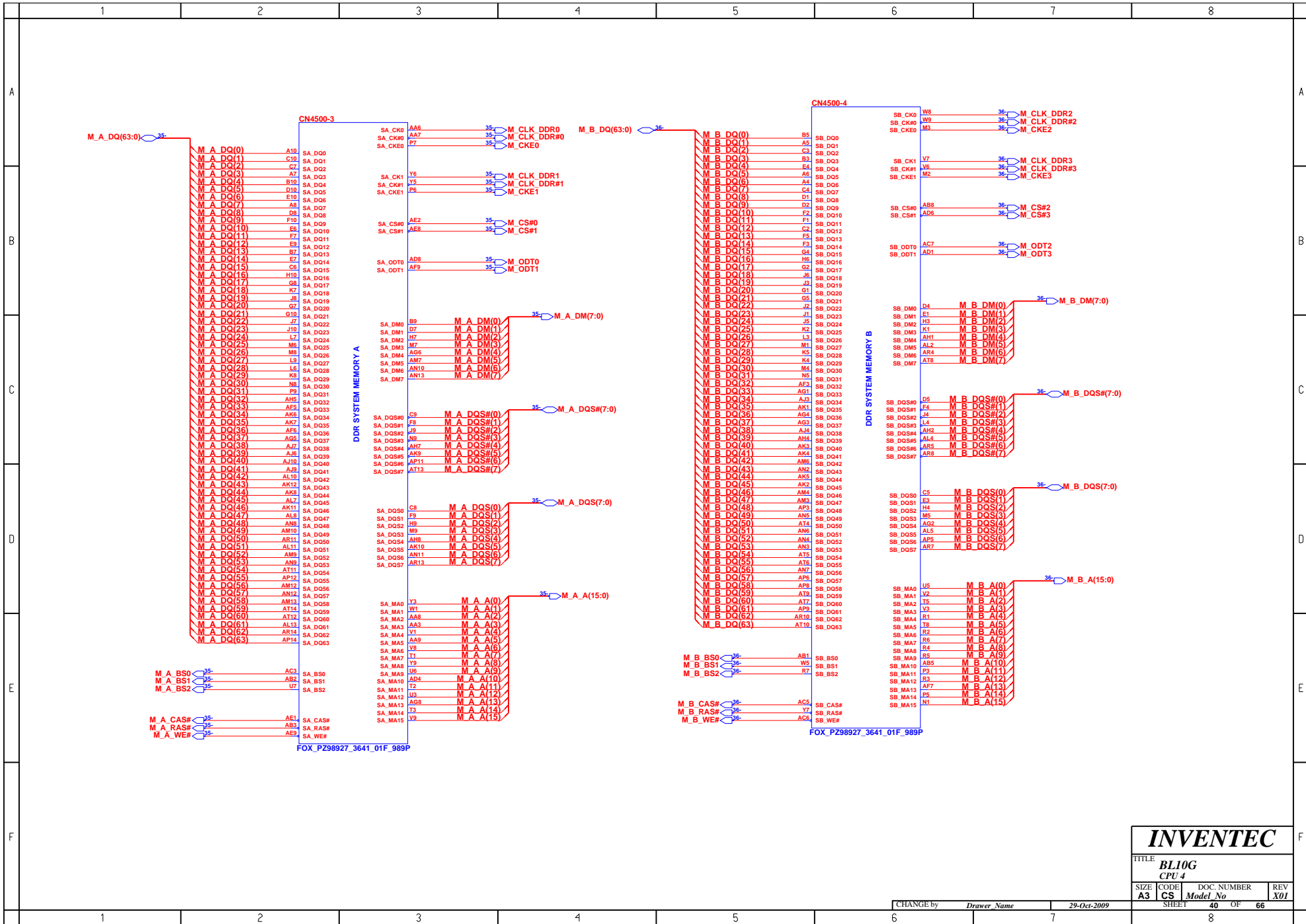
INVENTEC

TITLE
BLI0G
CPU 2

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
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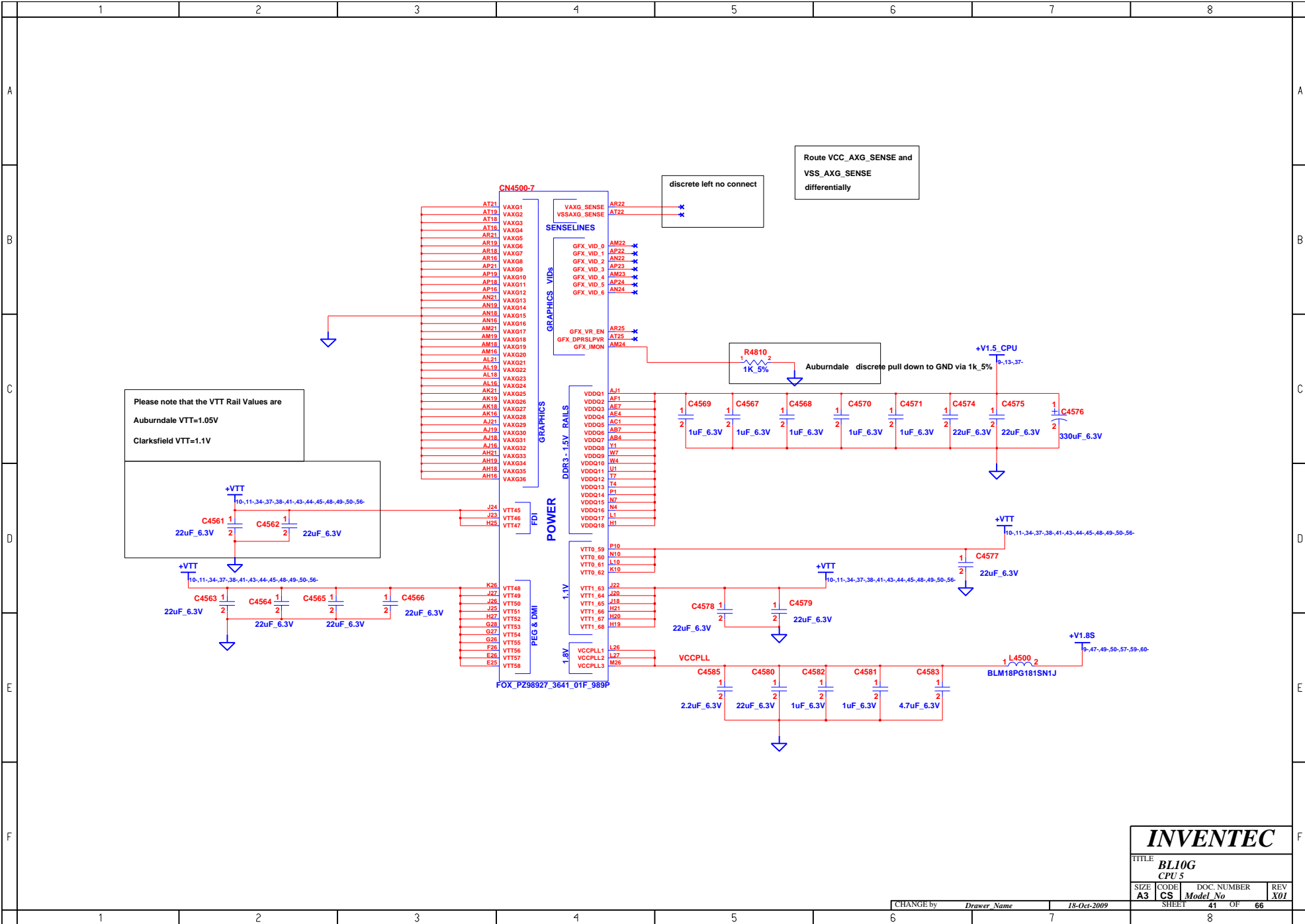
INVENTEC				
TITLE BLI0G CPU 3				
SIZE	CODE	DOC. NUMBER	REV	
A3	CS	Model No	X01	
CHANGE by		Drawer Name	18-Oct-2009	
SHEET			39	OF 66



INVENTEC

TITLE
BLI0G
CPU 4

SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
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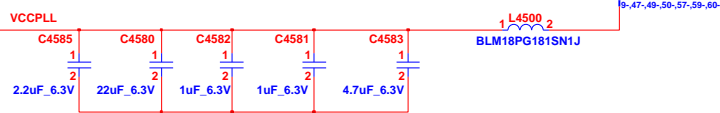
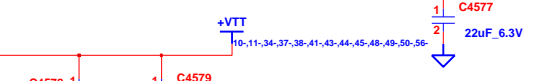
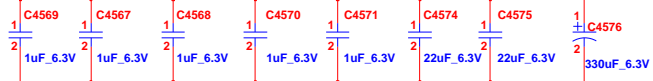
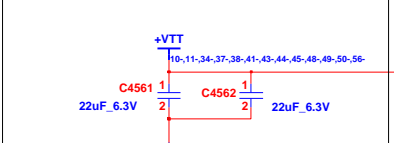


Please note that the VTT Rail Values are
 Auburndale VTT=1.05V
 Clarksfield VTT=1.1V

Route VCC_AXG_SENSE and
 VSS_AXG_SENSE
 differentially

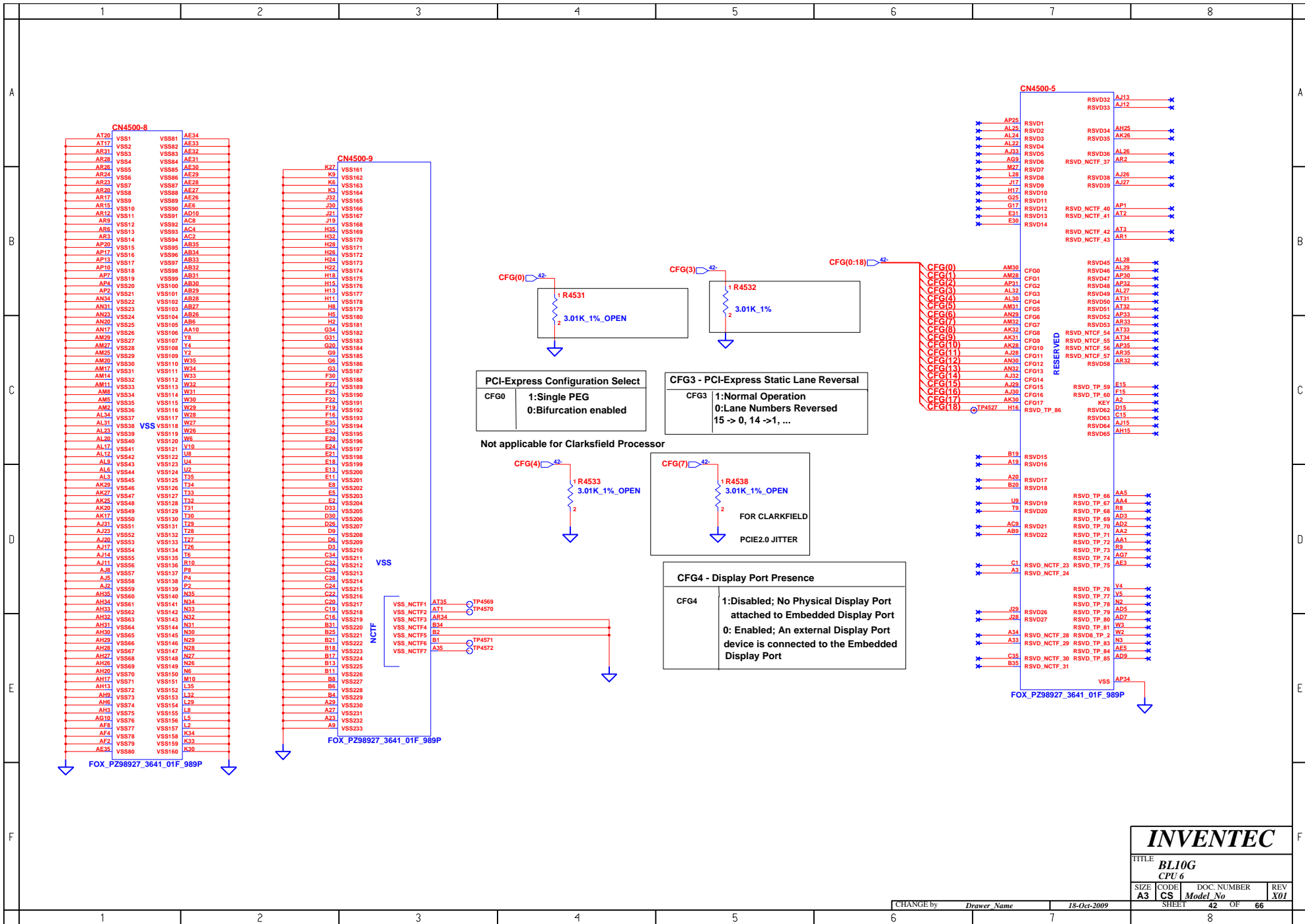
discrete left no connect

R4810
 1K_5%
 Auburndale discrete pull down to GND via 1k_5%

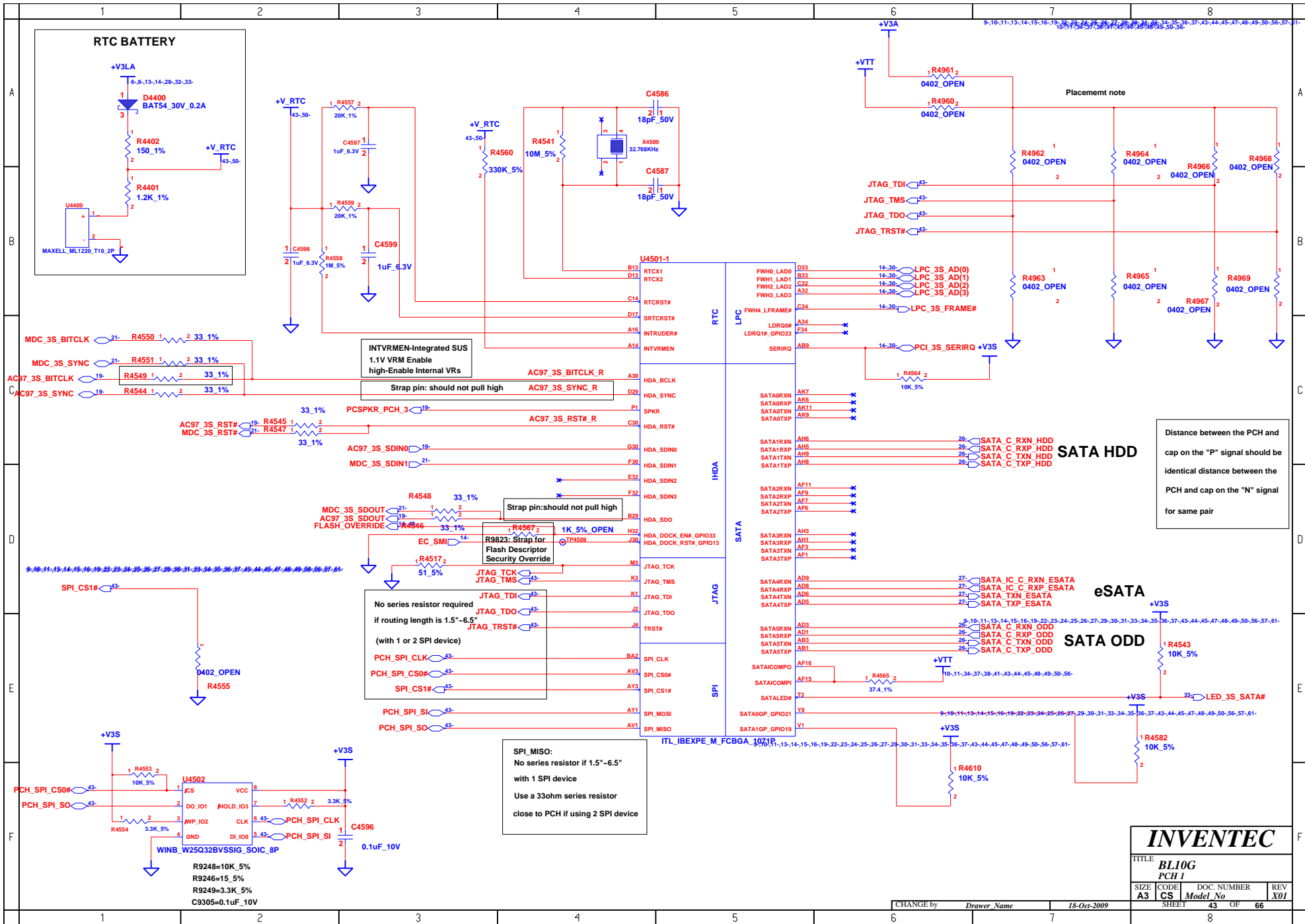


INVENTEC

TITLE			
BL10G			
CPU 5			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01



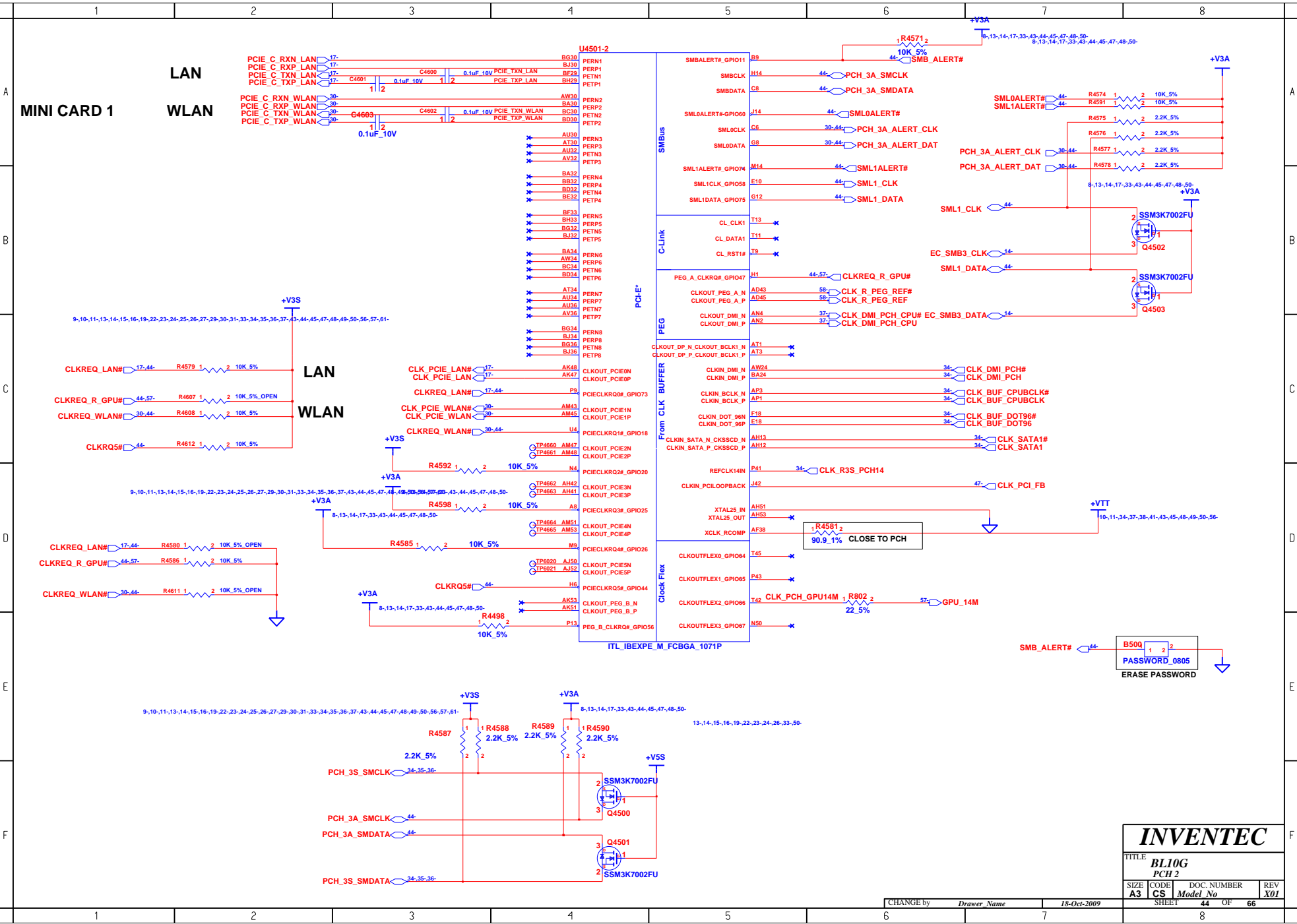
INVENTEC				
TITLE BLI0G CPU 6				
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01	
CHANGE by <i>Drawer Name</i>		18-Oct-2009		
SHEET 42 OF 66				



INVENTEC				
TITLE BLI0G PCH 1				
SIZE A3	CODE CS	DOC NUMBER <i>Model No</i>	REV X01	
CHANGE by <i>Drawer Name</i>		18-Oct-2009		
SHEET 43 OF 66			8	

MINI CARD 1

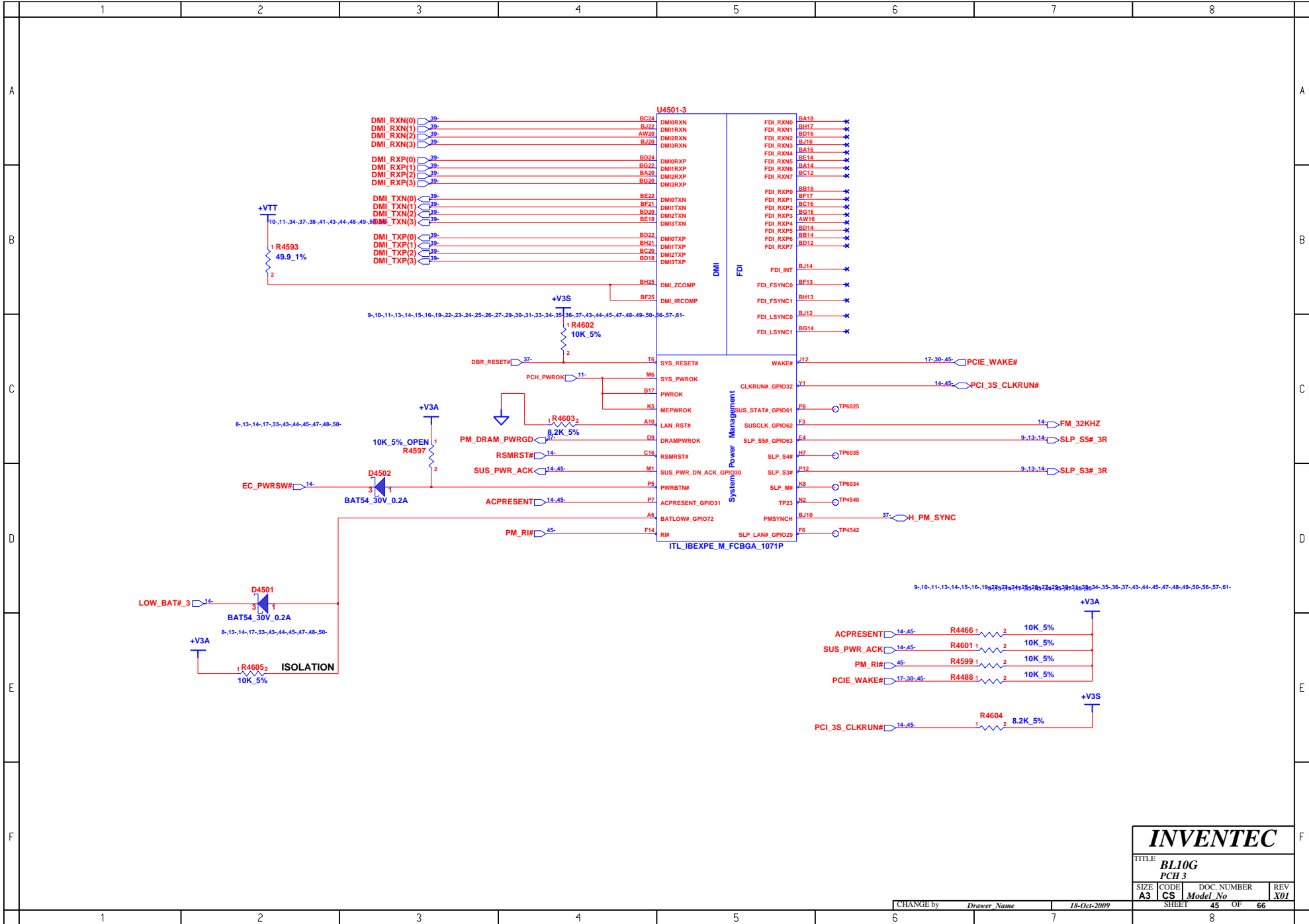
**LAN
WLAN**



INVENTEC

TITLE			
BL10G			
PCH 2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		44	OF 66

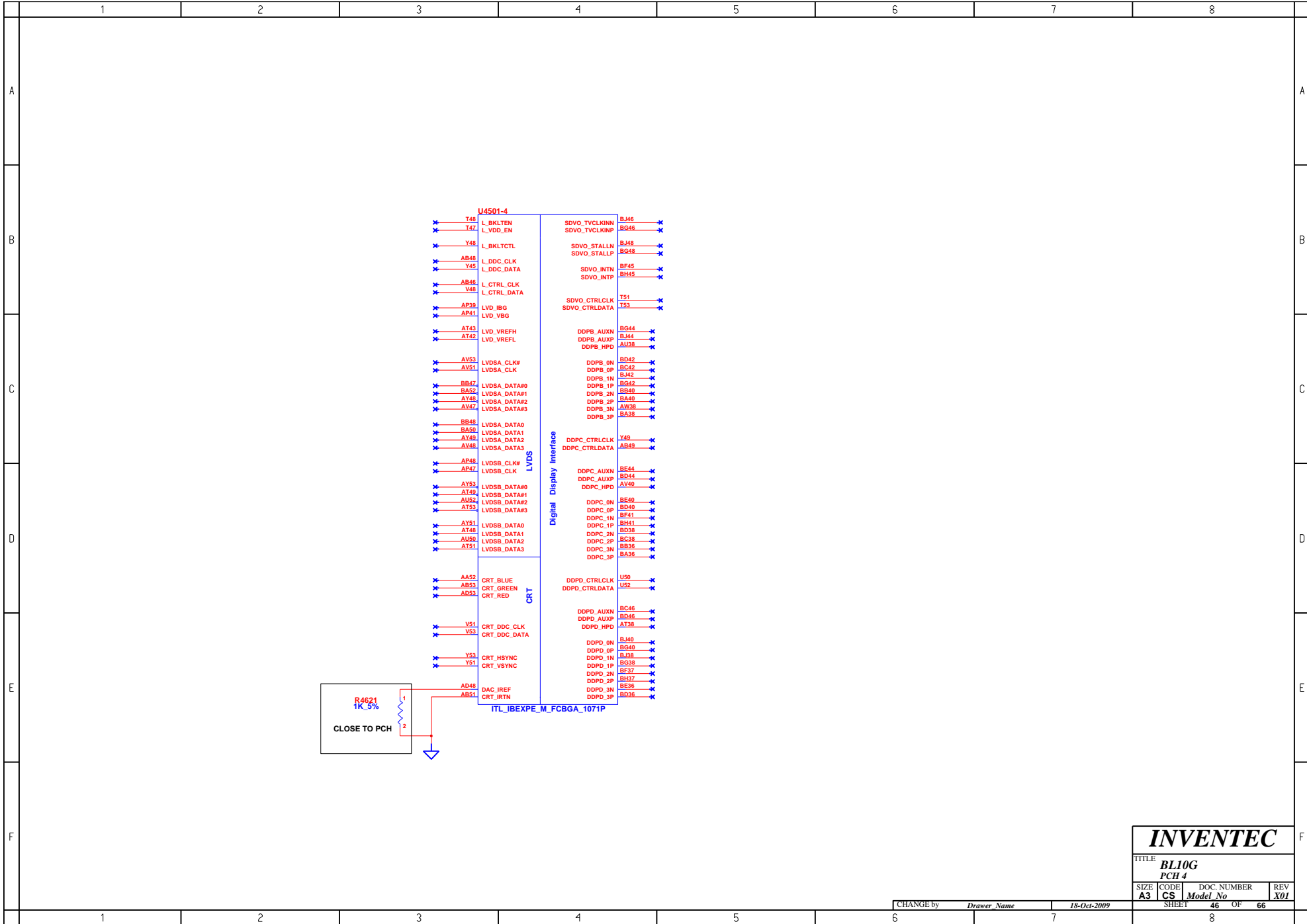
CHANGE by *Drawer Name* 18-Oct-2009



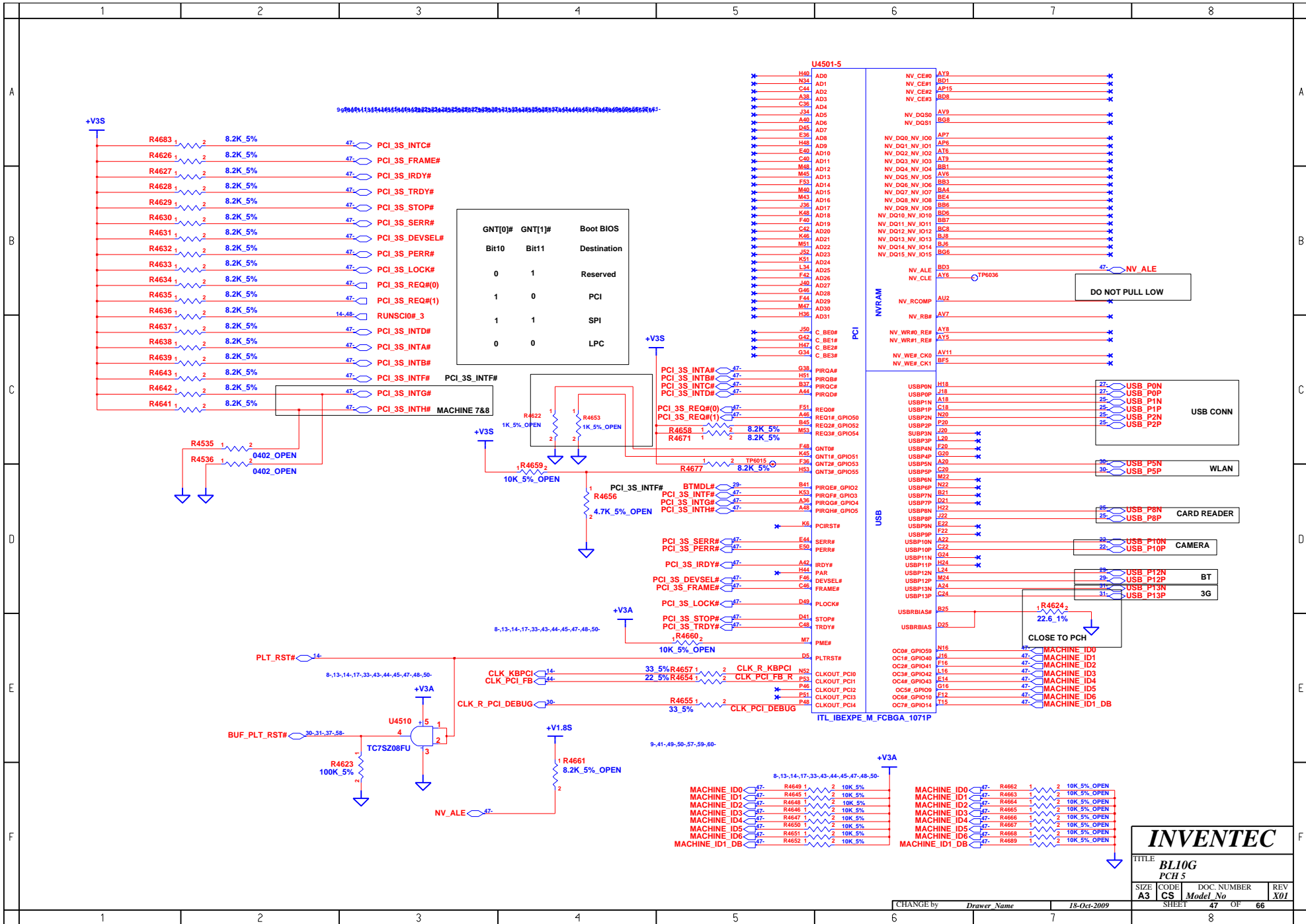
INVENTEC

TITLE			
BLI0G			
PCH 3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

CHANGE by	Drawer Name	18-Oct-2009	SHEET	45	OF	66
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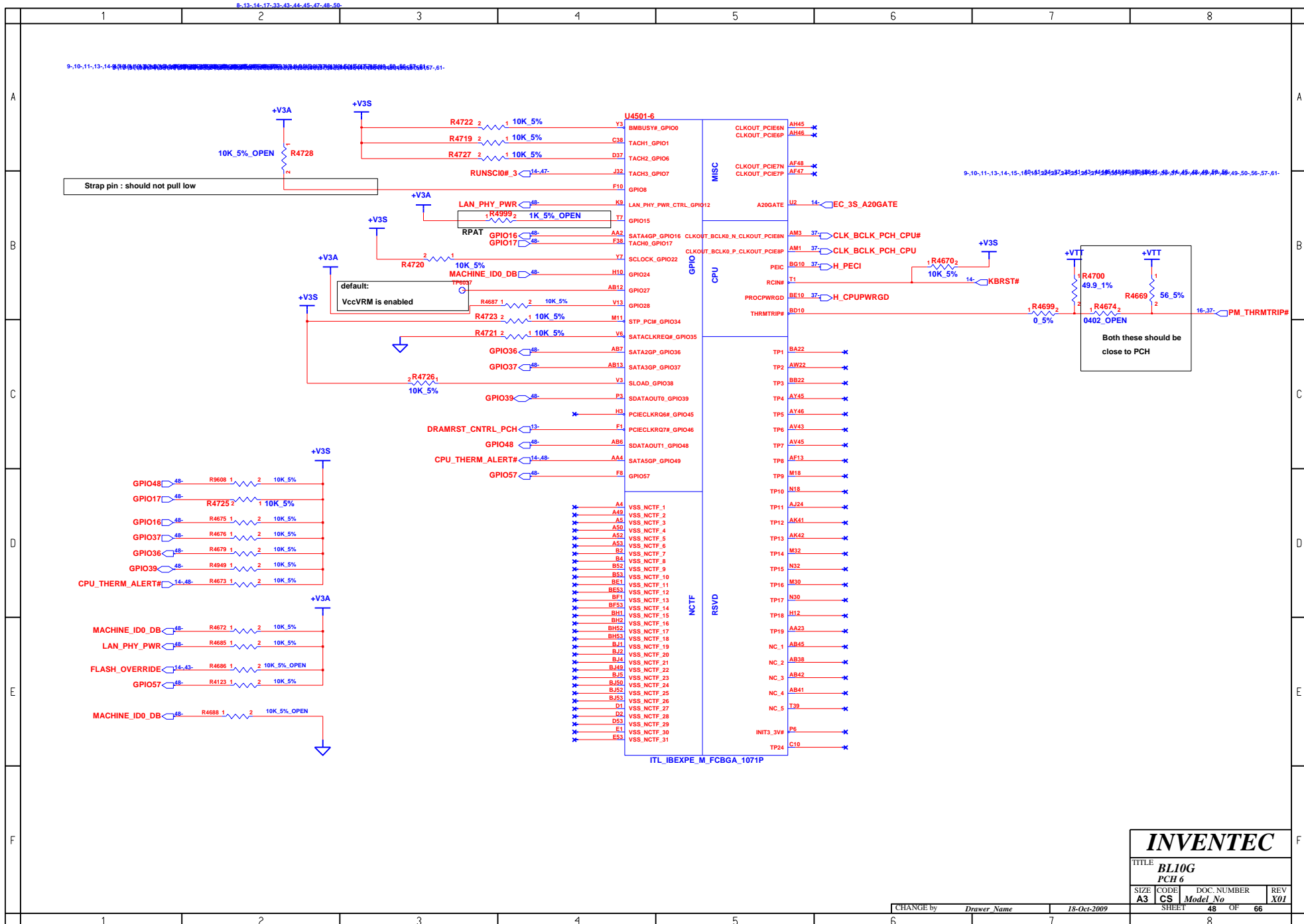
INVENTEC			
TITLE BL10G PCH 4			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 46 OF 66



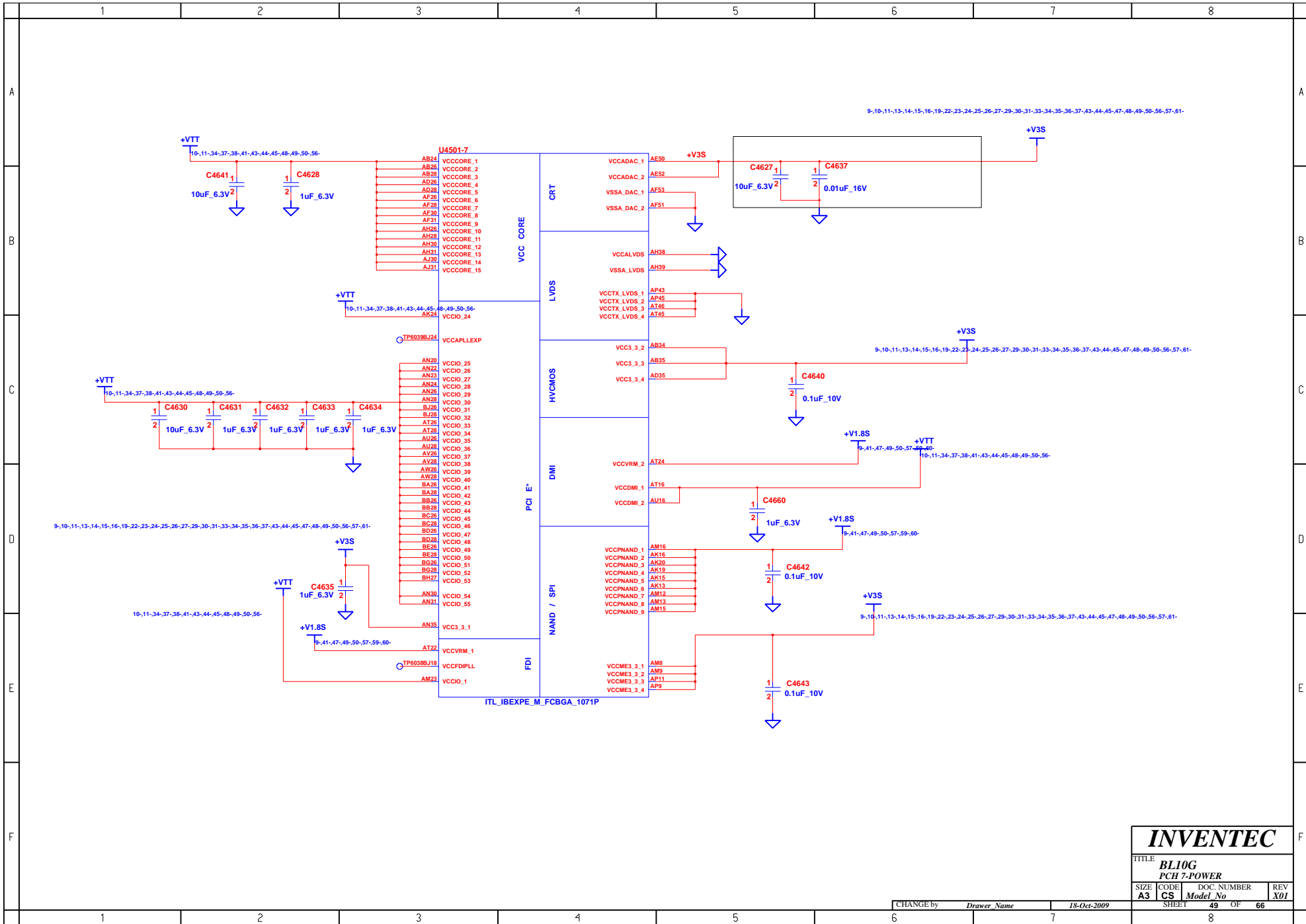
INVENTEC

TITLE
**BLI0G
PCH 5**

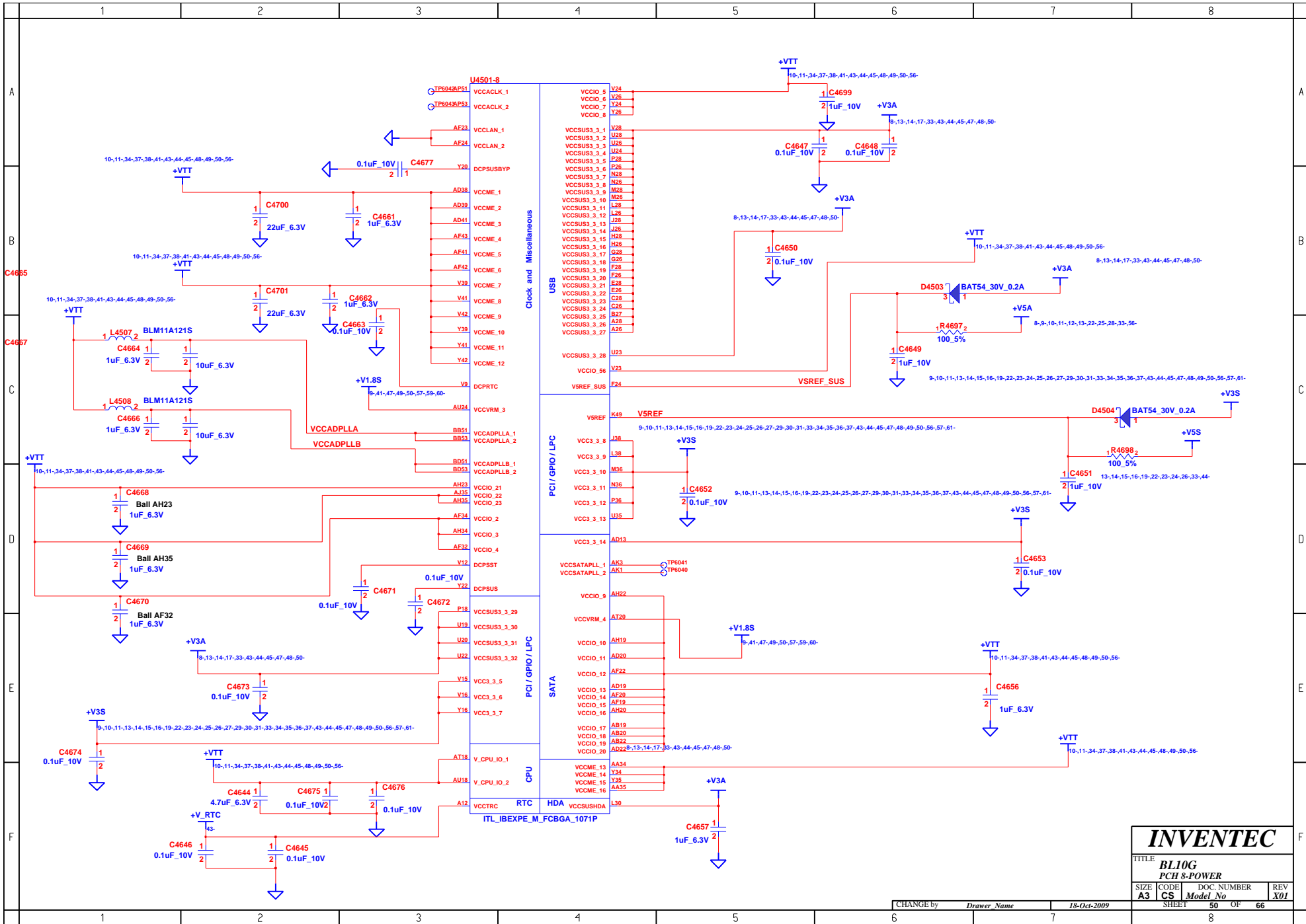
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 47 OF 66



INVENTEC			
TITLE BL10G PCH 6			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 8 of 66



INVENTEC			
TITLE BLI0G PCH 7-POWER			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	SHEET 49 OF 66

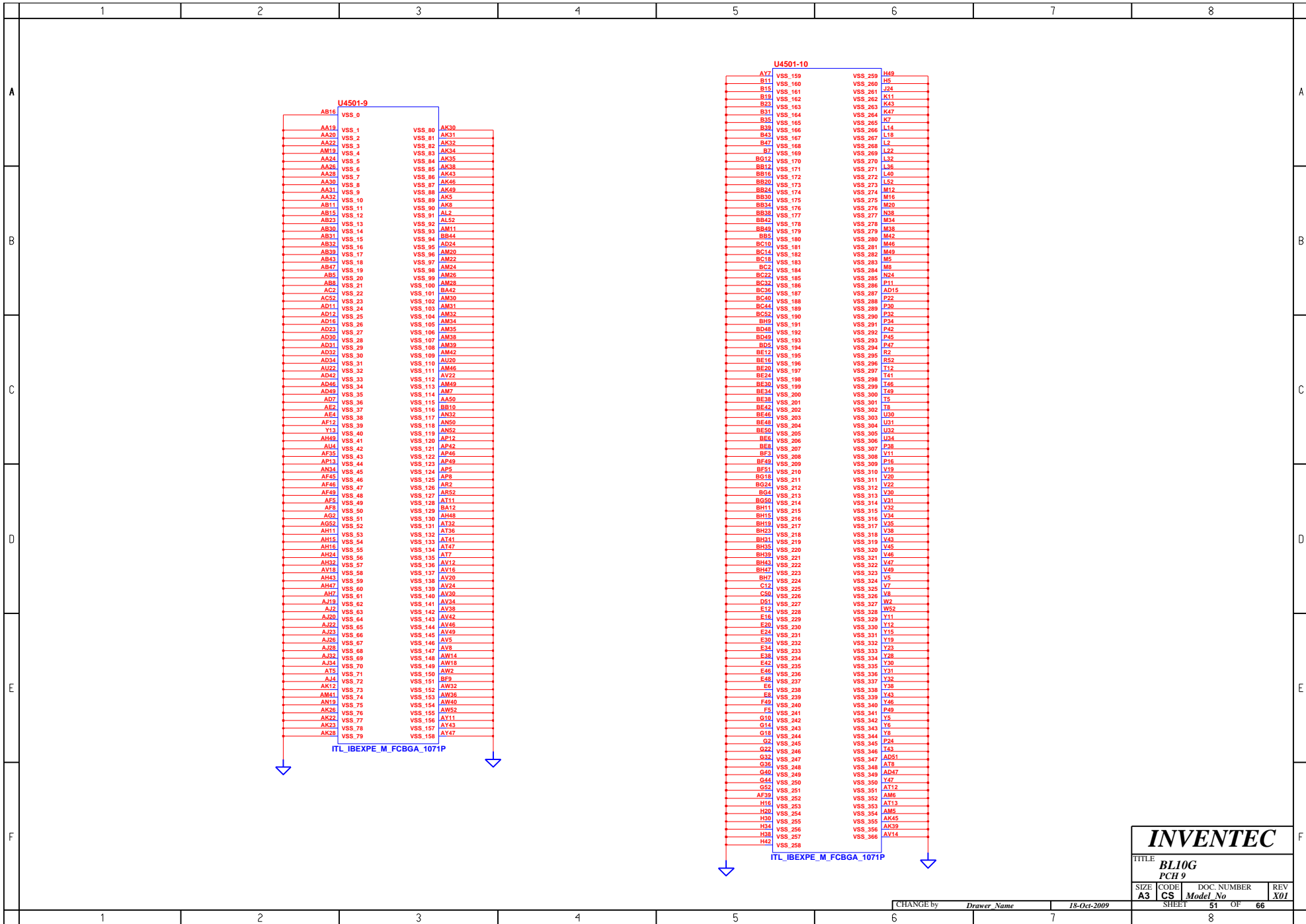


INVENTEC

TITLE
**BLI0G
PCH 8-POWER**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01

SHEET 50 OF 66



INVENTEC

TITLE
**BL10G
PCH 9**

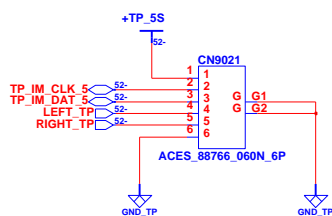
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
SHEET		51 OF	66

CHANGE by *Drawer Name* 18-Oct-2009

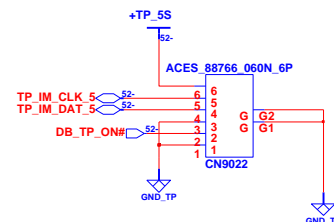
PICK BUTTON BOARD

(For - Touchpad Module, Touchpad Switch Board, Fingerprint Module)

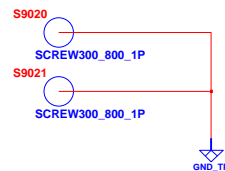
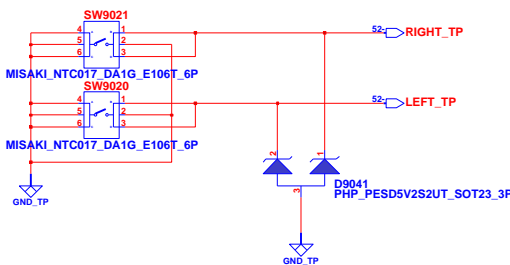
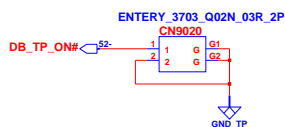
Connect to Touchpad Module



Connect to Mainboard's TP connector



Connect to Touchpad Switch Board

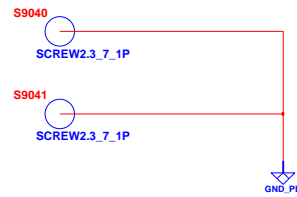
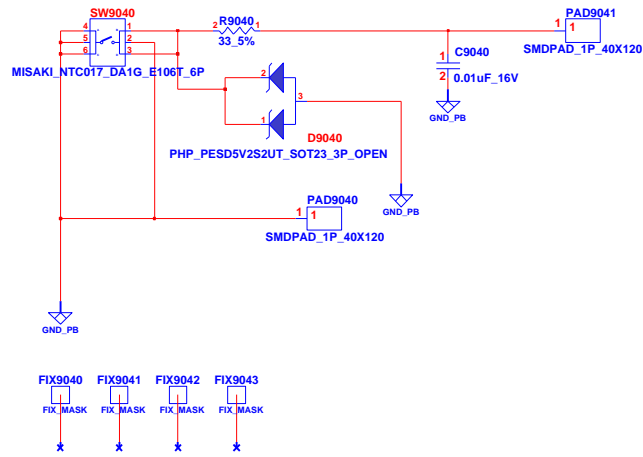


INVENTEC

TITLE			
BL10G			
Hot key board			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		52	OF 66

CHANGE by *Drawer Name* 18-Mar-2010

TOUCHPAD SWITCH BOARD

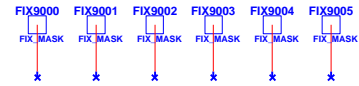
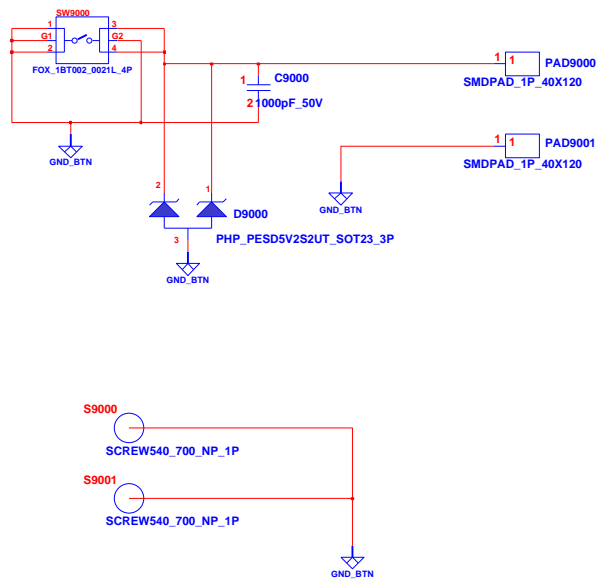


INVENTEC

TITLE			
BL10G			
Touch pad			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
SHEET		53	OF 66

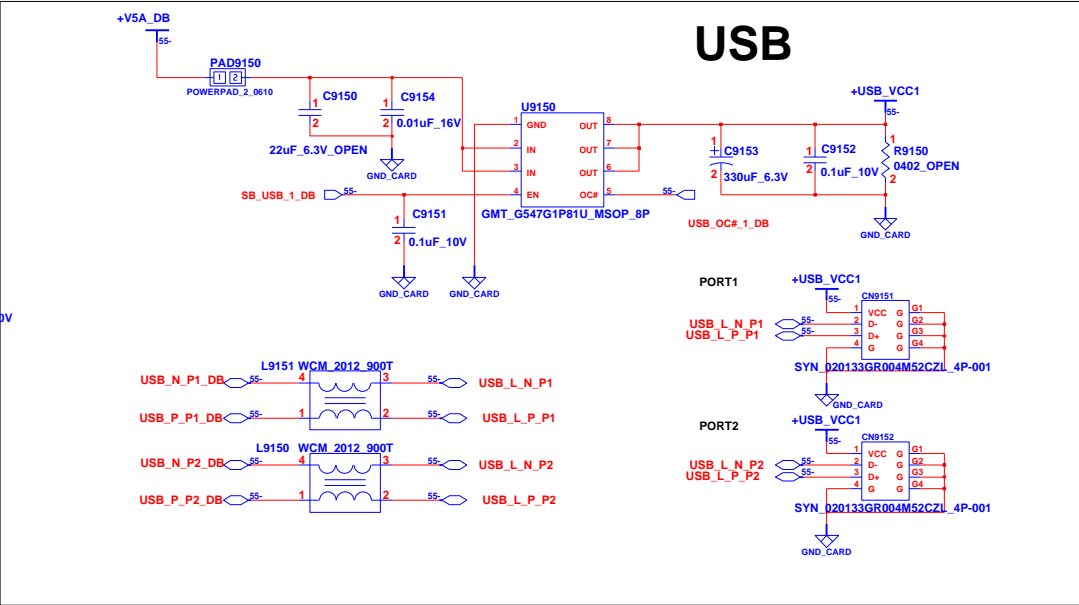
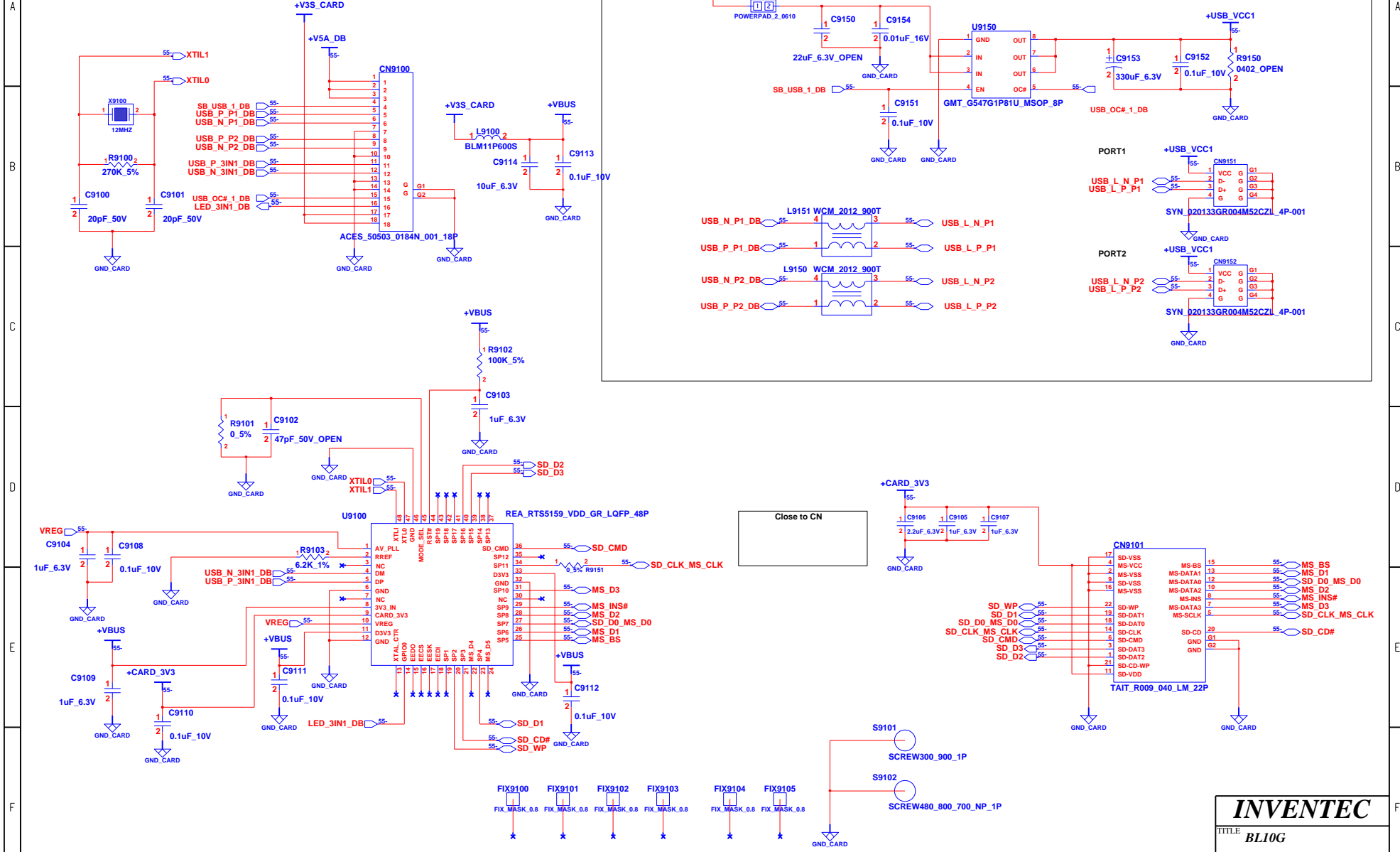
CHANGE by *Drawer Name* 20-Jan-2010

POWER BUTTON



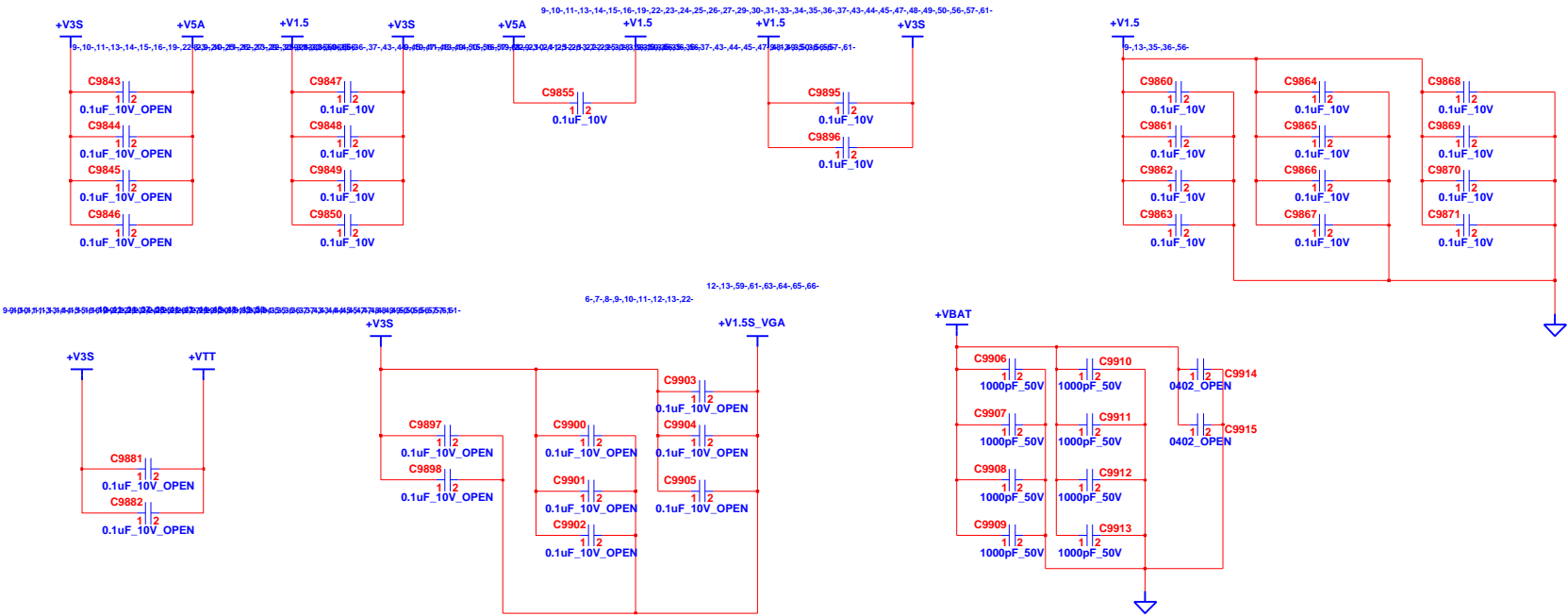
INVENTEC			
TITLE BL10G			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		11-Mar-2010	SHEET 54 OF 66

CARD READER & USB BOARD



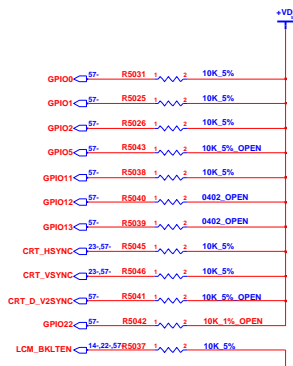
INVENTEC			
TITLE BLI0G			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV X01
CHANGE by <i>Drawer Name</i>		18-Oct-2009	
SHEET 55		OF 66	

EMI

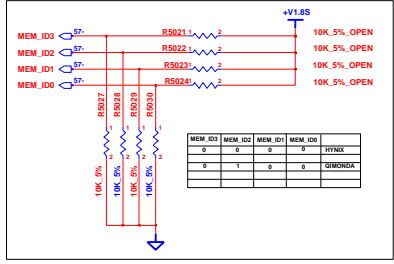


INVENTEC

TITLE			
BLI0G			
EMI			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	X01
CHANGE by		Drawer Name	18-Oct-2009
SHEET		56	OF 66

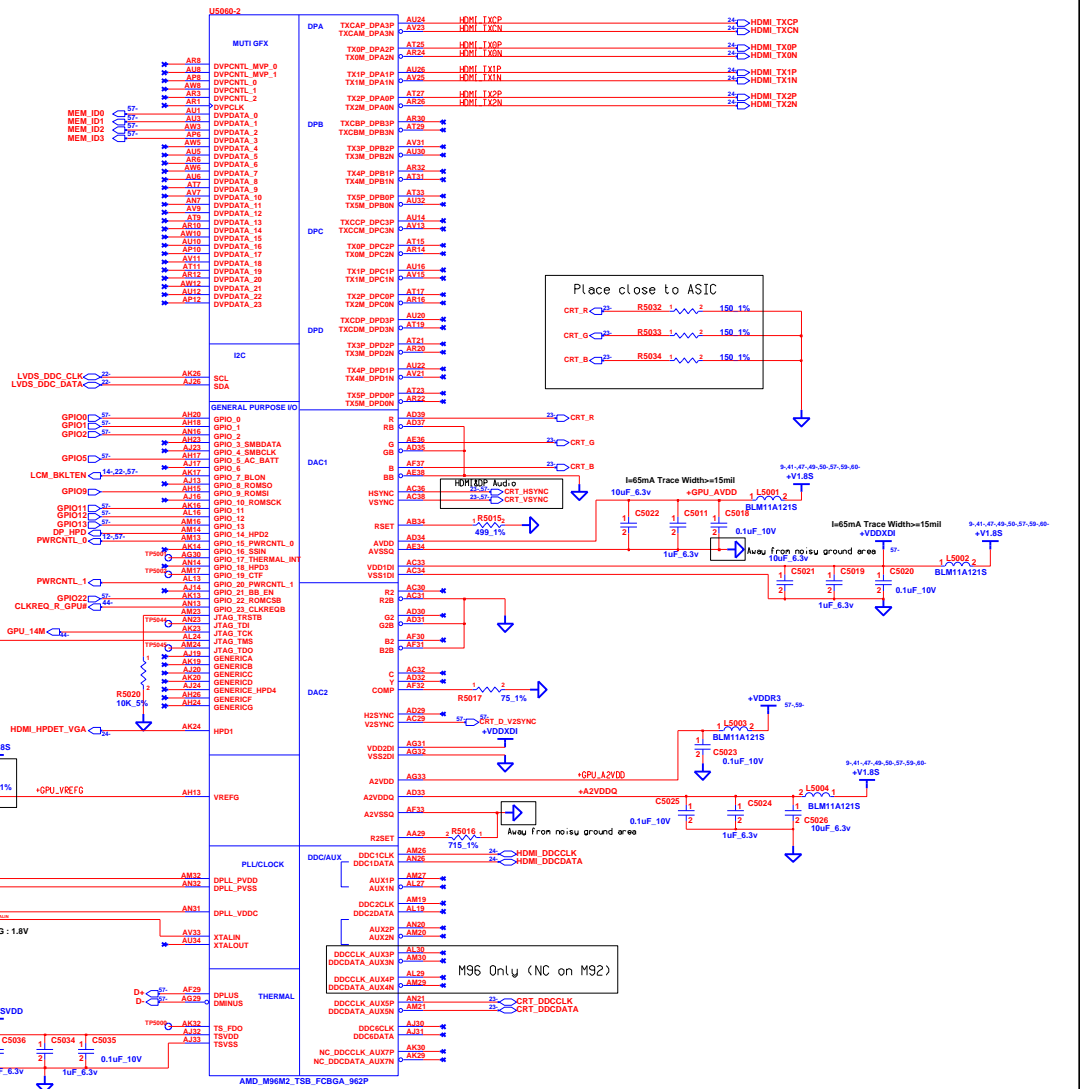


Pin Base Straps																							
GPIO_0	Transmitter Power Saving Enable	0 : 50% Tx output swing (default) 1 : Full Tx output swing																					
GPIO_1	PCIe transmitter De-emphasis	0 : De-emphasis disabled (default) 1 : De-emphasis enabled																					
GPIO_2	GEN1/GEN2 enable	0 : GEN1 (default) 1 : GEN2																					
GPIO_8	Must be low during reset	Left unconnected																					
GPIO_21																							
GPIO_9	VGA Disable	0 : Enable (default) 1 : Disable																					
GPIO_11[1:3]	Memory aperture size	<table border="1"> <thead> <tr> <th>GPIO_13</th> <th>GPIO_12</th> <th>GPIO_11</th> <th>Memory aperture size</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>128M</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>256M</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>512M</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>1GB</td> </tr> </tbody> </table>	GPIO_13	GPIO_12	GPIO_11	Memory aperture size	0	0	0	128M	0	0	1	256M	0	1	0	512M	0	1	1	1GB	
GPIO_13	GPIO_12	GPIO_11	Memory aperture size																				
0	0	0	128M																				
0	0	1	256M																				
0	1	0	512M																				
0	1	1	1GB																				
GPIO_22	Enable external BIOS ROM device	0 : Disable (default) 1 : Enable																					
HSYNC[1]	Audio[1:0]	00 : No audio function 01 : Audio for DP 10 : Audio for DP and HDMI if dongle is detected 11 : Audio for DP and HDMI																					
VSYNC[0]																							



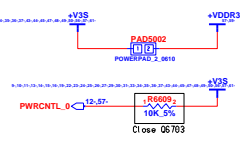
MEM_ID[0]	VRAM Vendor & Size
00h	M92 M92XT
01h	Hynix 64Mx16 (256MB)
02h	Hynix 64Mx16 (512MB)
03h	Samsung 64Mx16 (1GB)
04h	Samsung 64Mx16 (256MB)
05h	Olomonda 64Mx16 (256MB)
06h	Olomonda 64Mx16 (512MB)
07h	-----
08h	-----

M92 XT P/N = 6019B0591602
M96 P/N = 6019B0591701

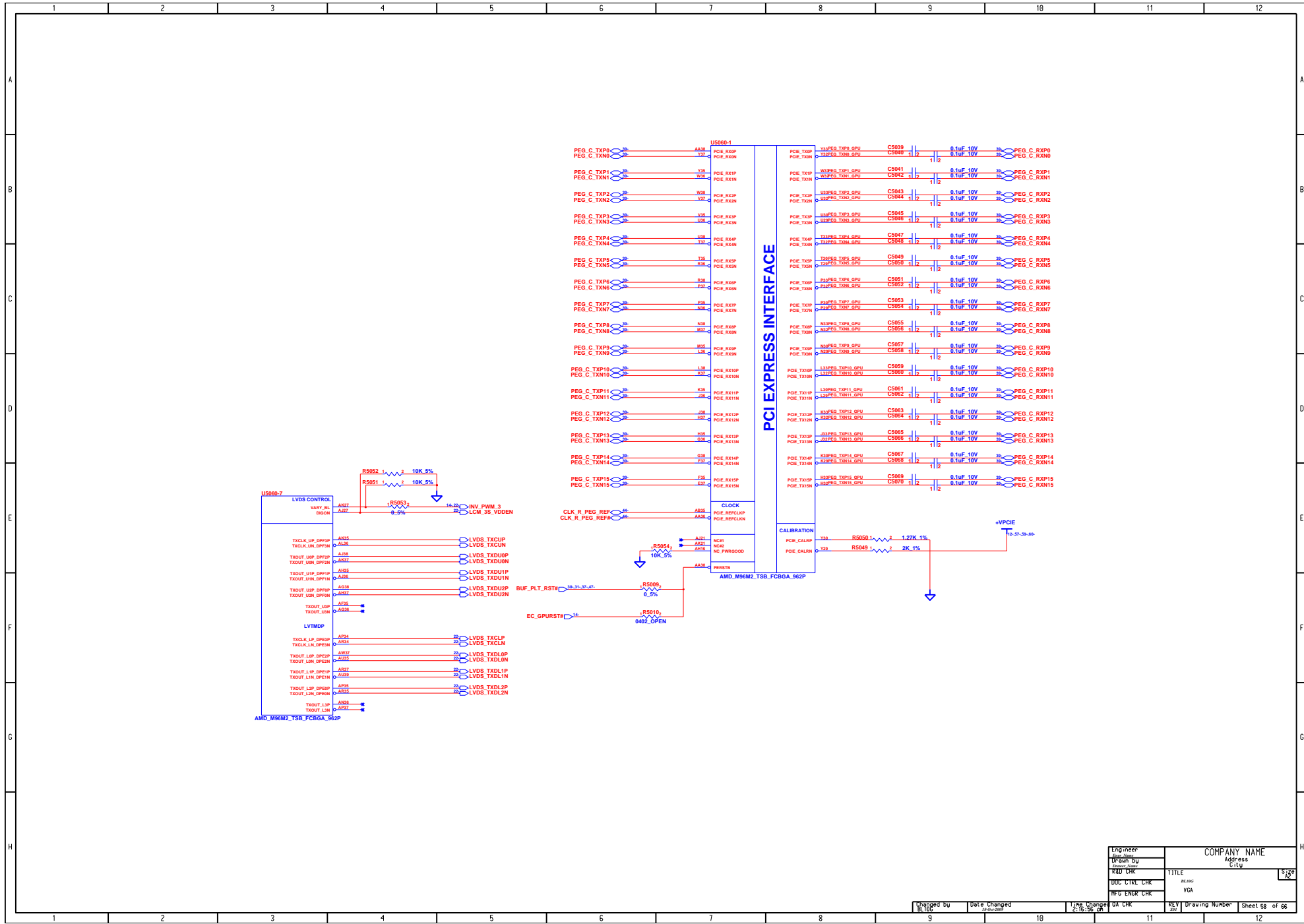


Place close to ASIC
 CRT_R = R5032 1 2 150 1%
 CRT_G = R5033 1 2 150 1%
 CRT_B = R5034 1 2 150 1%

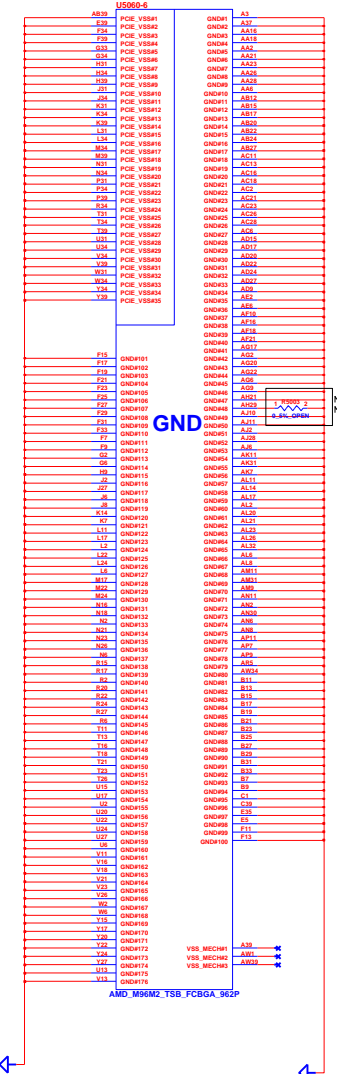
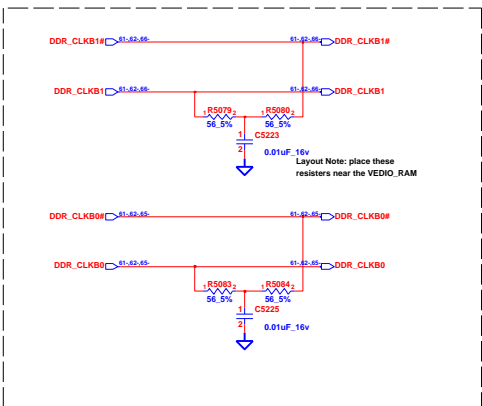
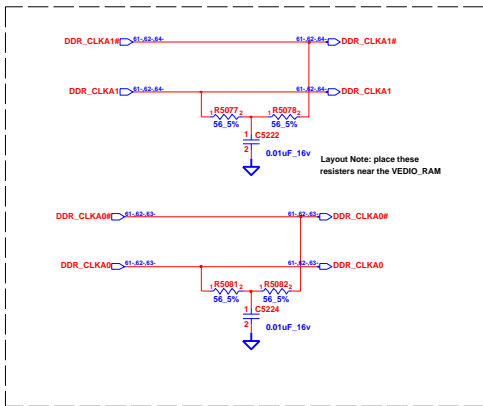
Closed to ASIC
 R5018 249 1% 1 C5022 0.1uF 10V
 R5019 499 1% 1 C5023 0.1uF 10V



Signature	COMPANY NAME
REV: 001	Address
REV: 002	City
REV: 003	Country
REV: 004	State
REV: 005	Zip
REV: 006	Phone
REV: 007	Fax
REV: 008	Website
REV: 009	E-mail
REV: 010	Part Number
REV: 011	Revision
REV: 012	Quantity
REV: 013	Unit Price
REV: 014	Total Price
REV: 015	Comments



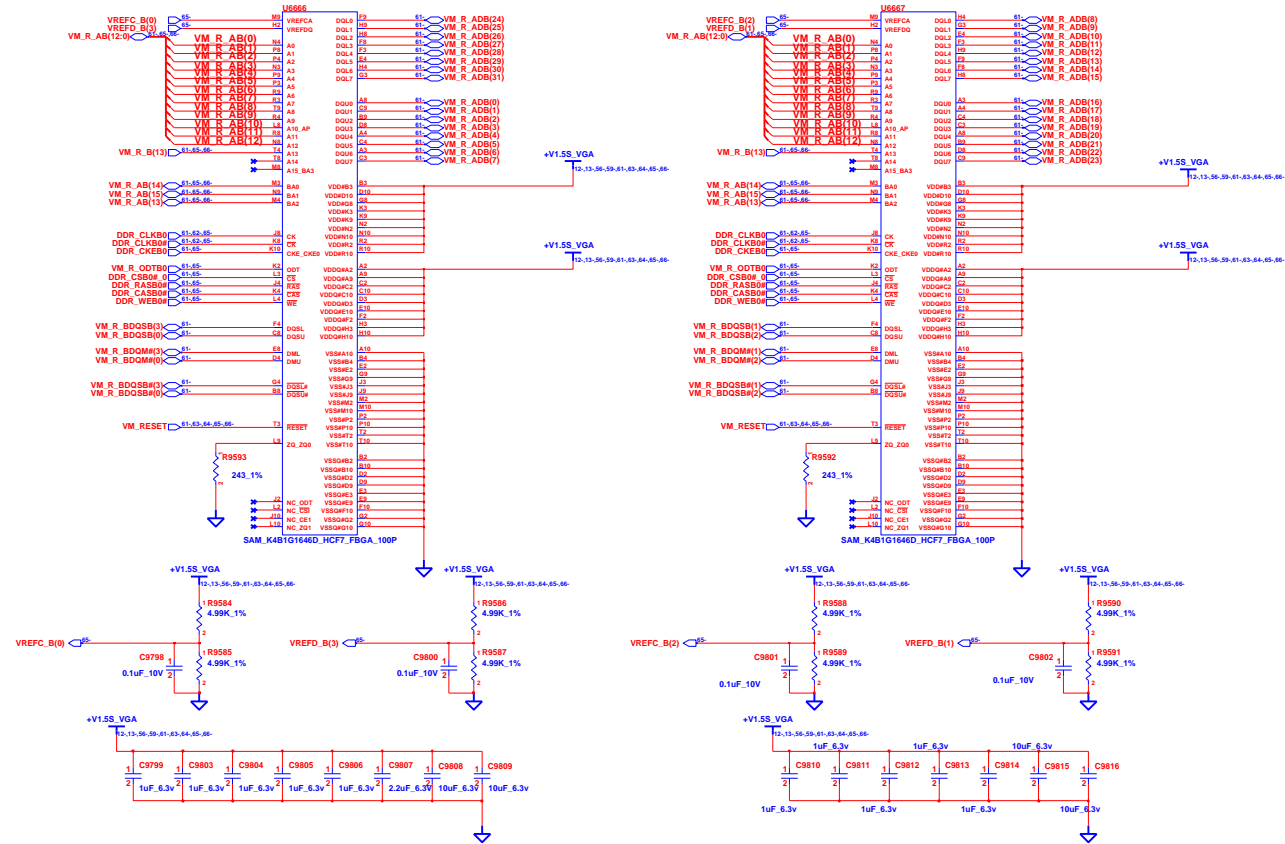
Engineer Date: 2023-09-20 By: [Signature]	COMPANY NAME Address City
QA CK DU: CTRL CK MFG ENGR CK	TITLE REV: [Signature] VGA
Checked by Date Changed	REV: [Signature] Drawing Number Sheet 58 of 66



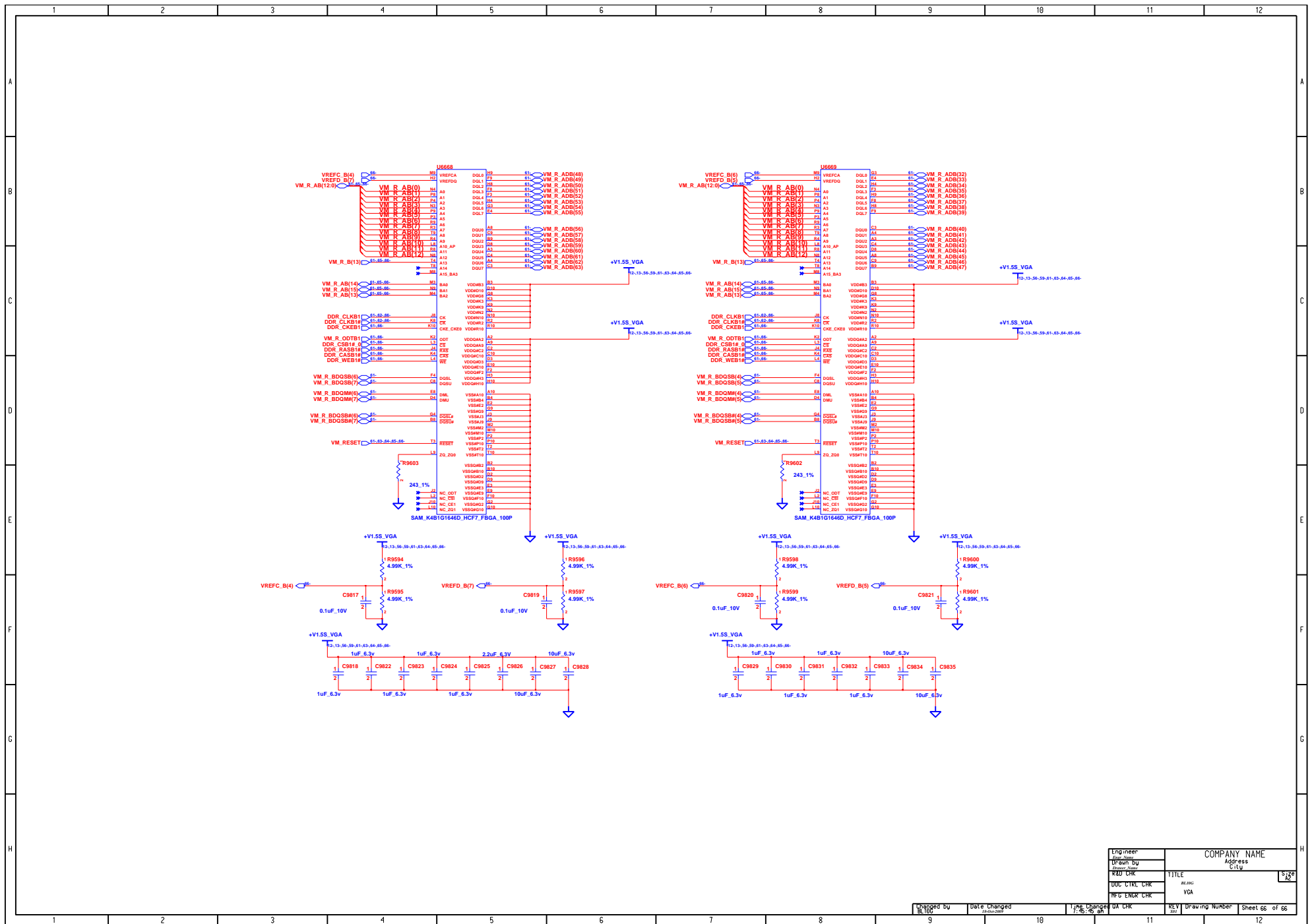
R5079/R5080 R5083 R5084
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 998 999
 999 1000

Engineer	COMPANY NAME
Drawn By	Address
Rev'd By	City
REV. CHK	TITLE
DWG. CTRL. CHK	DATE
PROG. ENGR. CHK	VGA
DATE	

M92 Use channel B



Engineer	COMPANY NAME
Drawn By	Address
Revise	City
CHK	State
DATE	Country
TITLE	
DESIGNER	VGA
DATE	
CHK	
DATE	
CHK	
DATE	
CHK	



Engineer Date: 20/10/2023 Drawn: DU Checked: Alvin Rev: 001	COMPANY NAME Address City
DU: CTR: CHK MFG: ENGR: CHK	TITLE VGA
Checked by Date Changed Type Changed	REV: 001 Drawing Number Sheet 66 of 66