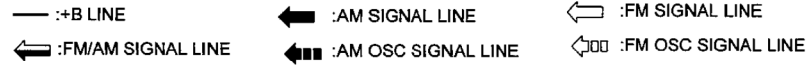


SCHEMATIC DIAGRAM - 1



A MAIN CIRCUIT

