


TFT LCD Approval Specification

MODEL NO.: M190A1-L02

Customer: _____

Approved by: _____

Note:

| Liquid Crystal Display Division | |
|---|--|
| QRA Division. | OA Head Division. |
| Approval | Approval |
|  |  |

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REVISION HISTORY

| | Date | Section | Description |
|-----|--------------|---------|--|
| 0.0 | Jan, 20, 06' | All | M190A1 -L02 Specifications was first issued. |
| 1.0 | Feb, 16, 06' | 7.2 | Optical specifications: Typ: (Rx,Ry): (0.636,0.349) → (0.640,0.335); (Gx,Gy):(0.290,0.589) → (0.283,0.599); (Bx,By): (0.143,0.080) → (0.153,0.079) Contrast Ratio :Min (TBD) → (600); Typ (800) → (850) Viewing angle: (Min, Typ) : θ_{x+} , (70,80)→ (75,85); θ_{x-} , (70,80) → (75,85) |
| 2.0 | Mar, 22, 06' | 9.1 | Update module label wording & model name from L01 to L02 |
| | | 5.1 | Note (1): connector part number: add STARCONN 093F30-B0T11A. |
| | | 7.2 | Color chromaticity: Typ, (Rx,Ry): (0.640,0.335) → (0.640,0.334); (Gx,Gy):(0.283,0.599) → (0.286,0.599); (Bx,By): (0.153,0.079) → (0.154,0.077) Contrast Ratio : Min, (600) → 500 Note (5) Measurement setup: 20 minutes → 15 minutes |
| | | 8.2 | Update packing method |
| | | 9.1 | Module label add "RoHS" |

1. GENERAL DESCRIPTION

1.1 OVERVIEW

M190A1-L02 is a 19" wide TFT Liquid Crystal Display module with 4 CCFL Backlight unit and 30 pins 2ch-LVDS interface. This module supports 1440 x 900 WXGA+ mode and can display 16.2M colors. The inverter module for Backlight is not built in.

1.2 FEATURES

- Super Wide viewing angle.
- Super High contrast ratio
- Super fast response time
- High color saturation
- WXGA+ (1440 x 900 pixels) resolution
- DE (Data Enable) only mode
- LVDS (Low Voltage Differential Signaling) interface
- RoHS Compliance

1.3 APPLICATION

- TFT LCD Monitor

1.4 GENERAL SPECIFICATIONS

| Item | Specification | Unit | Note |
|--------------------|---|-------|------|
| Diagonal Size | 481.4 (19.05" diagonal) | mm | |
| Active Area | 410.4 (H) x 256.5 (V) | mm | (1) |
| Bezel Opening Area | 414.36 x 260.45 | mm | |
| Driver Element | a-si TFT active matrix | - | - |
| Pixel Number | 1440 x R.G.B. x 900 | pixel | - |
| Pixel Pitch | 0.285 (H) x 0.285 (V) | mm | - |
| Pixel Arrangement | RGB vertical stripe | - | - |
| Display Colors | 16.2M | color | - |
| Transmissive Mode | Normally White | - | - |
| Surface Treatment | Hard coating (3H), Anti-glare (Haze 25) | - | - |

1.5 MECHANICAL SPECIFICATIONS

| Item | | Min. | Typ. | Max. | Unit | Note |
|-------------|---------------|-------|-------|-------|------|------|
| Module Size | Horizontal(H) | 426.7 | 427.2 | 427.7 | mm | (1) |
| | Vertical(V) | 276.9 | 277.4 | 277.9 | mm | |
| | Depth(D) | - | 17 | 17.5 | mm | |
| Weight | | - | - | 2500 | g | - |

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

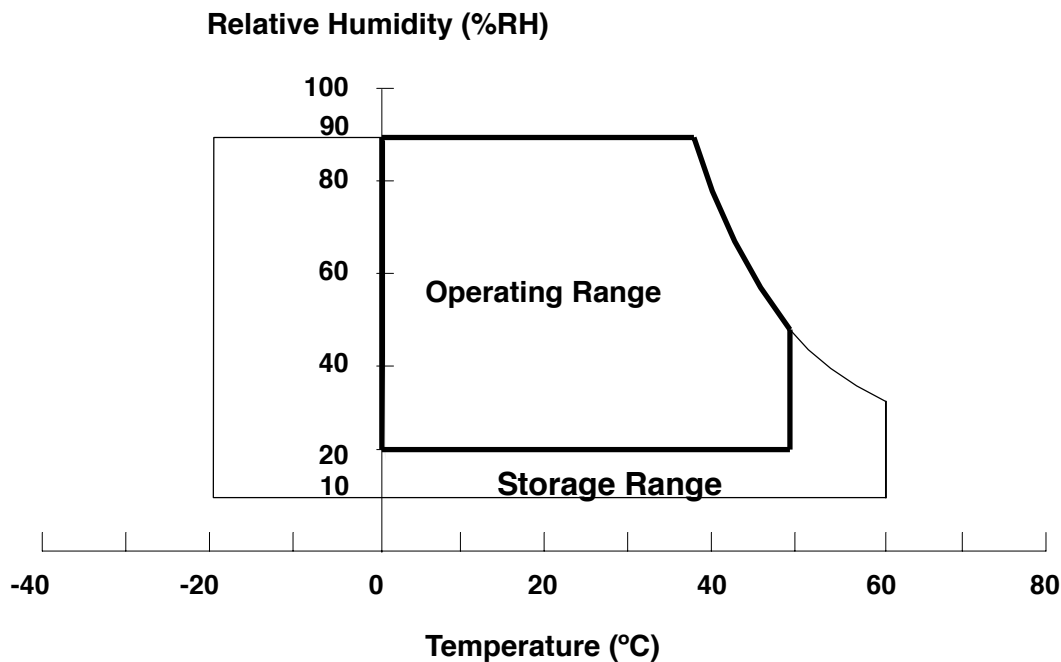
2. ABSOLUTE MAXIMUM RATINGS

2.1 ABSOLUTE RATINGS OF ENVIRONMENT

| Item | Symbol | Value | | Unit | Note |
|-------------------------------|------------------|-------|------|------|----------|
| | | Min. | Max. | | |
| Storage Temperature | T _{ST} | -20 | +60 | °C | (1) |
| Operating Ambient Temperature | T _{OP} | 0 | +50 | °C | (1), (2) |
| Shock (Non-Operating) | S _{NOP} | - | 50 | G | (3), (5) |
| Vibration (Non-Operating) | V _{NOP} | - | 1.5 | G | (4), (5) |

Note (1) Temperature and relative humidity range is shown in the figure below.

- (a) 90 %RH Max. ($T_a \leq 40^\circ\text{C}$).
- (b) Wet-bulb temperature should be 39°C Max. ($T_a > 40^\circ\text{C}$).
- (c) No condensation.

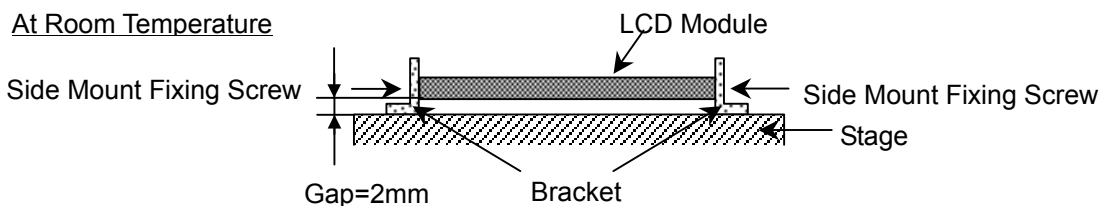


Note (2) The temperature of panel display surface area should be 0°C Min. and 60°C Max.

Note (3) 11ms, half sine wave, 1 time for $\pm X$, $\pm Y$, $\pm Z$.

Note (4) 10 ~ 300 Hz, 10min/cycle, 3 cycles each X, Y, Z.

Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.



2.2 ELECTRICAL ABSOLUTE RATINGS

2.2.1 TFT LCD MODULE

| Item | Symbol | Value | | Unit | Note |
|----------------------|-----------------|-------|------|------|------|
| | | Min. | Max. | | |
| Power Supply Voltage | V _{CC} | -0.3 | +6.0 | V | (1) |
| Logic Input Voltage | V _{IN} | -0.3 | 4.3 | V | |

2.2.2 BACKLIGHT UNIT

| Item | Symbol | Value | | Unit | Note |
|----------------|----------------|-------|------|-------------------|----------------------------------|
| | | Min. | Max. | | |
| Lamp Voltage | V _L | - | 2.5K | V _{RMS} | (1), (2), I _L = 7.0mA |
| Lamp Current | I _L | - | 7.5 | mA _{RMS} | |
| Lamp Frequency | F _L | - | 80 | KHz | |

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.2 for further information).

3. ELECTRICAL CHARACTERISTICS

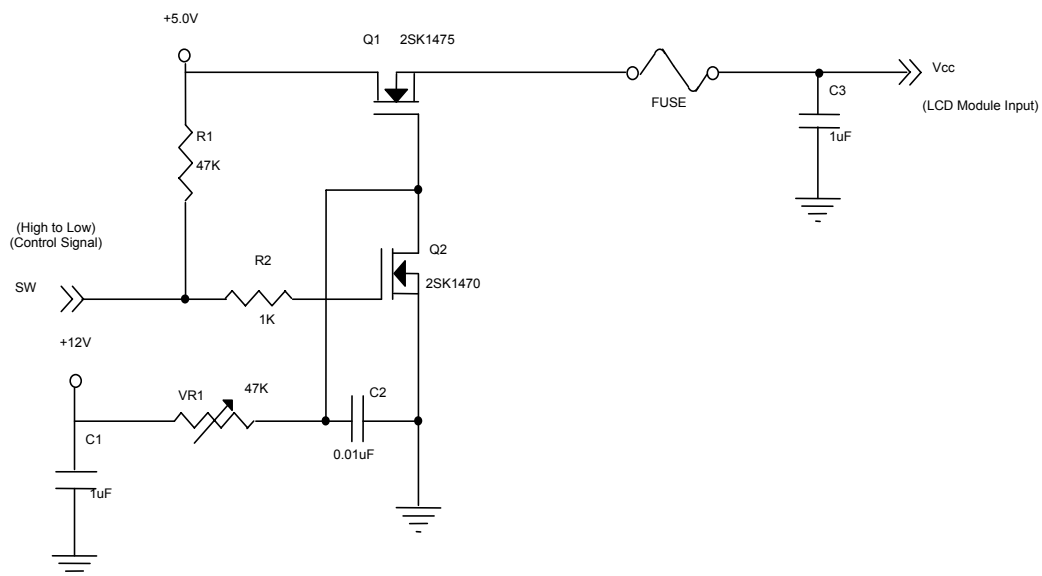
3.1 TFT LCD MODULE

$T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$

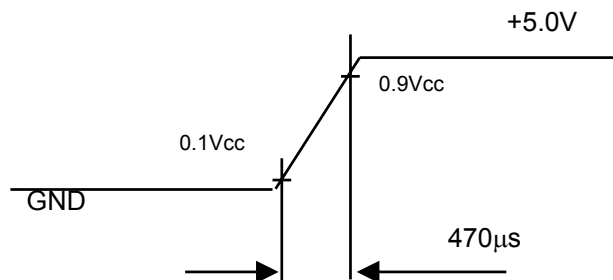
| Parameter | Symbol | Value | | | Unit | Note |
|---------------------------------|-----------------|----------|------|------|------|------|
| | | Min. | Typ. | Max. | | |
| Power Supply Voltage | V_{CC} | 4.5 | 5.0 | 5.5 | V | - |
| Ripple Voltage | V_{RP} | - | - | 100 | mV | - |
| Rush Current | I_{RUSH} | - | 1.6 | 3 | A | (2) |
| Power Supply Current | White | - | 0.5 | 0.7 | A | (3)a |
| | Black | - | 0.7 | 1.0 | A | (3)b |
| | Vertical Stripe | - | 0.7 | 1.0 | A | (3)c |
| LVDS differential input voltage | V_{id} | 100 | - | 600 | mV | |
| LVDS common input voltage | V_{ic} | - | 1.2 | - | V | |
| Logic "L" input voltage | V_{il} | V_{SS} | - | 0.8 | V | |

Note (1) The module should be always operated within above ranges.

Note (2) Measurement Conditions:



Vcc rising time is 470μs



Note (3) The specified power supply current is under the conditions at $V_{CC} = 5.0\text{ V}$, $T_a = 25 \pm 2\text{ }^{\circ}\text{C}$, $f_v = 60\text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



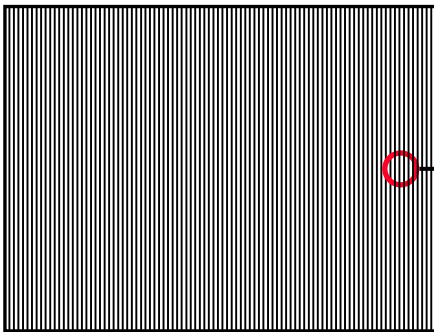
Active Area

b. Black Pattern

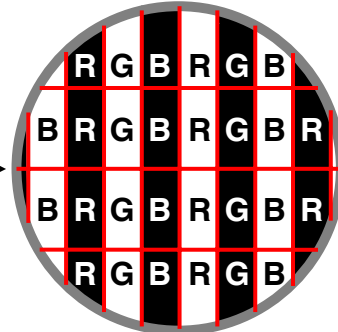


Active Area

c. Vertical Stripe Pattern



Active Area

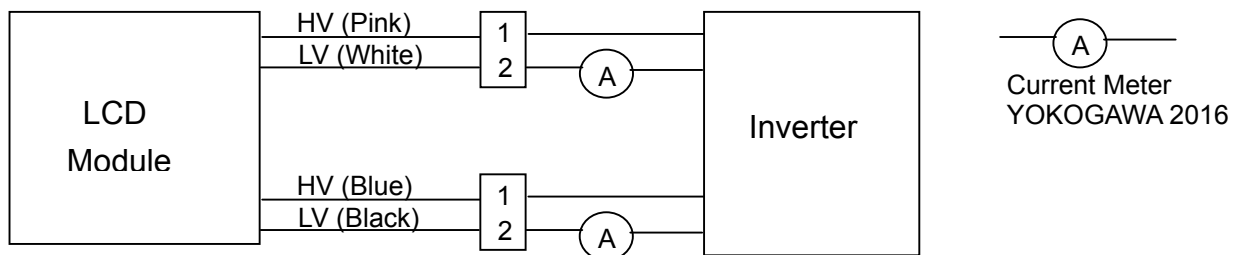


3.2 BACKLIGHT UNIT

$T_a = 25 \pm 2 \text{ } ^\circ\text{C}$

| Parameter | Symbol | Value | | | Unit | Note |
|----------------------|----------|-------|-------|----------------------------|-------------------|-----------------------------|
| | | Min. | Typ. | Max. | | |
| Lamp Input Voltage | V_L | 697 | 775 | 853 | V_{RMS} | $I_L = 7.0 \text{ mA}$ |
| Lamp Current | I_L | 2.0 | 7.0 | 7.5 | mA_{RMS} | (1) |
| Lamp Turn On Voltage | V_S | --- | --- | 1500(25 $^\circ\text{C}$) | V_{RMS} | (2) |
| | | --- | --- | 1710(0 $^\circ\text{C}$) | V_{RMS} | (2) |
| Operating Frequency | F_L | 40 | --- | 80 | KHz | (3) |
| Lamp Life Time | L_{BL} | 40000 | --- | --- | Hrs | (5) |
| Power Consumption | P_L | --- | 21.72 | --- | W | (4), $I_L = 7.0 \text{ mA}$ |

Note (1) Lamp current is measured by utilizing high frequency current meters as shown below:



Note (2) The voltage shown above should be applied to the lamp for more than 1 second after startup.

Otherwise the lamp may not be turned on.

Note (3) The lamp frequency may produce interference with horizontal synchronous frequency from the display, and this may cause line flow on the display. In order to avoid interference, the lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible.

Note (4) $P_L = I_L \times V_L \times 4 \text{ CCFLs}$

Note (5) The lifetime of lamp can be defined as the time in which it continues to operate under the condition

$T_a = 25 \pm 2 \text{ } ^\circ\text{C}$ and $I_L = 7.0 \text{ mA rms}$ until one of the following events occurs:

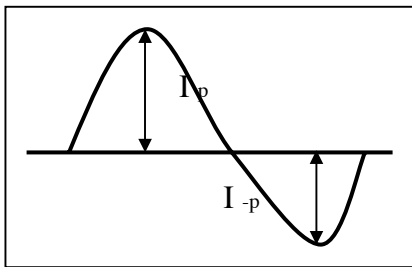
- (a) When the brightness becomes or lower than 50% of its original value.
- (b) When the effective ignition length becomes or lower than 80% of its original value. (Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)

Note (6) The waveform of the voltage output of inverter must be area-symmetric and the design of the inverter must have specifications for the modularized lamp. The performance of the Backlight, such as lifetime or brightness, is greatly influenced by the characteristics of the DC-AC inverter for the lamp. All the parameters of an inverter should be carefully designed to avoid producing too much current leakage from high voltage output of the inverter. When designing or ordering the inverter please make sure that a poor lighting caused by the mismatch of the Backlight and the inverter (miss-lighting, flicker, etc.) never occurs. If the above situation is confirmed, the module should be operated in the same manners when it is installed in your instrument.

The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform. (Unsymmetrical ratio is less than 10%) Please do not use the inverter which has unsymmetrical voltage and unsymmetrical current and spike wave. Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.

Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp. It shall help increase the lamp lifetime and reduce its leakage current.

- a. The asymmetry rate of the inverter waveform should be 10% below;
- b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$;
- c. The ideal sine wave form shall be symmetric in positive and negative polarities.



* Asymmetry rate:

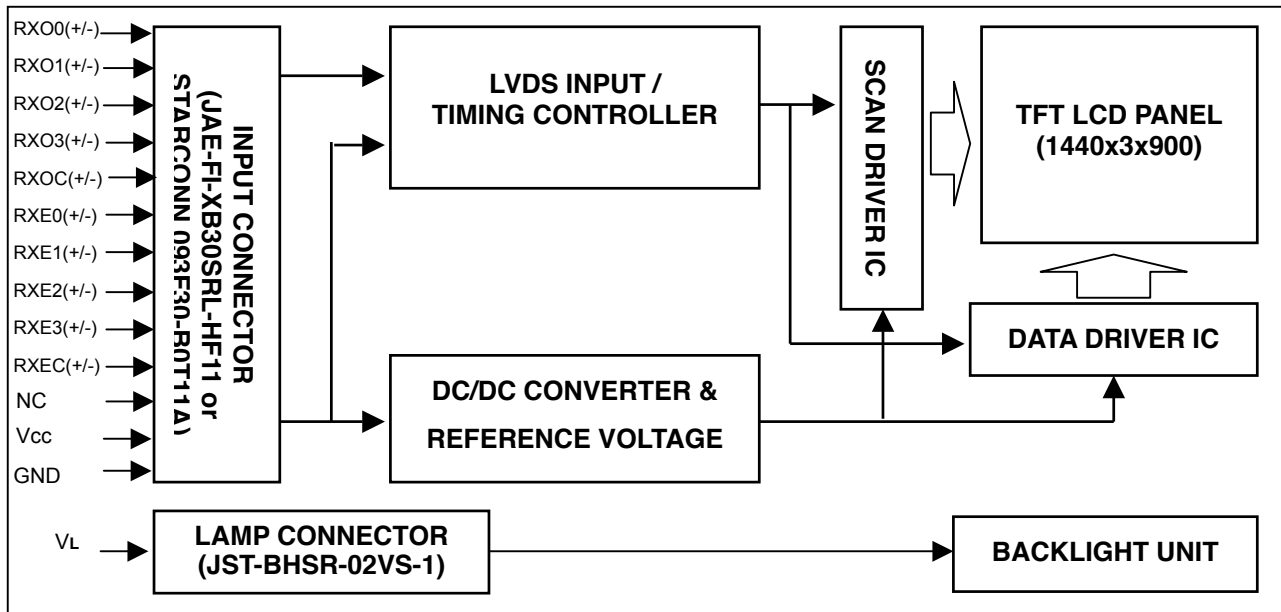
$$|I_p - I_{-p}| / I_{rms} * 100\%$$

* Distortion rate

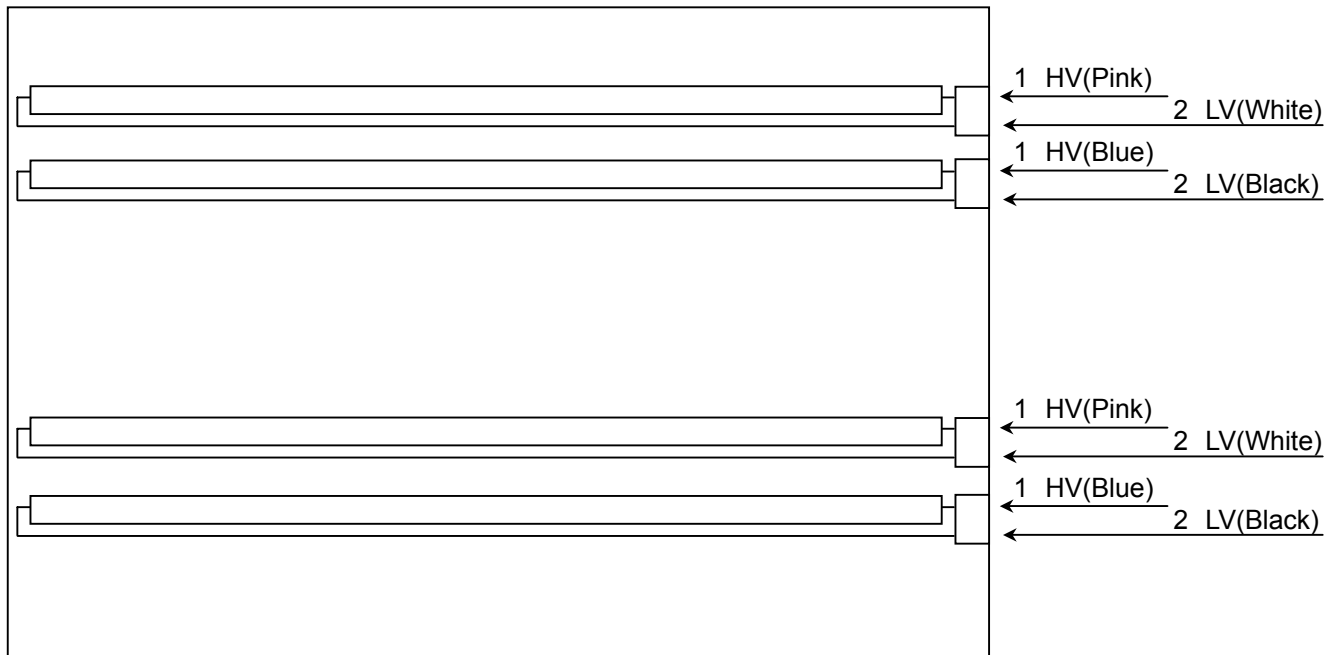
$$I_p \text{ (or } I_{-p}) / I_{rms}$$

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT



5. INPUT TERMINAL PIN ASSIGNMENT

5.1 TFT LCD MODULE

| Pin | Name | Description |
|-----|-------|--|
| 1 | RXO0- | Negative LVDS differential data input. Channel O0 (odd) |
| 2 | RXO0+ | Positive LVDS differential data input. Channel O0 (odd) |
| 3 | RXO1- | Negative LVDS differential data input. Channel O1 (odd) |
| 4 | RXO1+ | Positive LVDS differential data input. Channel O1 (odd) |
| 5 | RXO2- | Negative LVDS differential data input. Channel O2 (odd) |
| 6 | RXO2+ | Positive LVDS differential data input. Channel O2 (odd) |
| 7 | GND | Ground |
| 8 | RXOC- | Negative LVDS differential clock input. (odd) |
| 9 | RXOC+ | Positive LVDS differential clock input. (odd) |
| 10 | RXO3- | Negative LVDS differential data input. Channel O3(odd) |
| 11 | RXO3+ | Positive LVDS differential data input. Channel O3 (odd) |
| 12 | RXE0- | Negative LVDS differential data input. Channel E0 (even) |
| 13 | RXE0+ | Positive LVDS differential data input. Channel E0 (even) |
| 14 | GND | Ground |
| 15 | RXE1- | Negative LVDS differential data input. Channel E1 (even) |
| 16 | RXE1+ | Positive LVDS differential data input. Channel E1 (even) |
| 17 | GND | Ground |
| 18 | RXE2- | Negative LVDS differential data input. Channel E2 (even) |
| 19 | RXE2+ | Positive LVDS differential data input. Channel E2 (even) |
| 20 | RXEC- | Negative LVDS differential clock input. (even) |
| 21 | RXEC+ | Positive LVDS differential clock input. (even) |
| 22 | RXE3- | Negative LVDS differential data input. Channel E3 (even) |
| 23 | RXE3+ | Positive LVDS differential data input. Channel E3 (even) |
| 24 | GND | Ground |
| 25 | NC | Not connection. |
| 26 | NC | Not connection. |
| 27 | NC | Not connection. |
| 28 | VCC | +5.0V power supply |
| 29 | VCC | +5.0V power supply |
| 30 | VCC | +5.0V power supply |

Note (1) Connector Part No.: JAE-FI-XB30SRL-HF11 or STARCONN 093F30-B0T11A or equivalent.

Note (2) The first pixel is odd.

Note (3) Input signal of even and odd clock should be the same timing.

| | | | | | | | | |
|-----------------|-------------|-----|-----|-----|-----|-----|-----|-----|
| LVDS Channel E0 | LVDS output | D7 | D6 | D4 | D3 | D2 | D1 | D0 |
| | Data order | EG0 | ER5 | ER4 | ER3 | ER2 | ER1 | ER0 |
| LVDS Channel E1 | LVDS output | D18 | D15 | D14 | D13 | D12 | D9 | D8 |
| | Data order | EB1 | EB0 | EG5 | EG4 | EG3 | EG2 | EG1 |
| LVDS Channel E2 | LVDS output | D26 | D25 | D24 | D22 | D21 | D20 | D19 |
| | Data order | DE | NA | NA | EB5 | EB4 | EB3 | EB2 |
| LVDS Channel E3 | LVDS output | D23 | D17 | D16 | D11 | D10 | D5 | D27 |
| | Data order | NA | EB7 | EB6 | EG7 | EG6 | ER7 | ER6 |
| LVDS Channel O0 | LVDS output | D7 | D6 | D4 | D3 | D2 | D1 | D0 |
| | Data order | OG0 | OR5 | OR4 | OR3 | OR2 | OR1 | OR0 |
| LVDS Channel O1 | LVDS output | D18 | D15 | D14 | D13 | D12 | D9 | D8 |
| | Data order | OB1 | OB0 | OG5 | OG4 | OG3 | OG2 | OG1 |
| LVDS Channel O2 | LVDS output | D26 | D25 | D24 | D22 | D21 | D20 | D19 |
| | Data order | DE | NA | NA | OB5 | OB4 | OB3 | OB2 |
| LVDS Channel O3 | LVDS output | D23 | D17 | D16 | D11 | D10 | D5 | D27 |
| | Data order | NA | OB7 | OB6 | OG7 | OG6 | OR7 | OR6 |

5.2 BACKLIGHT UNIT

| Pin | Symbol | Description | Remark |
|-----|--------|--------------|--------|
| 1 | HV | High Voltage | Pink |
| 2 | LV | Low Voltage | White |

| | | | |
|---|----|--------------|-------|
| 1 | HV | High Voltage | Blue |
| 2 | LV | Low Voltage | Black |

Note (1) Connector Part No.: BHSR-02VS-1 (JST) or equivalent

Note (2) User's connector Part No.: SM02B-BHSS-1-TB (JST) or equivalent

5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

| Color | | Data Signal | | | | | | | | | | | | | | | | | | | | | | | |
|---------------------|-----------------|-------------|----|----|----|----|----|----|----|-------|----|----|----|----|----|----|----|------|----|----|----|----|----|----|----|
| | | Red | | | | | | | | Green | | | | | | | | Blue | | | | | | | |
| | | R7 | R6 | R5 | R4 | R3 | R2 | R1 | R0 | R7 | R6 | G5 | G4 | G3 | G2 | G1 | G0 | R7 | R6 | B5 | B4 | B3 | B2 | B1 | B0 |
| Basic Colors | Black | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Cyan | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Magenta | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| | Yellow | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | White | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Gray Scale Of Red | Red(0) / Dark | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(1) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(2) | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Red(253) | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(254) | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Red(255) | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale Of Green | Green(0) / Dark | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(1) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(2) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Green(253) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(254) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Green(255) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| Gray Scale Of Blue | Blue(0) / Dark | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| | Blue(1) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| | Blue(2) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : | : |
| | Blue(253) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
| | Blue(254) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| | Blue(255) | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |

Note (1) 0: Low Level Voltage, 1: High Level Voltage

6. INTERFACE TIMING

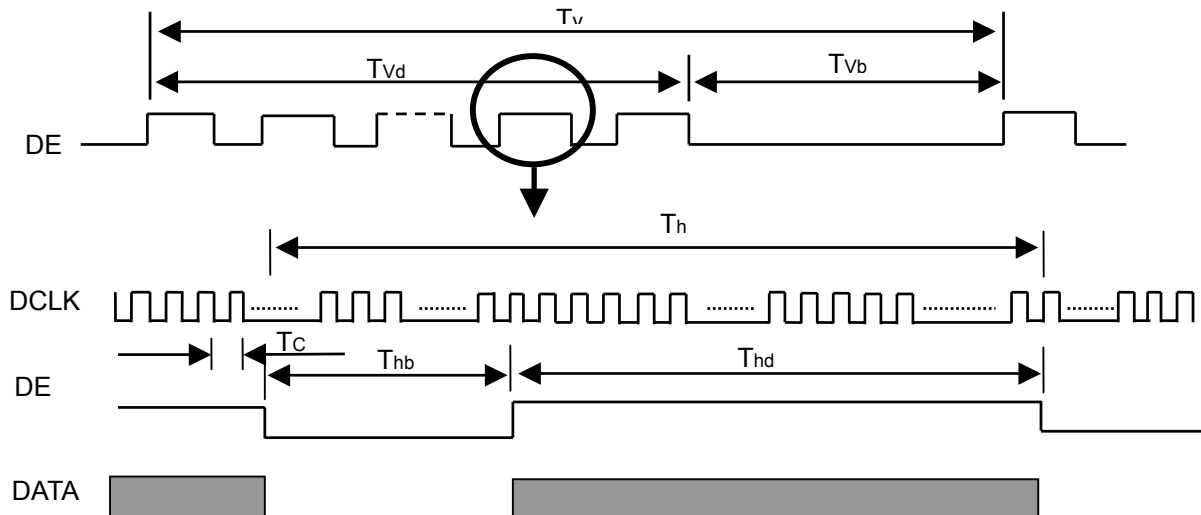
6.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

| Signal | Item | Symbol | Min. | Typ. | Max. | Unit | Note |
|--------------------------------|------------|--------|----------------|------|----------------|------|-------------------------|
| LVDS Clock | Frequency | Fc | - | 44.5 | 56 | MHz | - |
| | Period | Tc | 17.9 | 22.5 | - | ns | - |
| | High Time | Tch | - | 4/7 | - | Tc | - |
| | Low Time | Tcl | - | 3/7 | - | Tc | - |
| LVDS Data | Setup Time | Tlvs | 600 | - | - | ps | - |
| | Hold Time | Tlvh | 600 | - | - | ps | - |
| Vertical Active Display Term | Frame Rate | Fr | 56 | 60 | 75 | Hz | $T_v = T_{vd} + T_{vb}$ |
| | Total | Tv | 905 | 926 | 1050 | Th | - |
| | Display | Tvd | 900 | 900 | 900 | Th | - |
| | Blank | Tvb | $T_v - T_{vd}$ | 26 | $T_v - T_{vd}$ | Th | - |
| Horizontal Active Display Term | Total | Th | 750 | 800 | 960 | Tc | $T_h = T_{hd} + T_{hb}$ |
| | Display | Thd | 720 | 720 | 720 | Tc | - |
| | Blank | Thb | $T_h - T_{hd}$ | 80 | $T_h - T_{hd}$ | Tc | - |

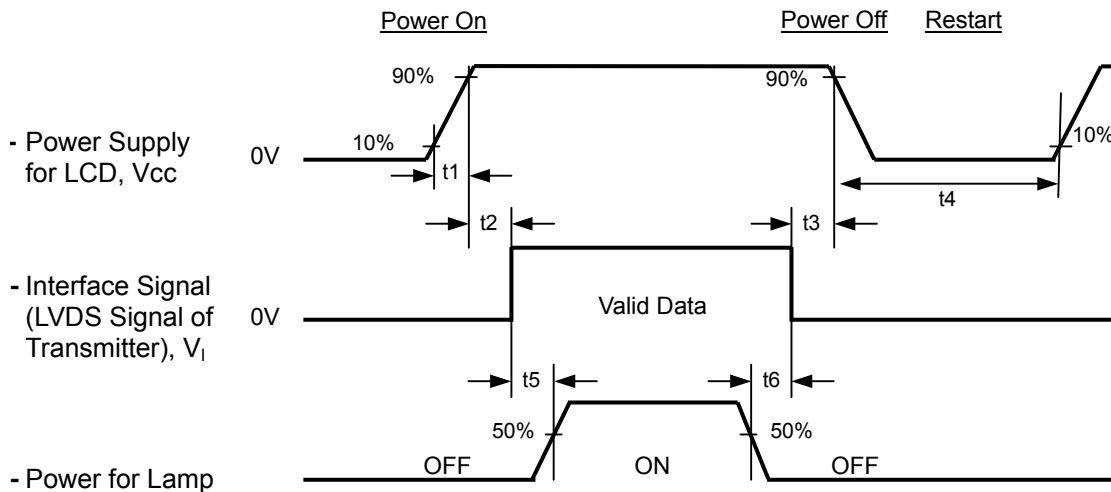
Note: Because this module is operated by DE only mode, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM



6.2 POWER ON/OFF SEQUENCE

To prevent a latch-up or DC operation of LCD module, the power on/off sequence should be as the diagram below.



Timing Specifications:

- $0.5 < t1 \leq 10 \text{ msec}$
- $0 < t2 \leq 50 \text{ msec}$
- $0 < t3 \leq 50 \text{ msec}$
- $t4 \geq 500 \text{ msec}$
- $t5 \geq 450 \text{ msec}$
- $t6 \geq 90 \text{ msec}$

Note.

- (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.
- (2) Apply the lamp voltage within the LCD operation range. When the backlight turns on before the LCD operation of the LCD turns off before the backlight turns off, the display may momentarily become abnormal screen.
- (3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

7. OPTICAL CHARACTERISTICS

7.1 TEST CONDITIONS

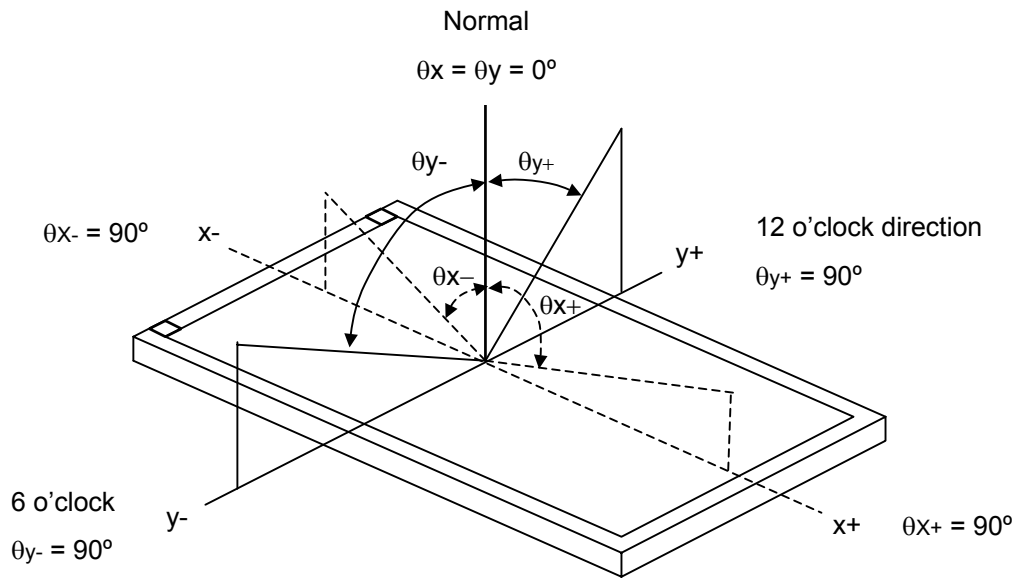
| Item | Symbol | Value | Unit |
|------------------------------|---|-------|------|
| Ambient Temperature | Ta | 25±2 | °C |
| Ambient Humidity | Ha | 50±10 | %RH |
| Supply Voltage | V _{CC} | 5.0 | V |
| Input Signal | According to typical value in "3. ELECTRICAL CHARACTERISTICS" | | |
| Lamp Current | I _L | 7.0 | mA |
| Inverter Operating Frequency | F _L | 61 | KHz |
| Inverter | SUMIDA H05-5307 | | |

7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (5).

| Item | | Symbol | Condition | Min. | Typ. | Max. | Unit | Note |
|---------------------------|------------|-----------------|--|------------|-------|------------|-------------------|----------|
| Color Chromaticity | Red | R _x | $\theta_x=0^\circ, \theta_Y=0^\circ$ CS-1000T | Typ – 0.03 | 0.640 | Typ + 0.03 | | (1), (5) |
| | | R _y | | | 0.334 | | | |
| | Green | G _x | | | 0.286 | | | |
| | | G _y | | | 0.599 | | | |
| | Blue | B _x | | | 0.154 | | | |
| | | B _y | | | 0.077 | | | |
| | White | W _x | | | 0.313 | | | |
| | | W _y | | | 0.329 | | | |
| Center Luminance of White | | L _C | | 230 | 300 | --- | cd/m ² | (4), (5) |
| Contrast Ratio | | CR | | 500 | 850 | --- | - | (2), (5) |
| Response Time | | T _R | $\theta_x=0^\circ, \theta_Y=0^\circ$ | --- | 1.5 | 6.5 | ms | (3) |
| | | T _F | | --- | 3.5 | 8.5 | ms | |
| White Variation | | δW | $\theta_x=0^\circ, \theta_Y=0^\circ$ | --- | 1.3 | 1.5 | - | (5), (6) |
| Viewing Angle | Horizontal | θ _{x+} | CR ≥ 10 | 75 | 85 | --- | Deg. | (1), (5) |
| | | θ _{x-} | | 75 | 85 | --- | | |
| | Vertical | θ _{y+} | | 70 | 80 | --- | | |
| | | θ _{y-} | | 70 | 80 | --- | | |

Note (1) Definition of Viewing Angle (θ_x , θ_y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{255} / L_0$$

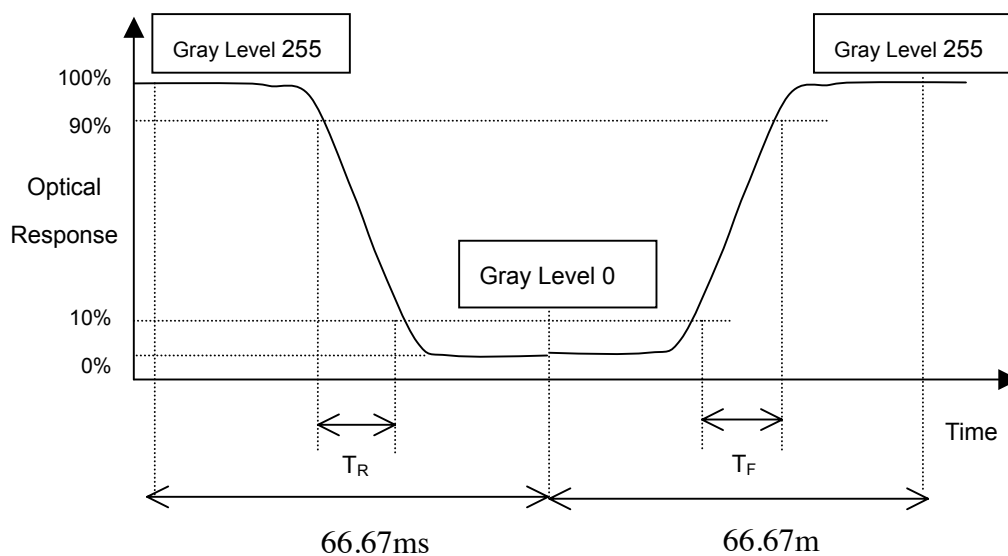
L255: Luminance of gray level 255

L 0: Luminance of gray level 0

$$\text{CR} = \text{CR} (1)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R , T_F):



Note (4) Definition of Luminance of White (L_C):

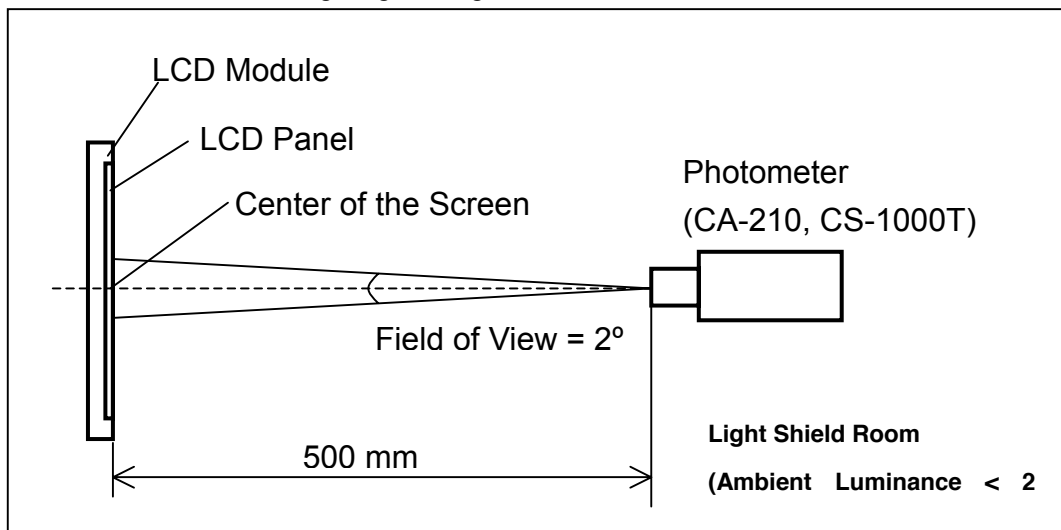
Measure the luminance of gray level 255 at center point

$$L_C = L(1)$$

$L(x)$ is corresponding to the luminance of the point X at Figure in Note (6).

Note (5) Measurement Setup:

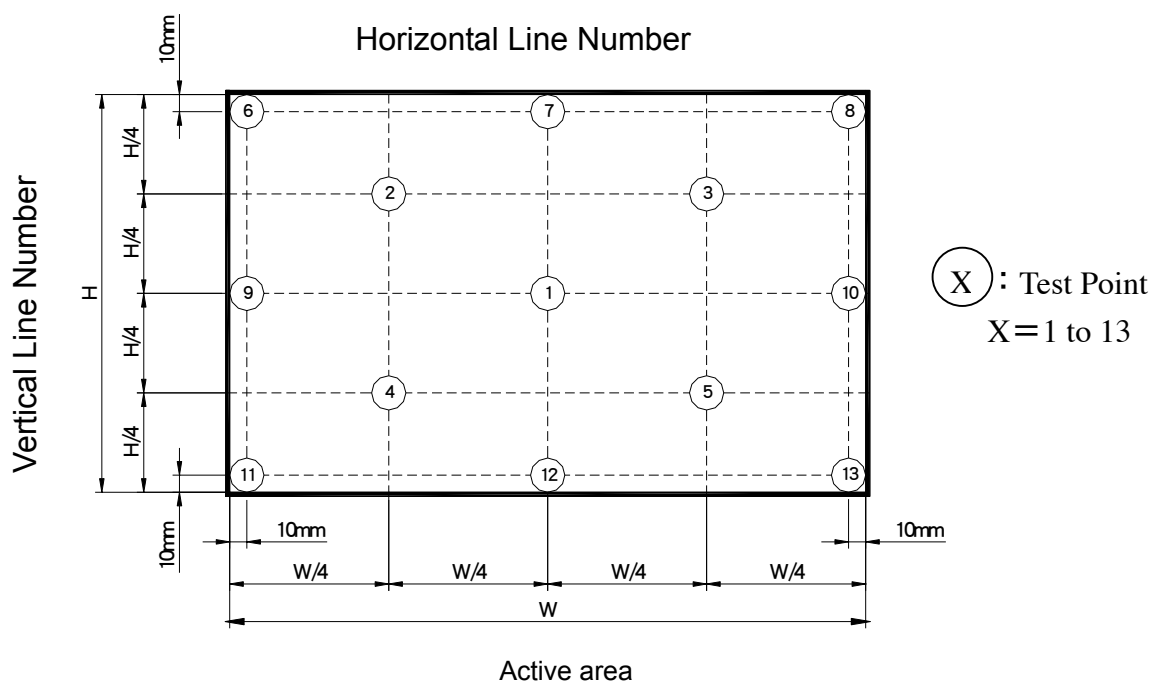
The LCD module should be stabilized at given temperature for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 15 minutes in a windless room.



Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 13 points

$$\delta W = \text{Maximum} [L(1), L(2) \dots L(4), L(13)] / \text{Minimum} [L(1), L(2) \dots L(4), L(13)]$$



8. PACKAGING

8.1 PACKING SPECIFICATIONS

- (1) 5 LCD modules / 1 Box
- (2) Box dimensions: 545(L) X 320 (W) X 360 (H) mm
- (3) Weight: approximately 13.87 Kg (5 modules per box)

8.2 PACKING METHOD

- (1) Carton Packing should have no failure in the following reliability test items.

| Test Item | Test Conditions | Note |
|---------------|--|---------------|
| Vibration | ISTA STANDARD Random, Frequency Range: 1 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y) | Non Operation |
| Dropping Test | 1 Angle, 3 Edge, 6 Face, 60cm | Non Operation |

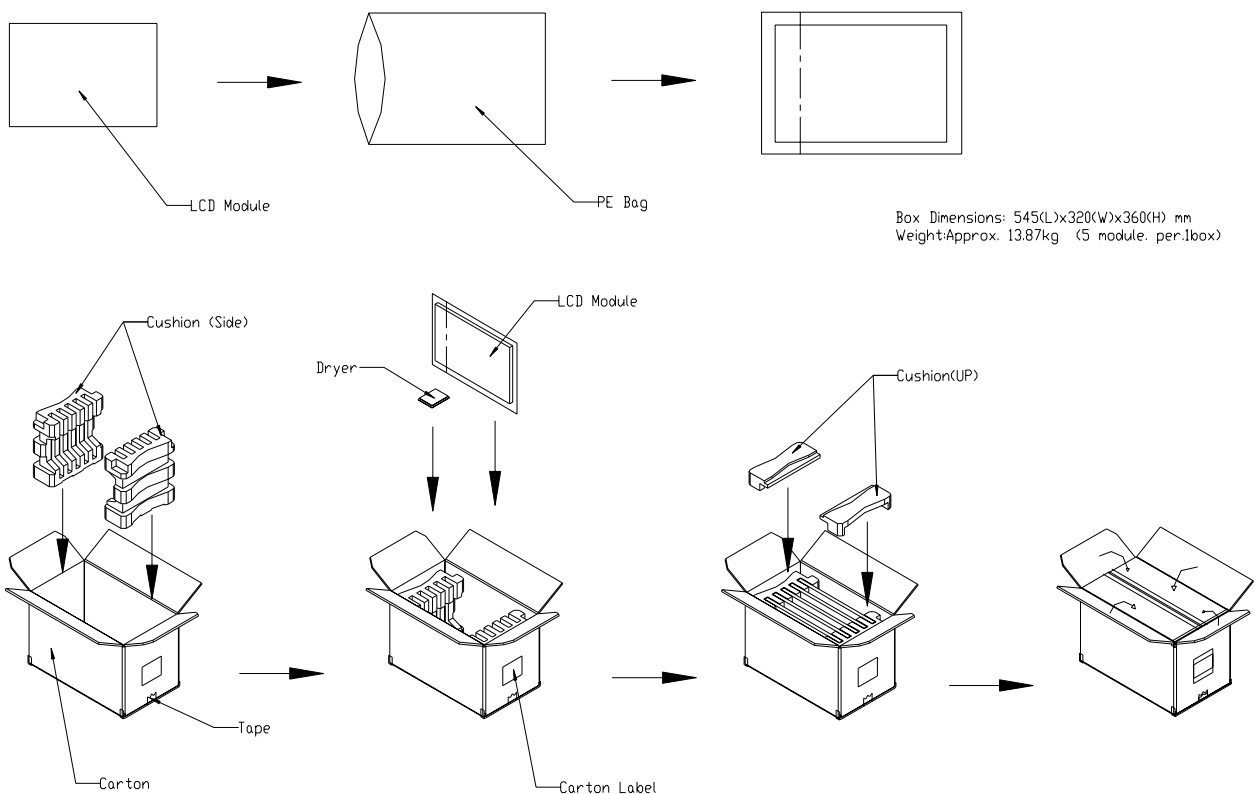
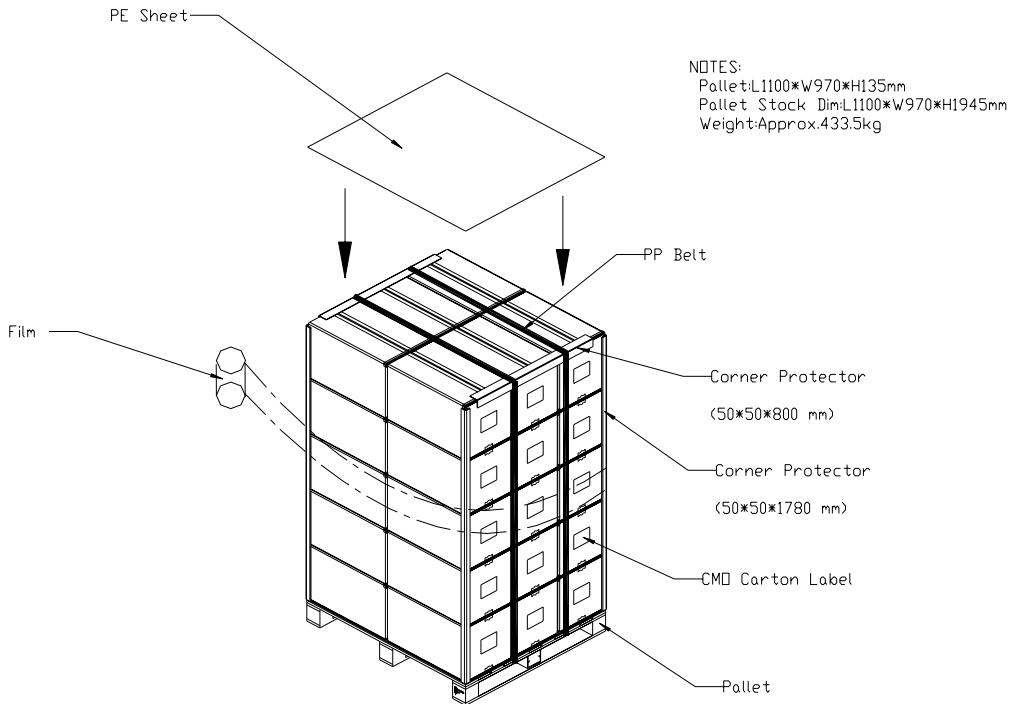


Figure. 8-1 Packing method

For ocean shipping



For air transport

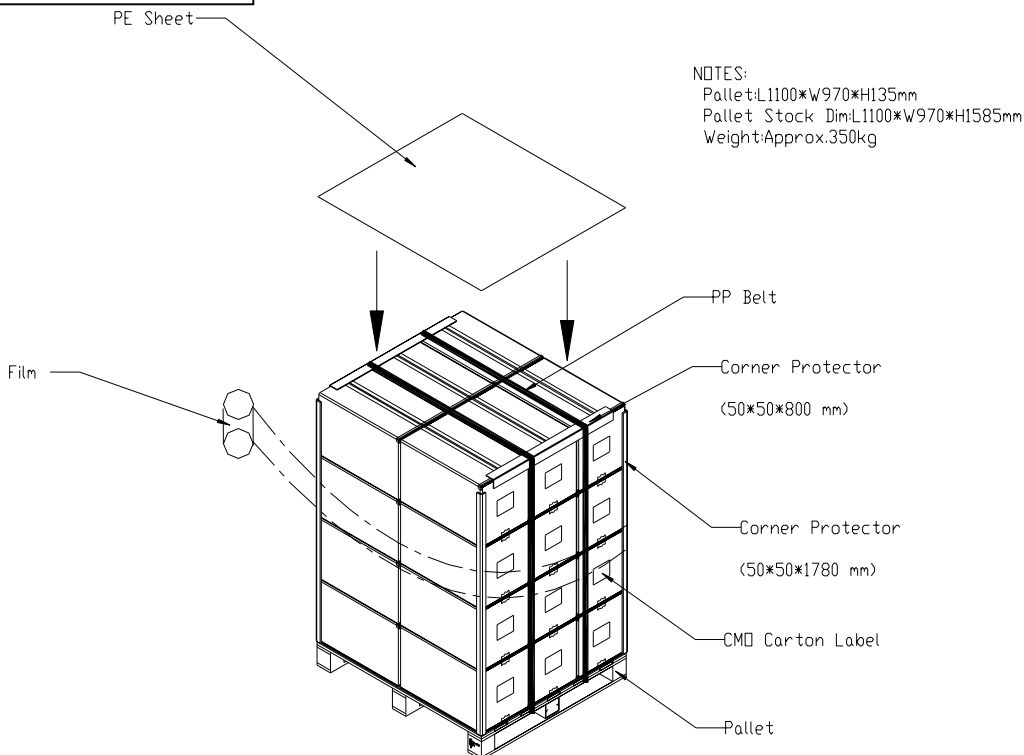


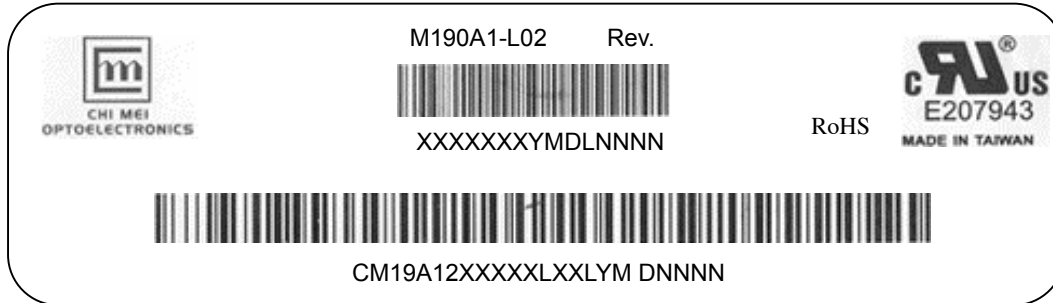
Figure. 8-2 Packing method

Figure. 8-3 Packing method

9. DEFINITION OF LABELS

9.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



- (a) Model Name: M190A1-L02
- (b) Revision: Rev. XX, for example: A0, A1... B1, B2... or C1, C2...etc.
- (c) CMO barcode definition:

Serial ID: XX-XX-X-XX-YMD-L-NNNN

| Code | Meaning | Description |
|------|------------------|---|
| XX | CMO internal use | - |
| XX | Revision | Cover all the change |
| X | CMO internal use | - |
| YMD | Year, month, day | Year: 2001=1, 2002=2, 2003=3, 2004=4... Month: 1~12=1, 2, 3, ~, 9, A, B, C Day: 1~31=1, 2, 3, ~, 9, A, B, C, ~, W, X, Y, exclude I, O, and U. |
| L | Product line # | Line 1=1, Line 2=2, Line 3=3, ... |
| NNNN | Serial number | Manufacturing sequence of product |

- (d) Customer's barcode definition:

Serial ID: CM-19A12-X-X-X-XX-L-XX-L-YMD-NNNN

| Code | Meaning | Description |
|-------|-----------------------|---|
| CM | Supplier code | CMO=CM |
| 19A12 | Model number | M190A1-L02=19A12 |
| X | Revision code | Non ZBD: 0~9, ZBD: A~Z |
| X | Source driver IC code | Century=1, CLL=2, Demos=3, Epson=4, Fujitsu=5, Himax=6, Hitachi=7, Hynix=8, LDI=9, Matsushita=A, NEC=B, Novatec=C, OKI=D, Philips=E, Renesas=F, Samsung=G, Sanyo=H, Sharp=I, TI=J, Topro=K, Toshiba=L, Windbond=M |
| X | Gate driver IC code | |
| XX | Cell location | Tainan, Taiwan=TN |
| L | Cell line # | 0~12=0~C |
| XX | Module location | Tainan, Taiwan=TN |
| L | Module line # | 0~12=0~C |
| YMD | Year, month, day | Year: 2001=1, 2002=2, 2003=3, 2004=4... Month: 1~12=1, 2, 3, ~, 9, A, B, C Day: 1~31= 1, 2, 3, ~, 9, A, B, C, ~, T, U, V |
| NNNN | Serial number | Manufacturing sequence of product |

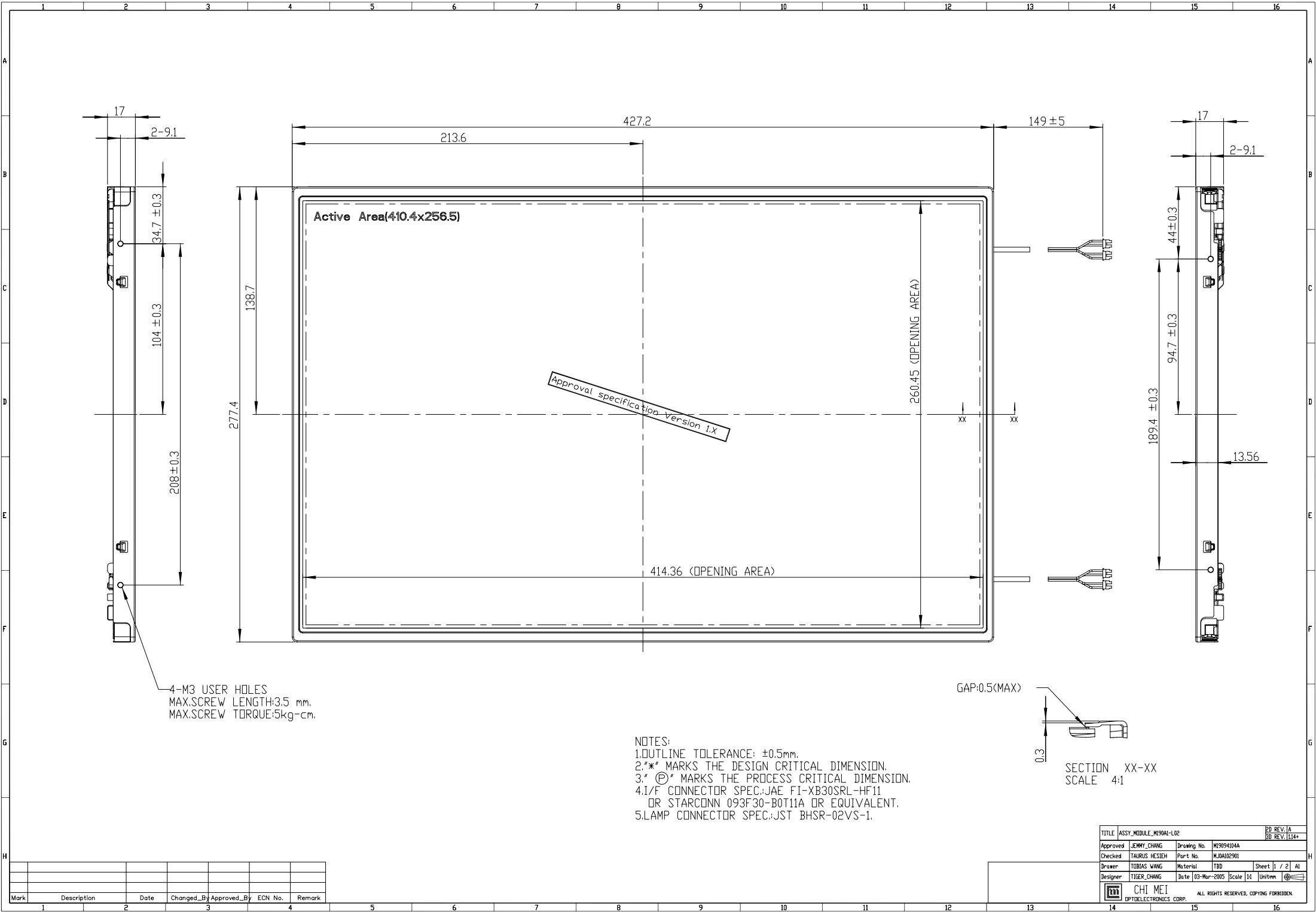
10. PRECAUTIONS

10.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly, and the starting voltage of CCFL will be higher than room temperature.

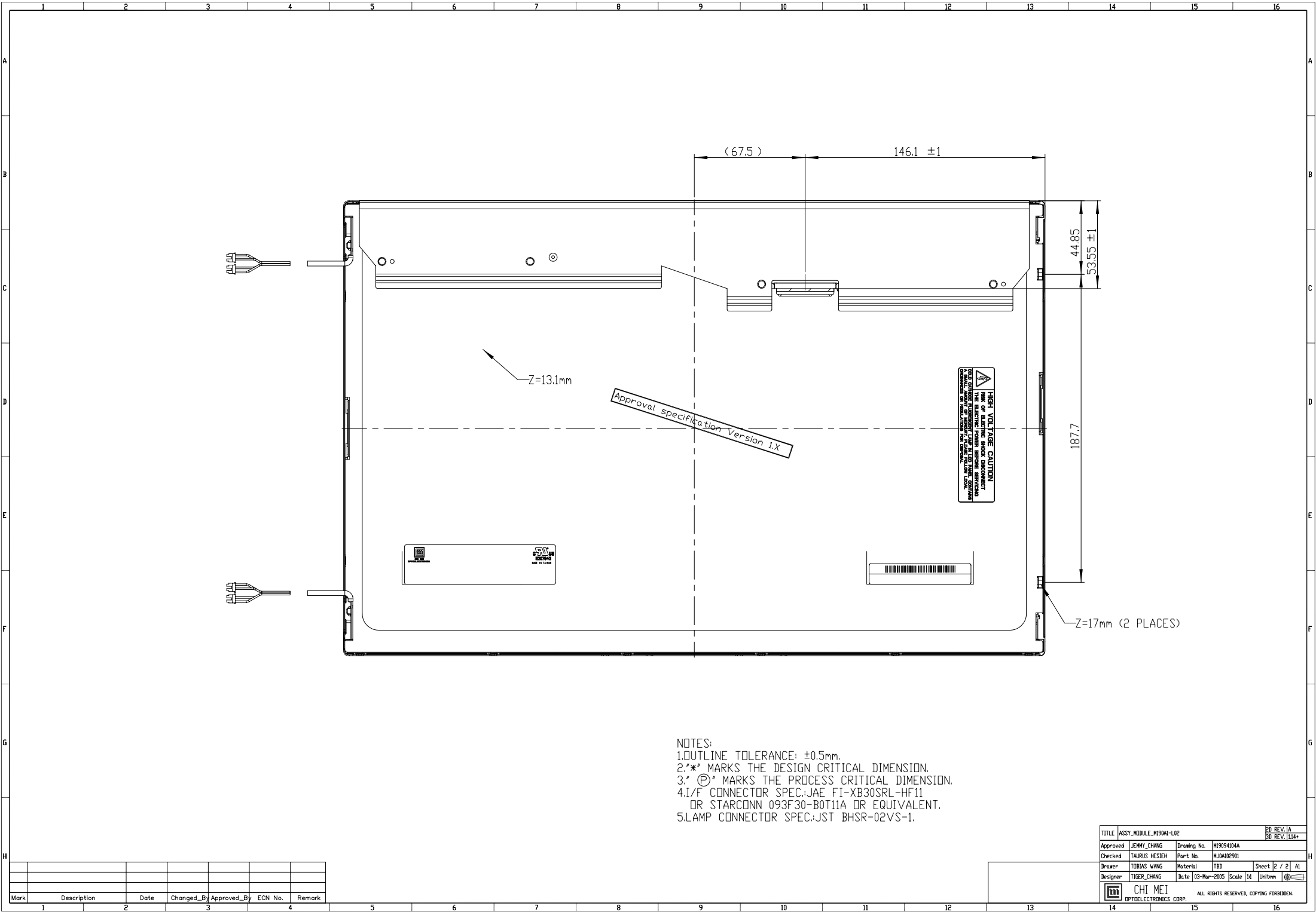
10.2 SAFETY PRECAUTIONS

- (1) The startup voltage of Backlight is approximately 1000 Volts. It may cause electrical shock while assembling with inverter. Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.



| Mark | Description | Date | Changed_By | Approved_By | ECN No. | Remark |
|------|-------------|------|------------|-------------|---------|--------|
| | | | | | | |
| | | | | | | |
| | | | | | | |

| | | | |
|-------------------------------|------------------------|---|--------|
| TITLE ASSY_MODULE_M1904I-L02 | | PD REV. 1A | |
| Approved JENNY CHANG | Drawing No. M19094104A | OD REV. 114* | |
| Checked TAURUS HESIEH | Part No. M10A02901 | | |
| Drawer TOBIAS WANG | Material TBD | Sheet 1 / 2 | AI |
| Designer TIGER CHANG | Date 03-Mar-2005 | Scale 1:1 | Unitmm |
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NOTES:
1.OUTLINE TOLERANCE: ±0.5mm.
2.*" MARKS THE DESIGN CRITICAL DIMENSION.
3.⊙ MARKS THE PROCESS CRITICAL DIMENSION.
4.I/F CONNECTOR SPEC.:JAE FI-XB30SRL-HF11
OR STARCONN 093F30-B0T11A OR EQUIVALENT.
5.LAMP CONNECTOR SPEC.:JST BHSR-02VS-1.

| Mark | Description | Date | Changed_By | Approved_By | ECN No. | Remark |
|------|-------------|------|------------|-------------|---------|--------|
| | | | | | | |
| | | | | | | |
| | | | | | | |

| | | | | | |
|-------------------------------|--|------------------------|-----------|---|----|
| TITLE ASSY_MODULE_M190A1-L02 | | | | PD REV. 1A | |
| Approved JENNY CHANG | | Drawing No. M19094104A | | OD REV. 1114* | |
| Checked TAURUS HSEIH | | Part No. M10A102901 | | | |
| Drawer TOBIAS WANG | | Material TBD | | Sheet 2 / 2 | AI |
| Designer TIGER CHANG | | Date 03-Mar-2005 | Scale 1:1 | Unitmm | ④ |
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