

[illegible]

**Vout=0.765 x [(RA+RB)/RB]**

[illegible]

The schematic shows a TPS427D02 voltage regulator configured as a voltage follower. The input is a +12V PWR source. The feedback network consists of resistors R204 (100k) and R205 (99k) connected to the FB pin. The output is 1.31V. The regulator's output is connected to a load through a 2.2uF capacitor (C87). The feedback pin is connected to the output through a 100nF capacitor (C90). The regulator's internal compensation network includes capacitors C88 (16uF) and C89 (100uF). The regulator's output is also connected to a 1.3V VDDC output through a 100nF capacitor (C90).

**Vout=0.765 x [(RA+RB)/RB]**

U7 TLV117-33CDMVR

3 VIN

2 VOUT

1 ADJ/GND

4 TAB(V)

TLV117-33

+5V Standby

C15 100n

0R0

+3.3V Standby

R38

EC5 16V 100n

C16 100n

R21 0R0

TP254

R15 10K

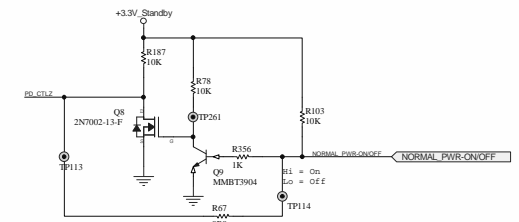
R1 10K

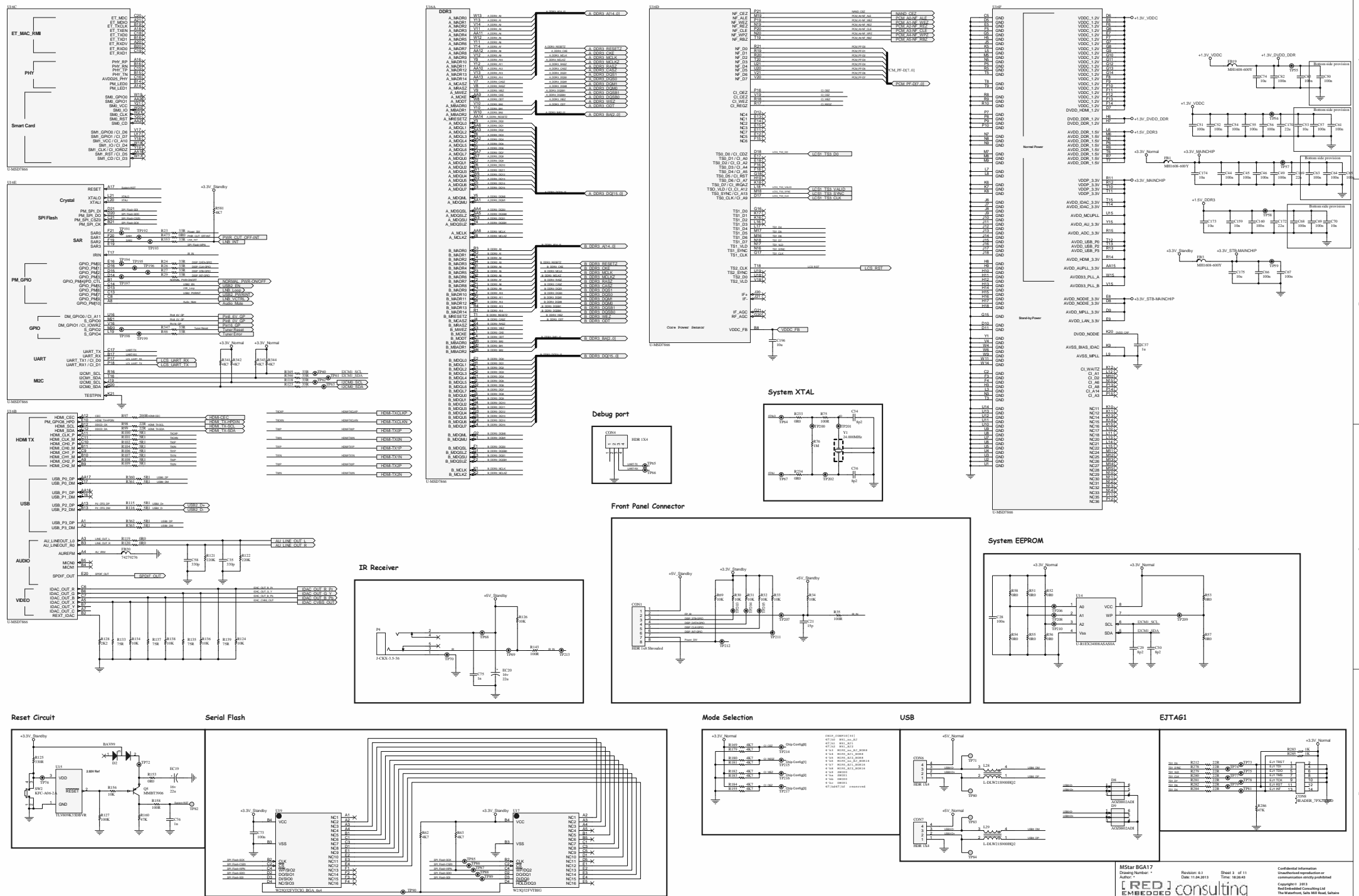
RF2

$$V_{out} = V_{ref} \times (1 + (R2/R1)) + I_{adj} \times R2$$

$$V_{ref} = 1.25V$$

$V_{out} = 0.765 \times [(R_A + R_B) / R_B]$







A

B

C

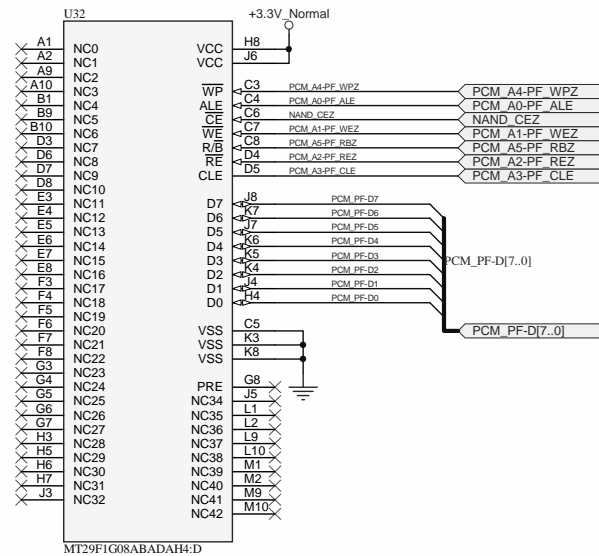
D

A

B

C

D

**NAND Flash**

Drawing Number: \*  
Author: \*

Revision: 0.1  
Date: 11.04.2013

Sheet 5 of 11  
Time: 18:26:44

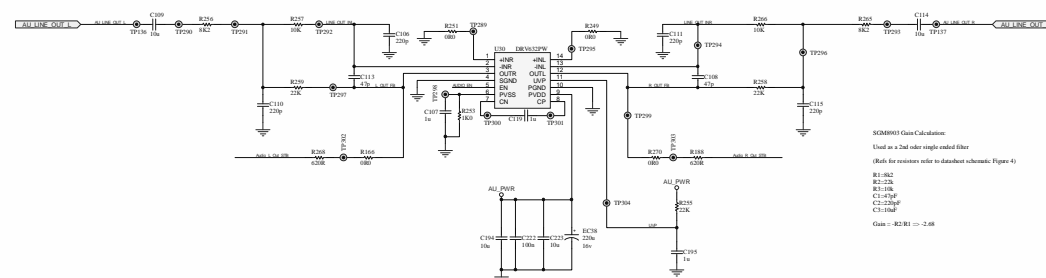
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The schematic diagram illustrates the U-1HS7749PWK receiver module. The module is represented as a black rectangular component with pins on both sides. The top pins are labeled 1 through 100, and the bottom pins are labeled 101 through 200. The module is connected to a power supply (VCC, GND) and a signal source (RF IN). The output is connected to a 50-ohm load. The module is labeled "U-1HS7749PWK".

[illegible][illegible]

<p>Video / Audio Output          Drawing Number: *          Author: *</p>	<p>Revision: 0.3          Date: 11.04.2013</p>	<p>Sheet 6 of 11          Time: 18:26:44</p>	<p>Confidential information          Unauthorised reproduction or          communication strictly prohibited</p>
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A

B

C

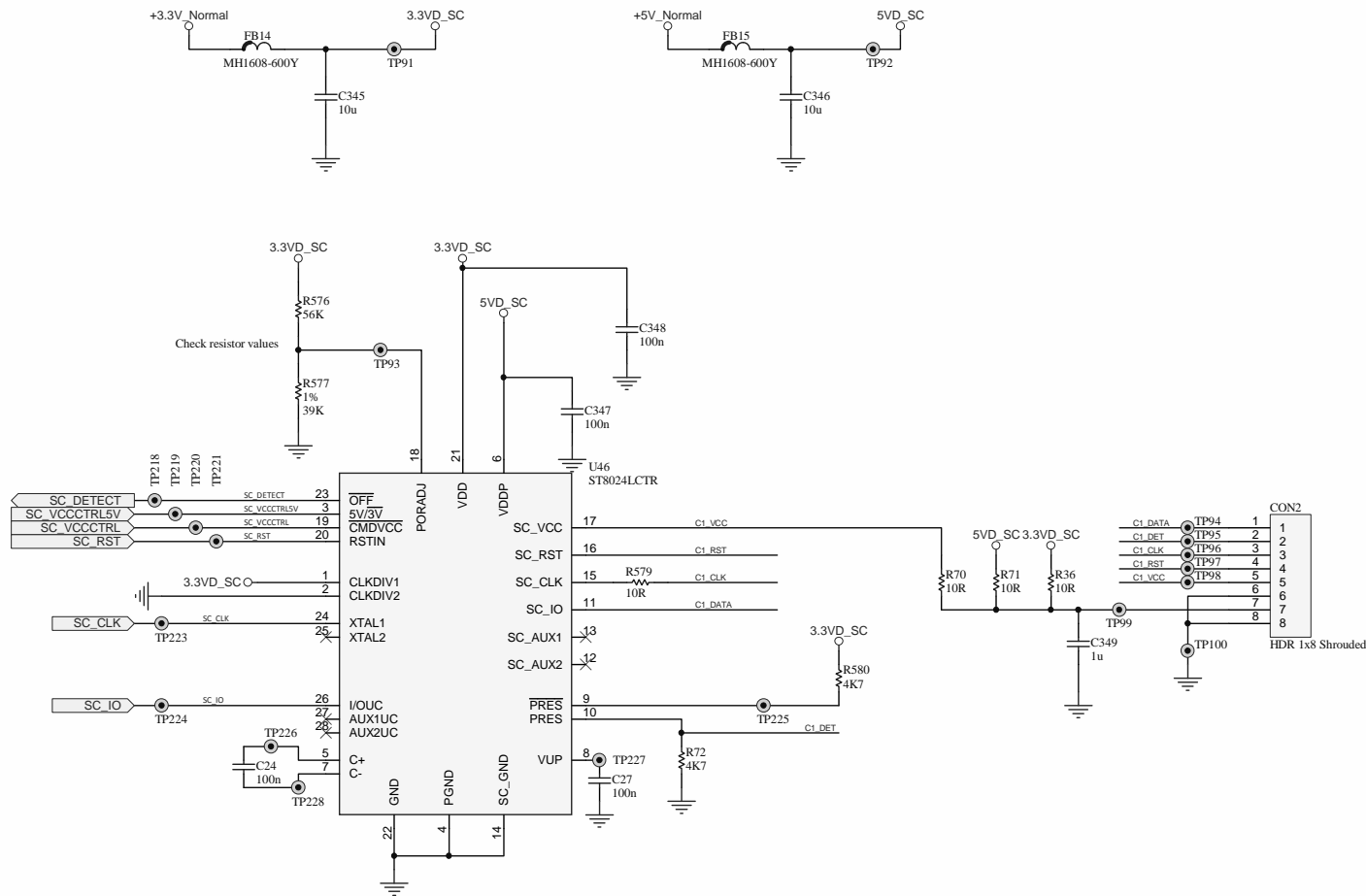
D

A

B

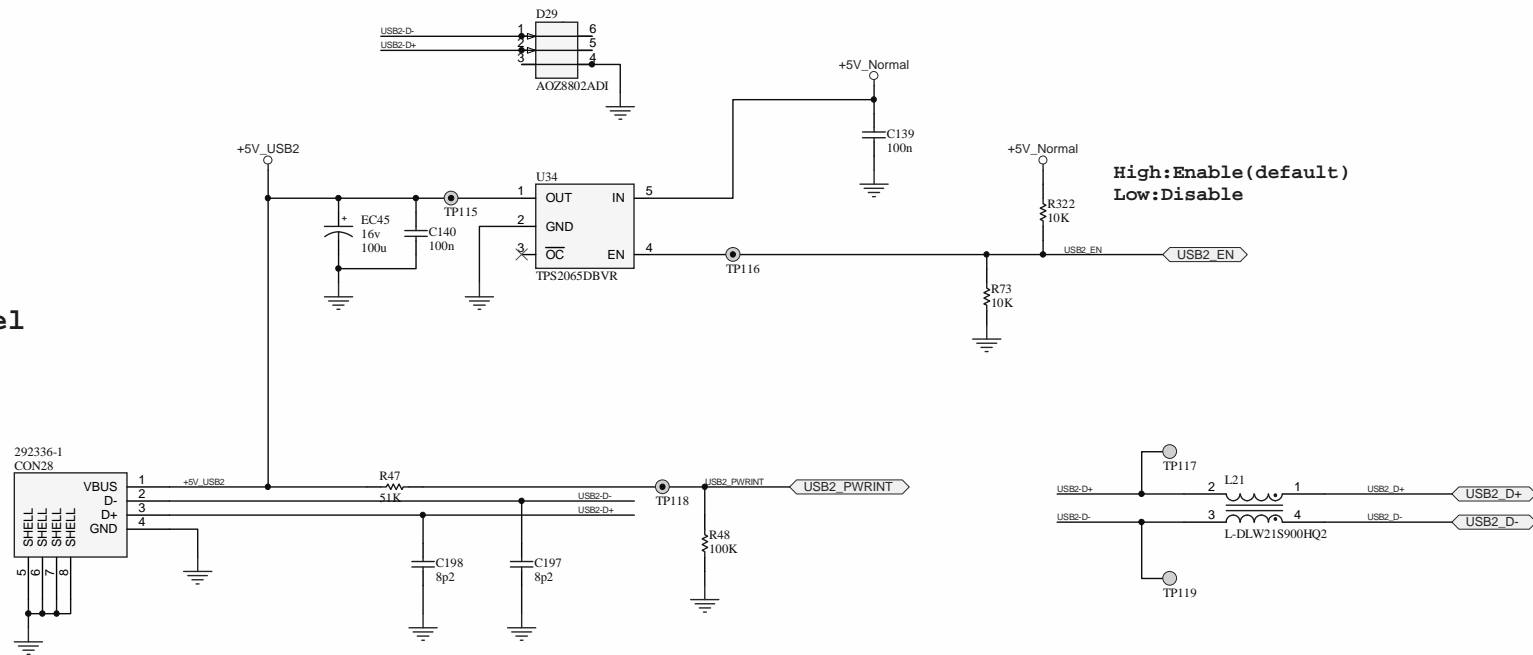
C

D





## USB Rear Panel



USB

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Author: \*

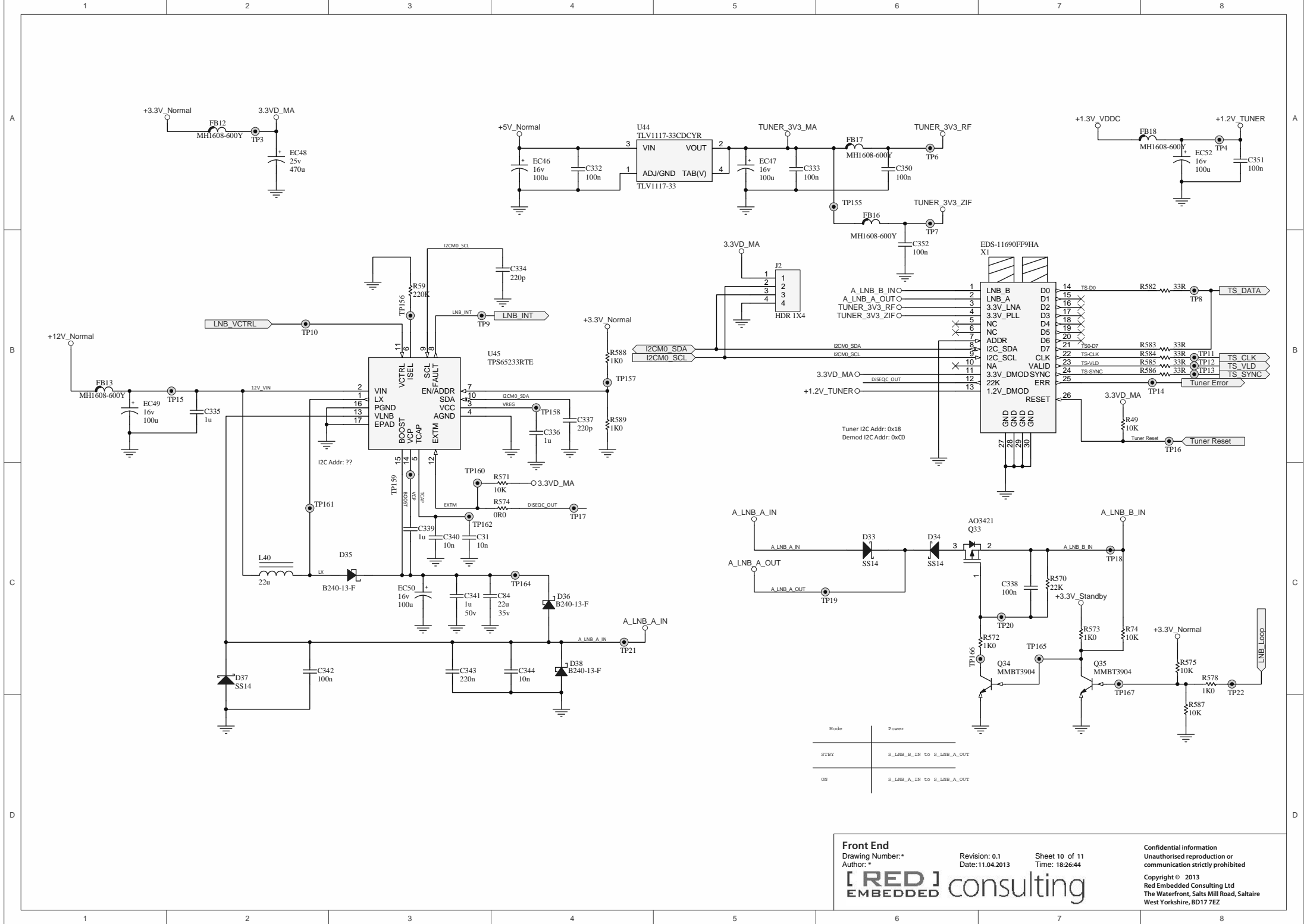
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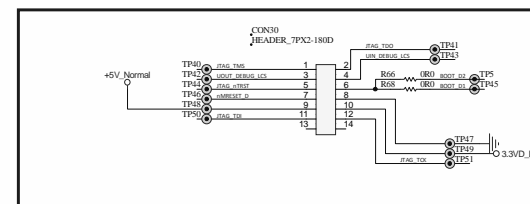
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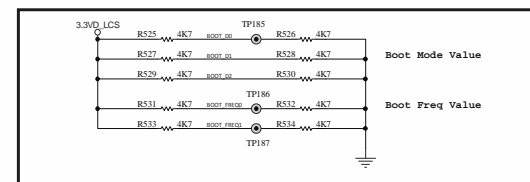
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C



### Boot setting



## SPI Serial Flash

