

MS-6717 V.0B

CPU: AMD Socket-462 Processor
Chipset: SIS 746

SIS 963 SOUTH BRIDGE

LPC I/O: W83697HF
AC'97 CODEC:Realtek ALC650
Expansion: PCI Slot * 5

MSI Power On / Suspend LED State

BIOS	S0	S1/S3	S5
Single Color	Green	Off	Off
Dual Color	Green	Red	Off
Green: MSI_LED1=' 1' MSI_LED2=' 0' Red : MSI_LED1=' 0' MSI_LED2=' 1' Single Color S1/S3 status: MSI_LED1=' 0' MSI_LED2=' 1'			

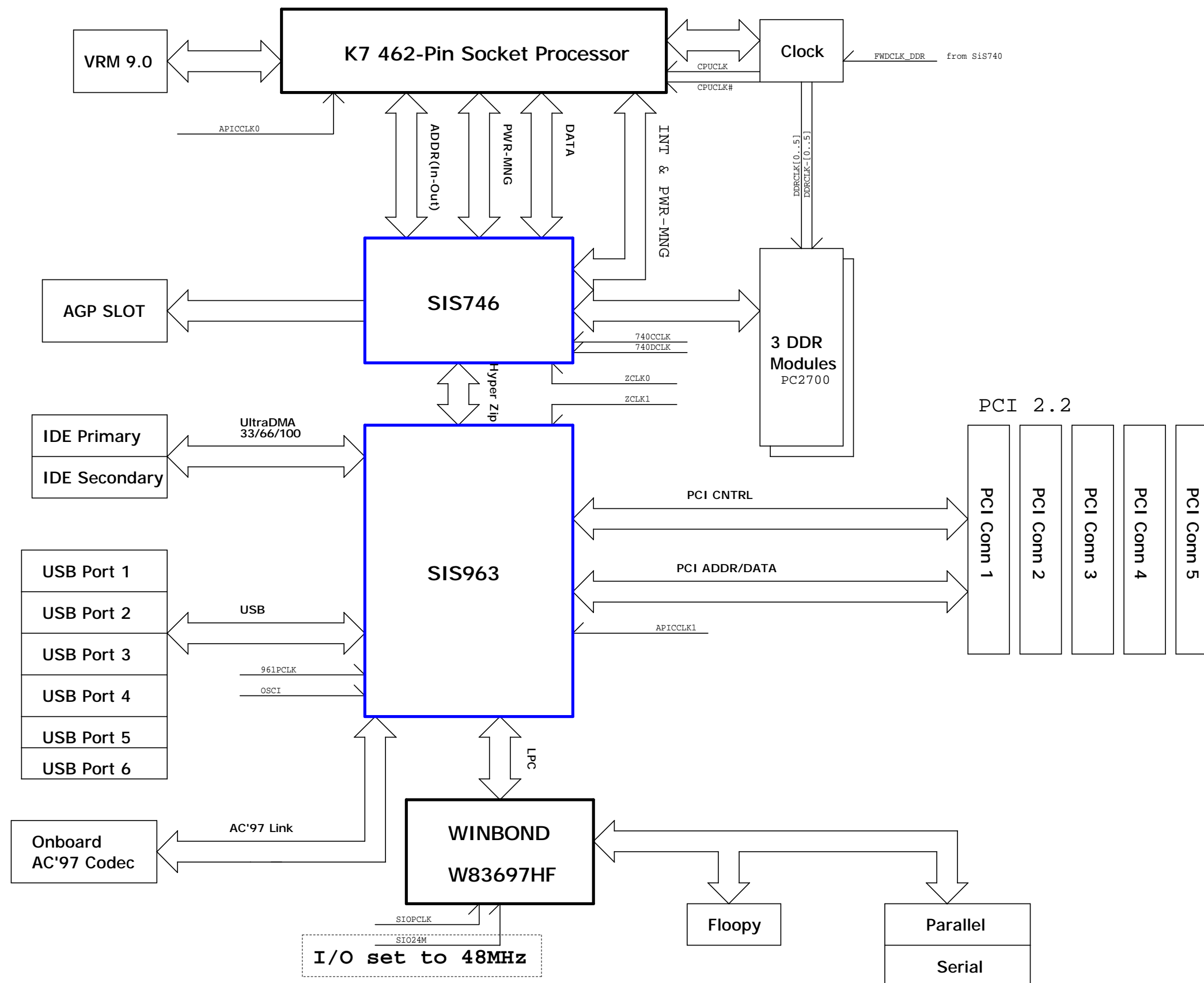
MSI ACPILED & VDIM state

BIOS	S0	S1/S3	S5
Single Color	ACPILED:Low VDIM :Hi	ACPILED:Hi & VDIM:Hi	ACPILED:Hi-z VDIM :Low
Dual Color	ACPILED:Low VDIM :Hi	ACPILED:Hi & VDIM:Hi	ACPILED:Hi-z VDIM :Low
Note : 1). 1Hz means Blinking frequency of ACPILED signal. 2). VDIM is no voltage in S5 state.			

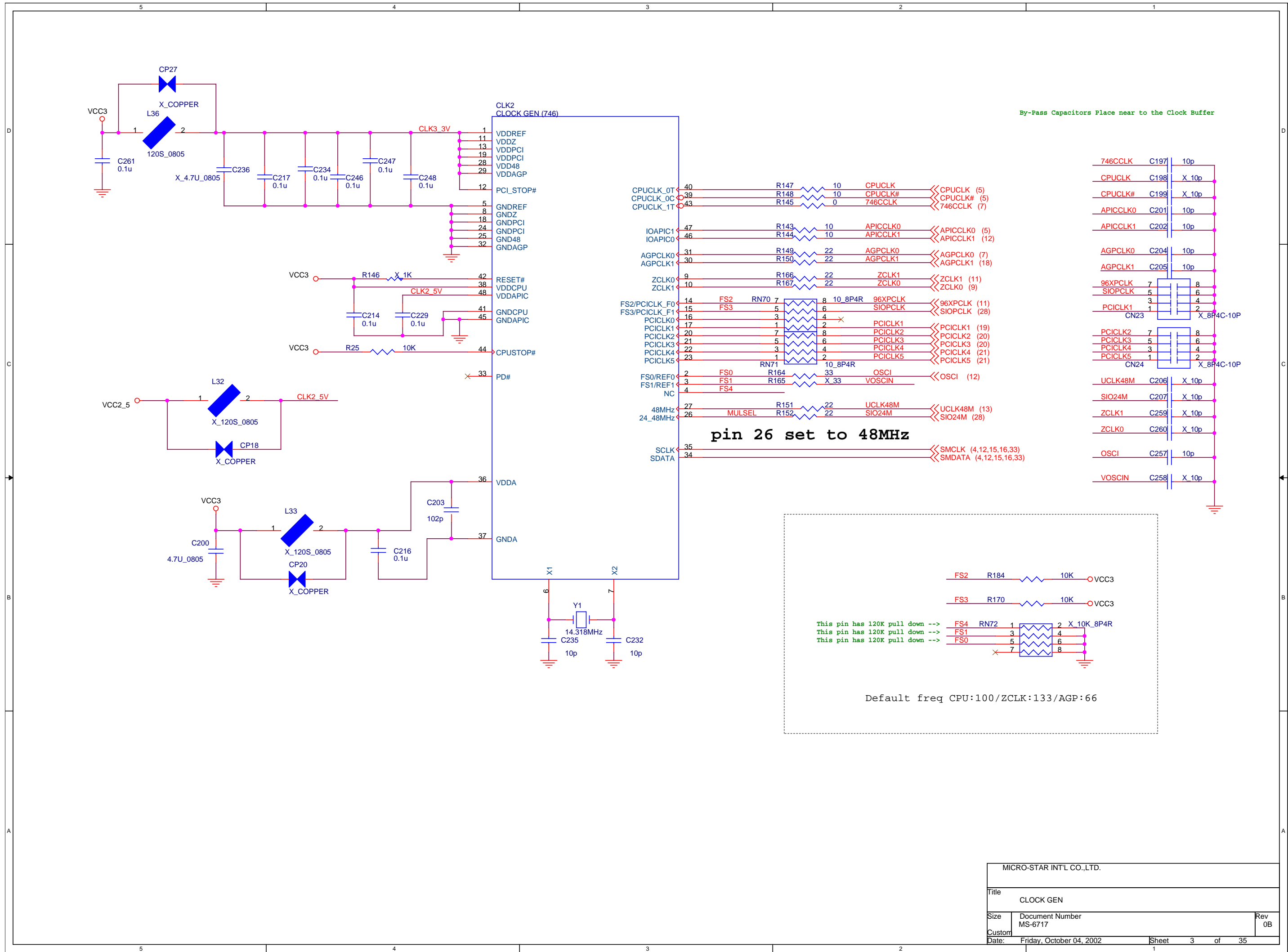
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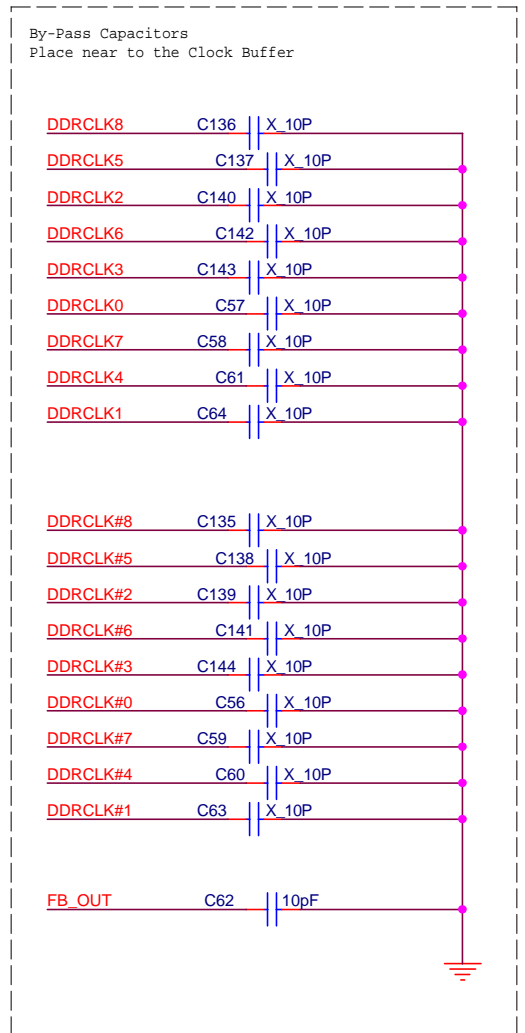
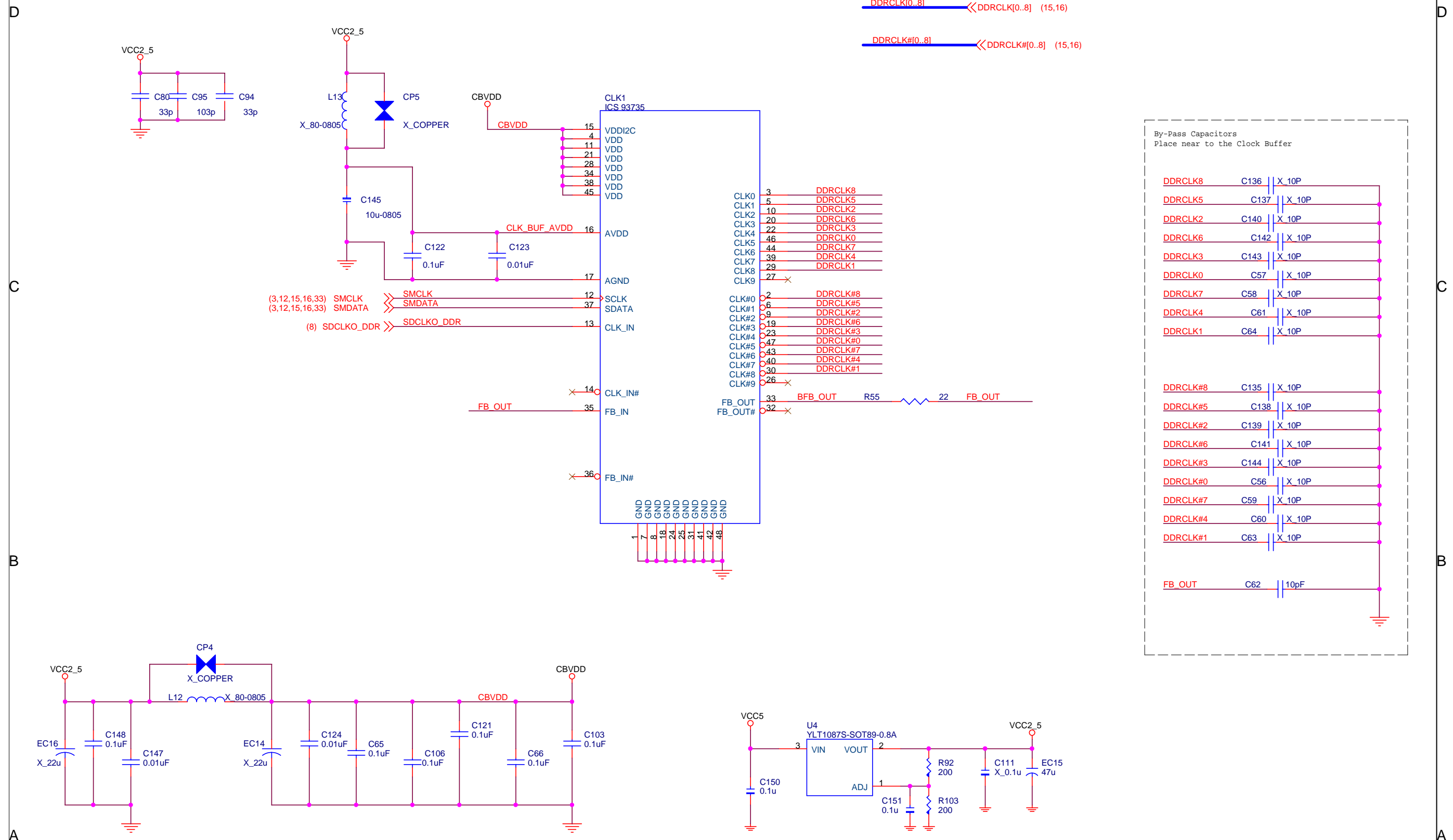
MS-6717 Block Diagram



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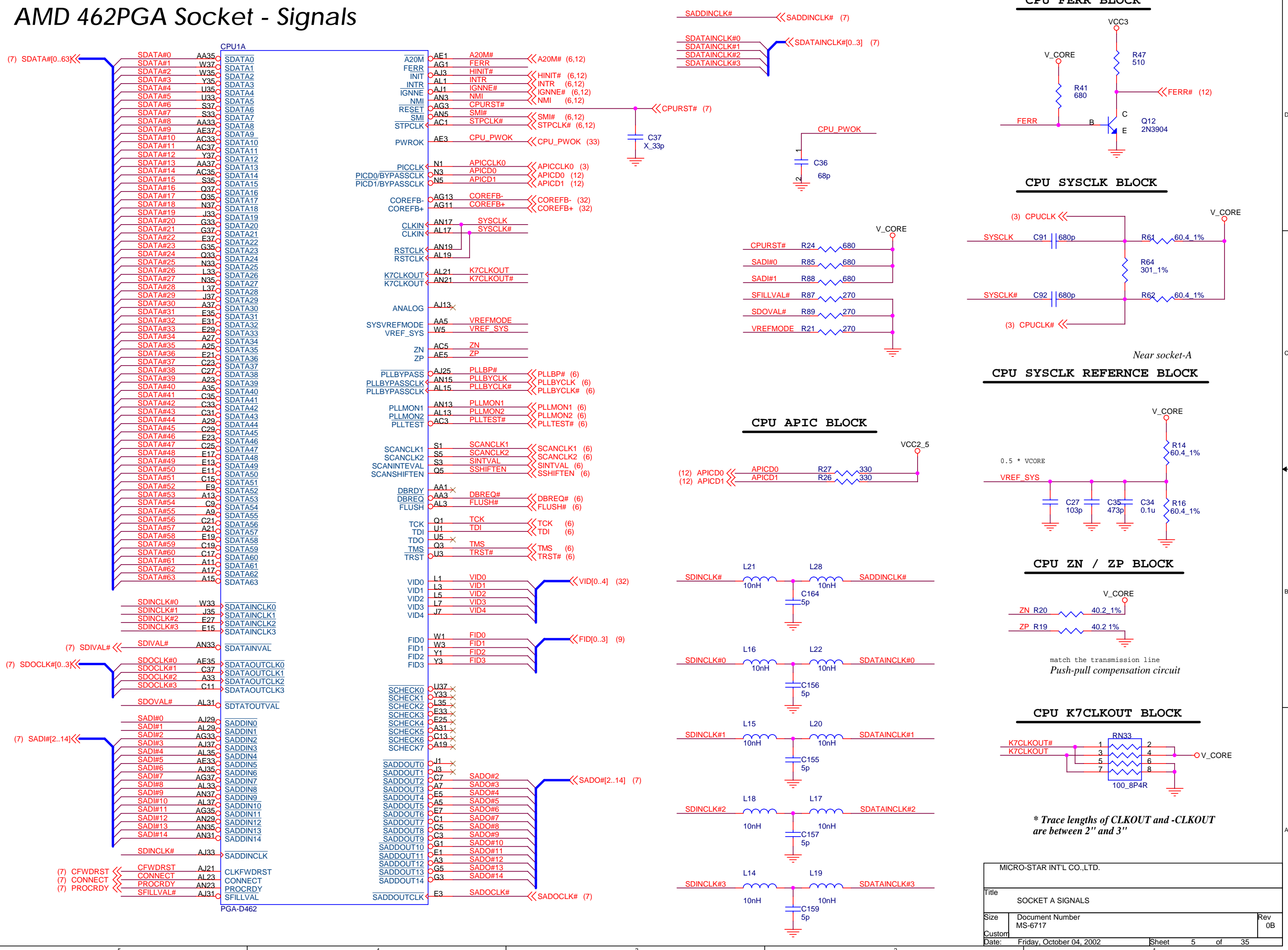


Clock Buffer (FOR 3 DDR SDRAM DIMMS)



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AMD 462PGA Socket - Signals



[illegible]

CPU DECOUPLING CAPACITORS

DECOUPLING CAPACITOR
Place on Inside socket.

The diagram illustrates the placement of decoupling capacitors for the CPU V_CORE supply. It is divided into two main sections, labeled 5 and 4. Section 5 shows capacitors C129, C83, C127, C132, C131, C76, C74, C71, C104, C73, C97, C146, C75, C79, C100, C72, and C125. Section 4 shows capacitors C133, C105, C77, C69, C134, C113, C93, C90, C126, C68, C116, C117, C109, C89, C84, C88, C85, C110, C128, C130, C114, C112, C98, C81, and C87. Each capacitor is connected to the V_CORE supply and ground.

place on
solder side
inside socket

V_CORE

The diagram shows a vertical column of 14 pins. A red circle at the top is labeled 'V_CORE'. A blue vertical line runs through the center of the pins. To the left of the line are the pin numbers: C431, C425, C70, C78, C118, C428, C434, C432, C429, C427, C426, C433, and C430. To the right of the line are the functions: X 224P, X 224F, X 33p, X 224F, X 39P, X 224F, X 224F, X 33p, X 33p, X 33p, X 33p, X 33p, and X 33p. A ground symbol is at the bottom right.

Pin	Function
C431	X 224P
C425	X 224F
C70	X 33p
C78	X 224F
C118	X 39P
C428	X 224F
C434	X 224F
C432	X 33p
C429	X 33p
C427	X 33p
C426	X 33p
C433	X 33p
C430	X 33p

CPU PULL-UP / DOWN BLOCK

The diagram illustrates the CPU Pull-Up / Down Block, which manages the voltage levels of various CPU signals. It features three main components: RN76 (680_8P4R), RN77 (680_8P4R), and RN30 (100_8P4R). The signals are connected to V_CORE or ground through these components.

Signal Connections:

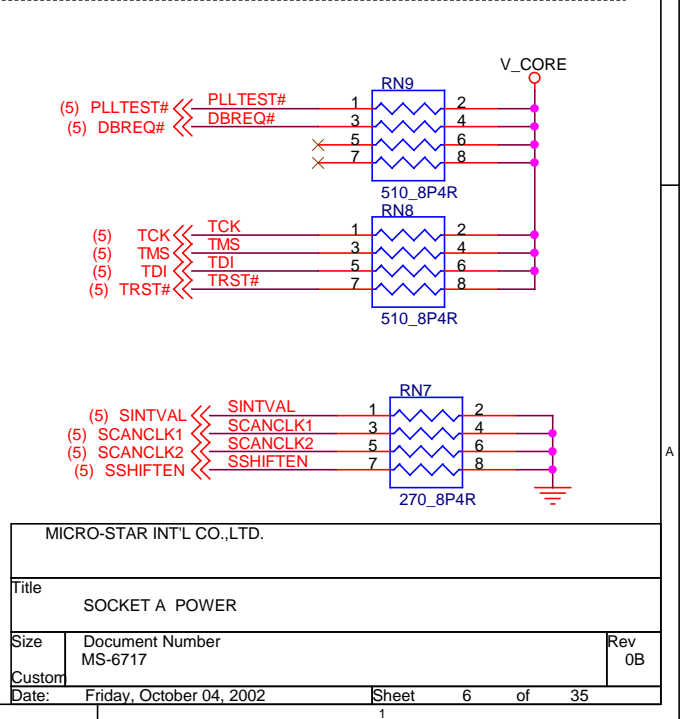
- IGNNE#** (5,12) to **IGNNE#** (pin 1 of RN76) to **V_CORE** (pin 2 of RN76).
- A20M#** (5,12) to **A20M#** (pin 3 of RN76) to **V_CORE** (pin 4 of RN76).
- STPCLK#** (5,12) to **STPCLK#** (pin 5 of RN76) to **V_CORE** (pin 6 of RN76).
- HINIT#** (5,12) to **HINIT#** (pin 1 of RN77) to **V_CORE** (pin 2 of RN77).
- SMI#** (5,12) to **SMI#** (pin 3 of RN77) to **V_CORE** (pin 4 of RN77).
- INTR** (5,12) to **INTR** (pin 5 of RN77) to **V_CORE** (pin 6 of RN77).
- NMI** (5,12) to **NMI** (pin 7 of RN77) to **V_CORE** (pin 8 of RN77).
- FLUSH#** (5) to **FLUSH#** (pin R45) to **V_CORE** (pin 680 of RN30).
- PLLBP#** (5) to **PLLBP#** (pin R82) to **V_CORE** (pin 680 of RN30).
- PLLMON1** (5) to **PLLMON1** (pin R63) to **V_CORE** (pin 56 of RN30).
- PLLMON2** (5) to **PLLMON2** (pin R60) to **V_CORE** (pin 56 of RN30).
- PLLBYCLK** (5) to **PLLBYCLK** (pin 1 of RN30) to **V_CORE** (pin 2 of RN30).
- PLLBYCLK#** (5) to **PLLBYCLK#** (pin 3 of RN30) to **GROUND** (pin 6 of RN30).

Component Details:

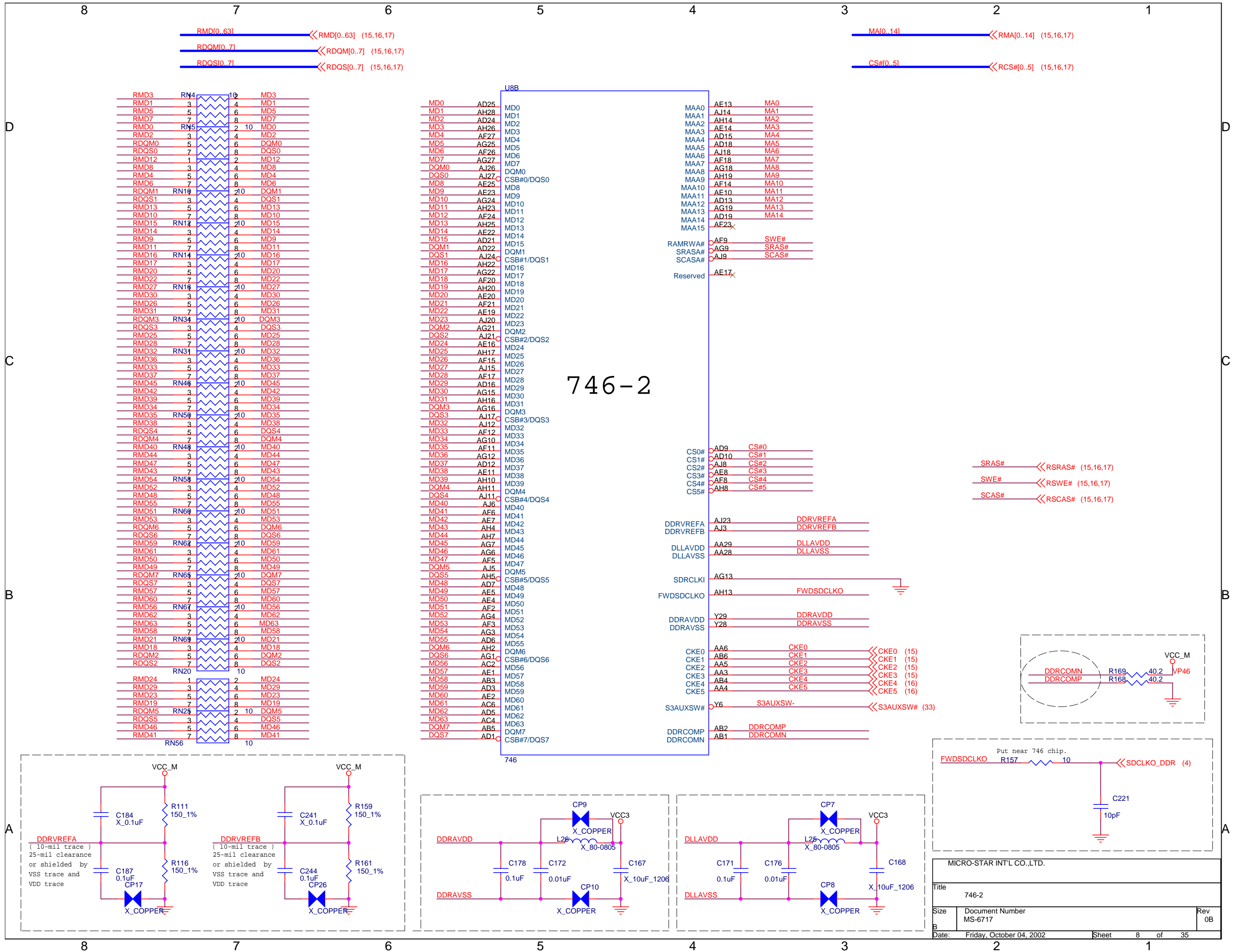
- RN76:** 680_8P4R
- RN77:** 680_8P4R
- RN30:** 100_8P4R

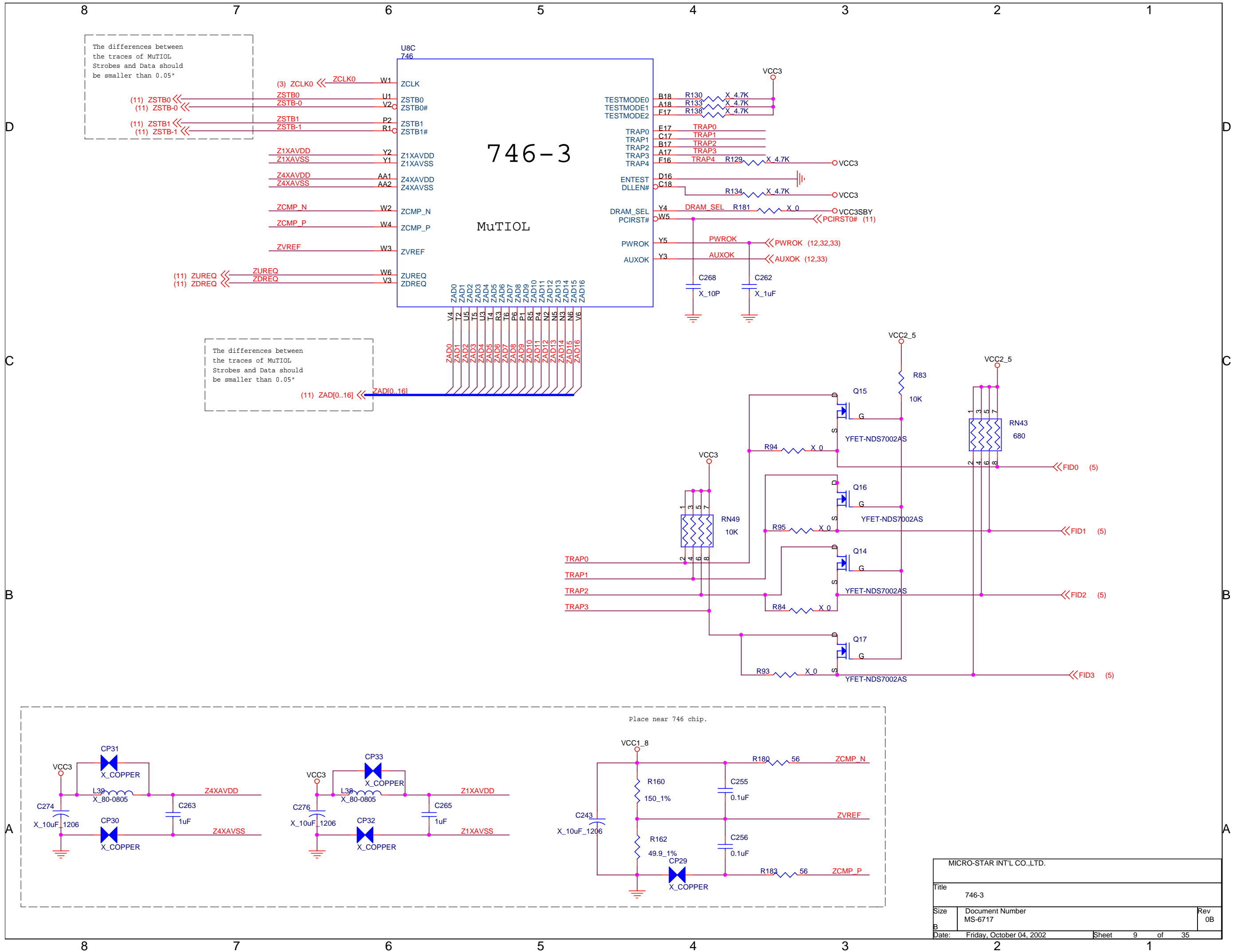
Pin Connections:

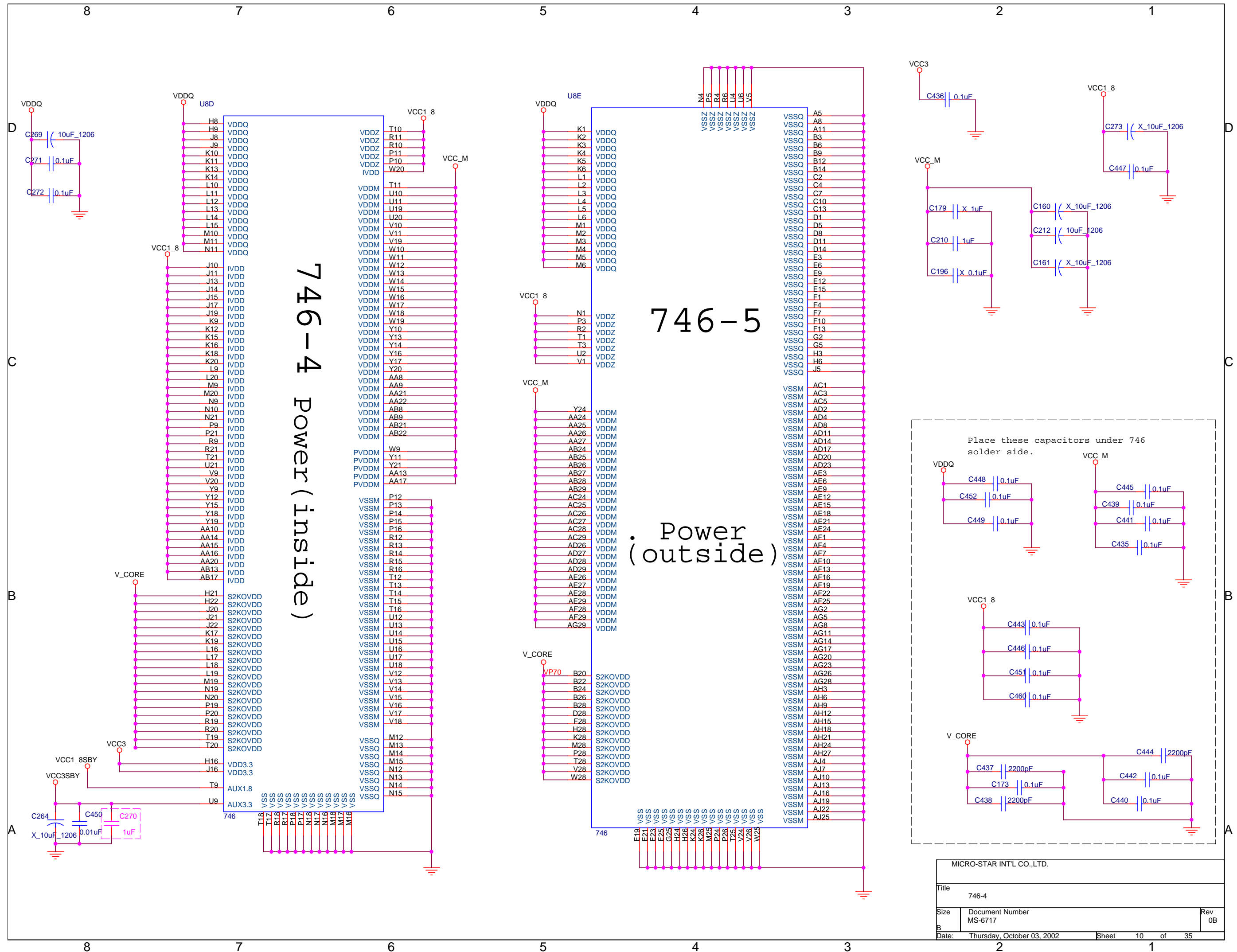
- V_CORE:** Connected to pins 2, 4, 6, 8 of RN76, 2, 4, 6, 8 of RN77, and 680, 56 of RN30.
- GROUND:** Connected to pin 6 of RN30.

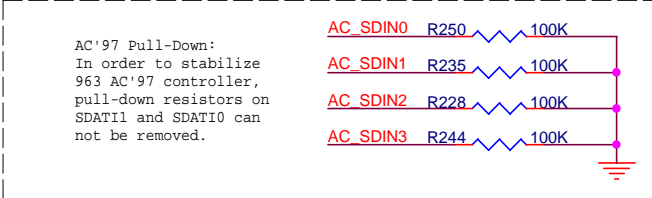
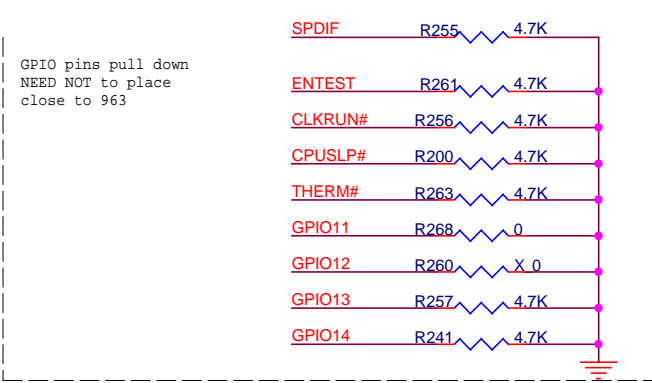
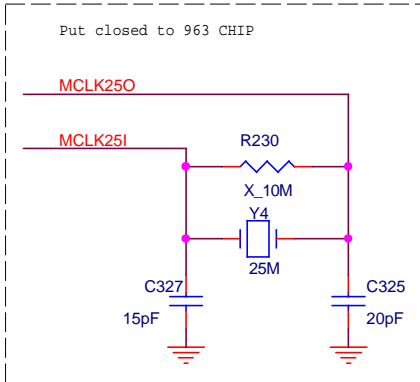
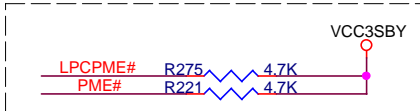
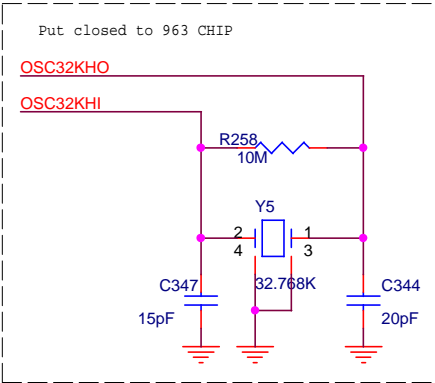
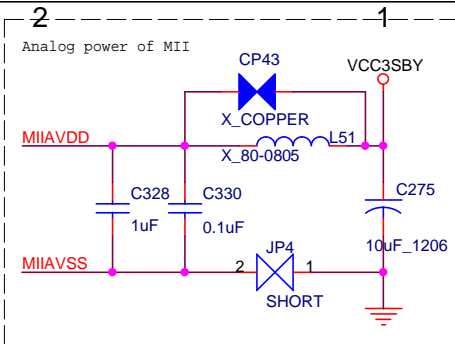
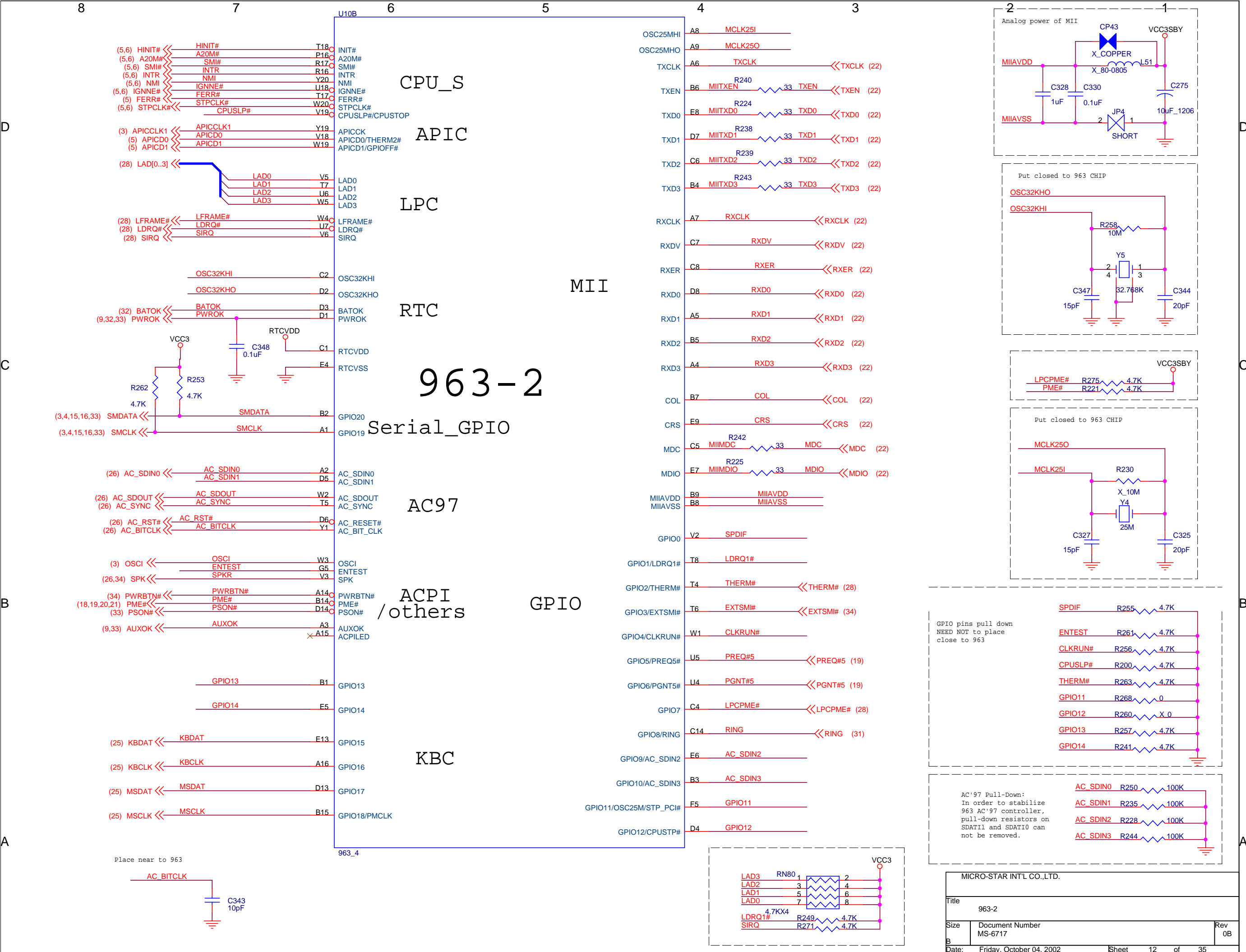




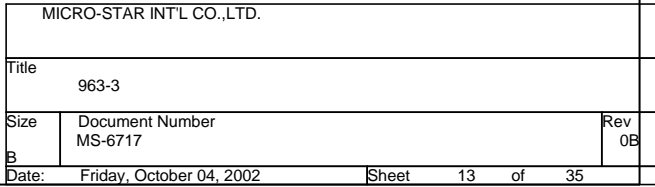


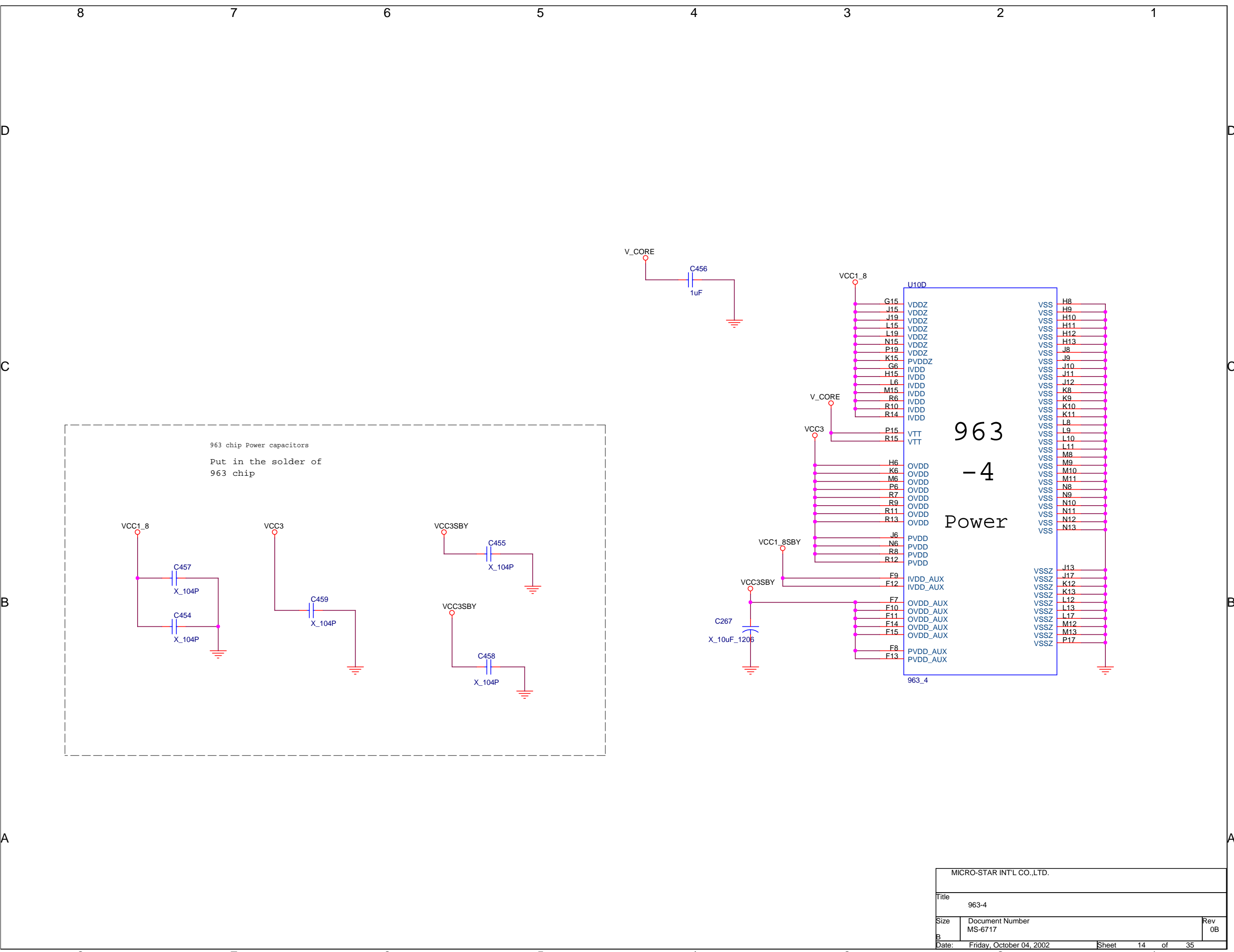




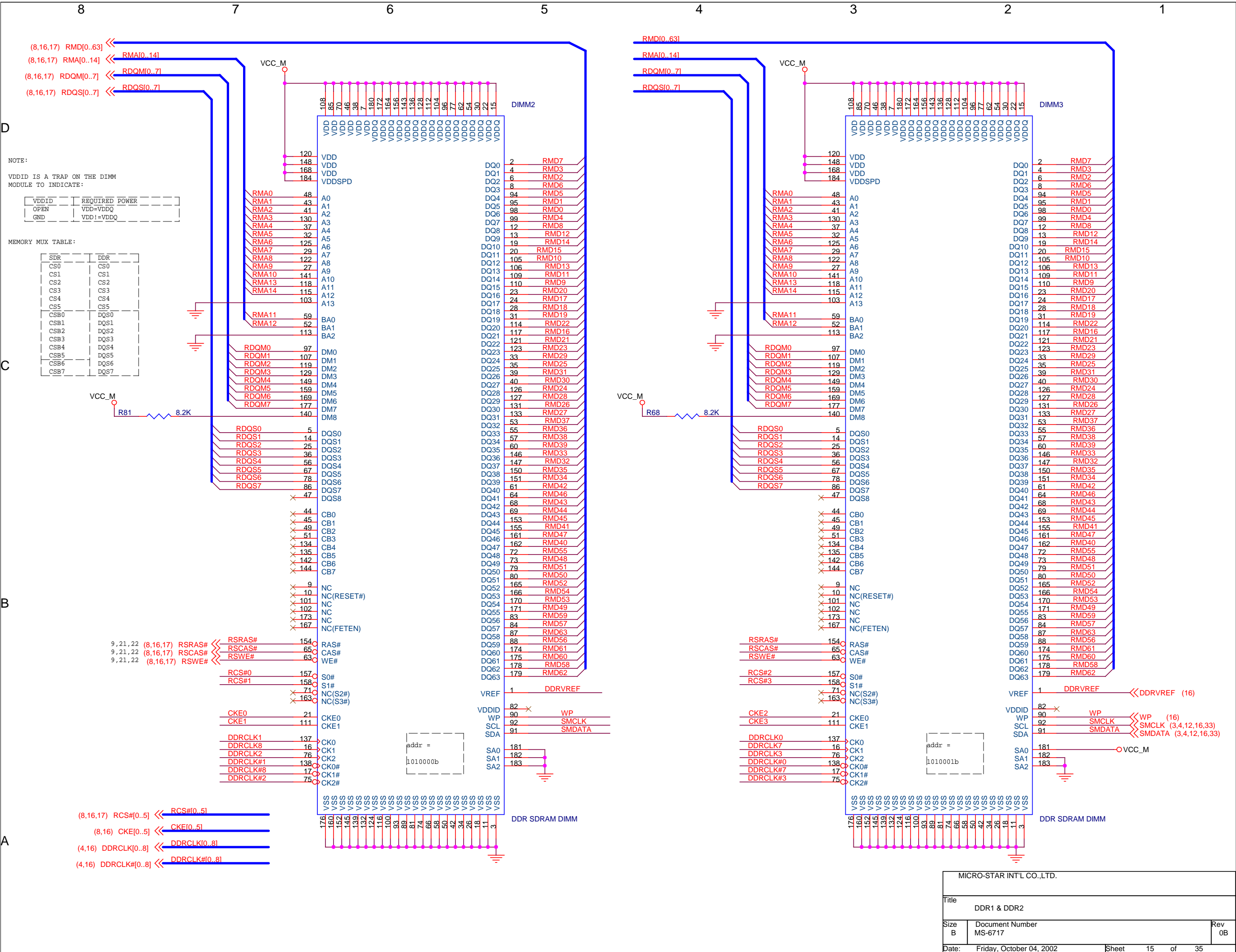


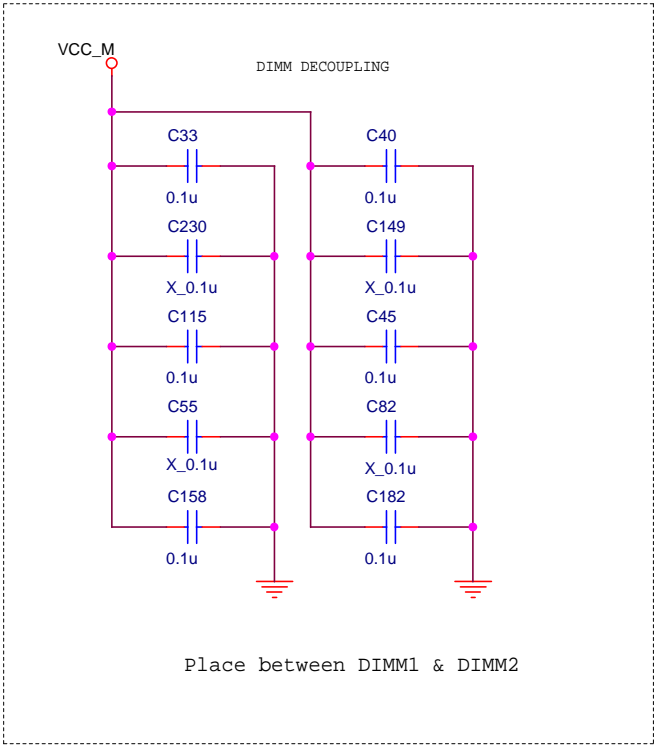
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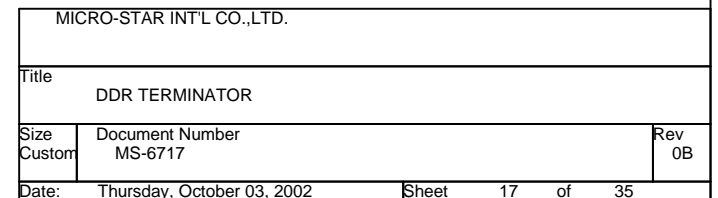
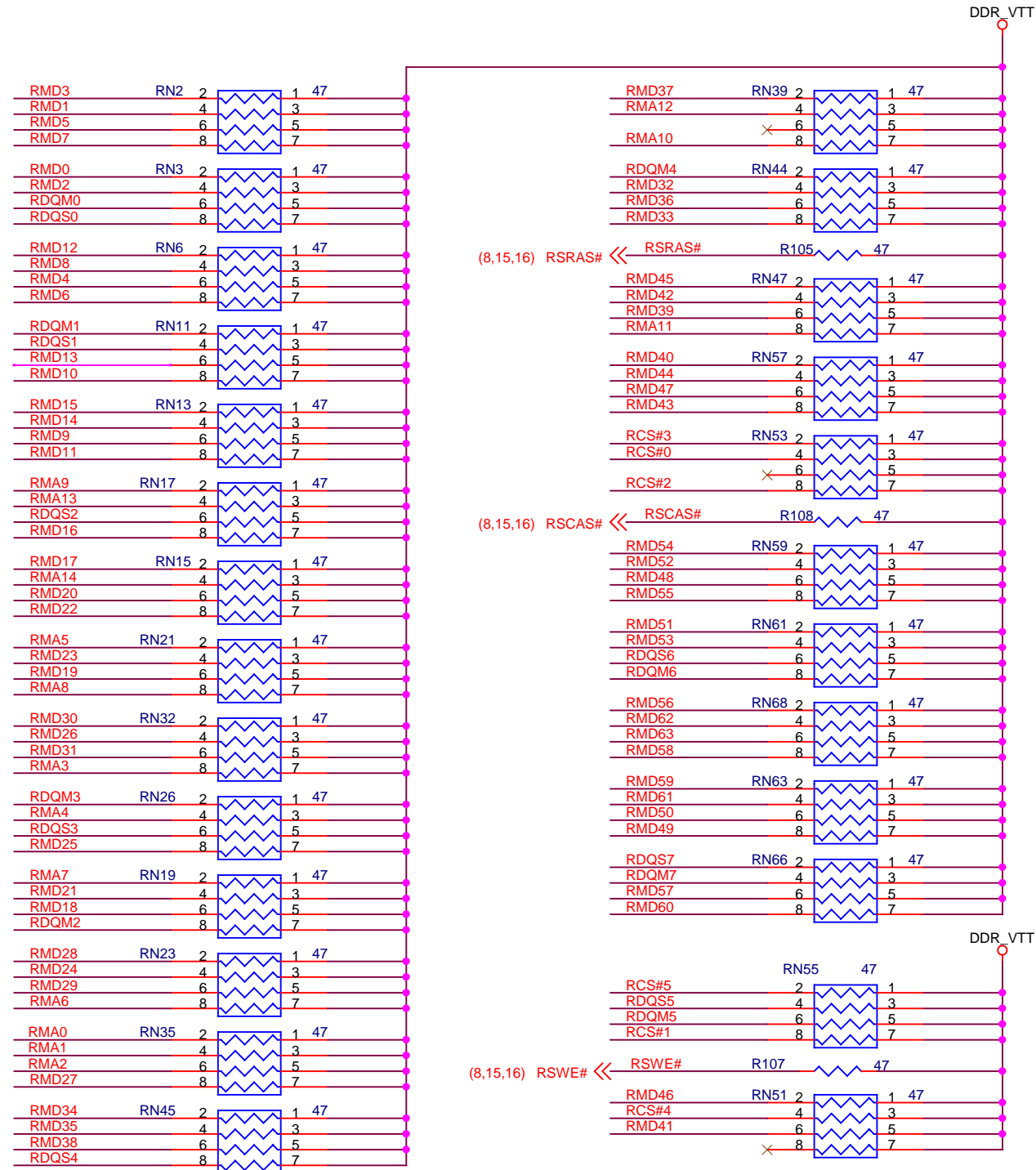


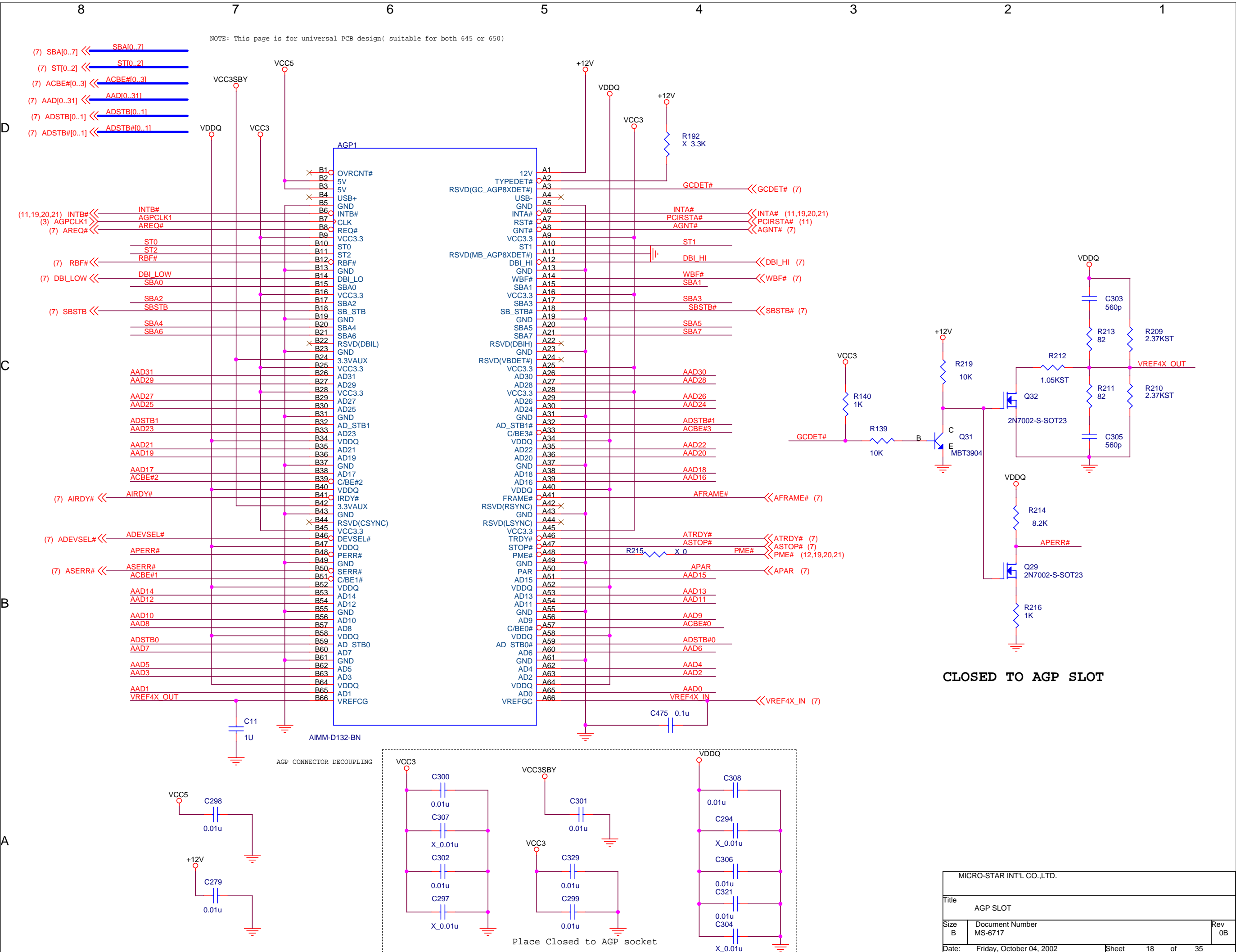
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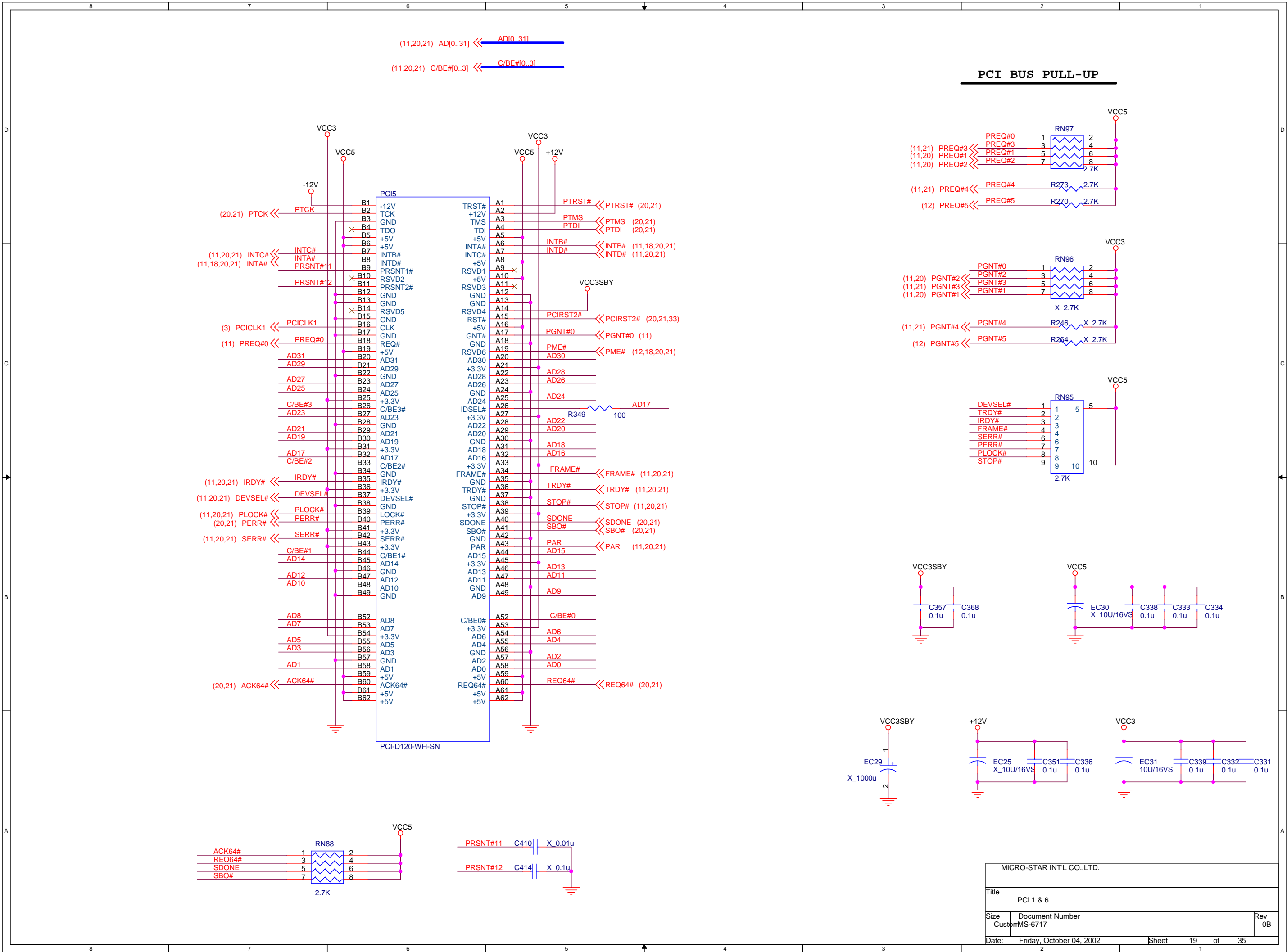


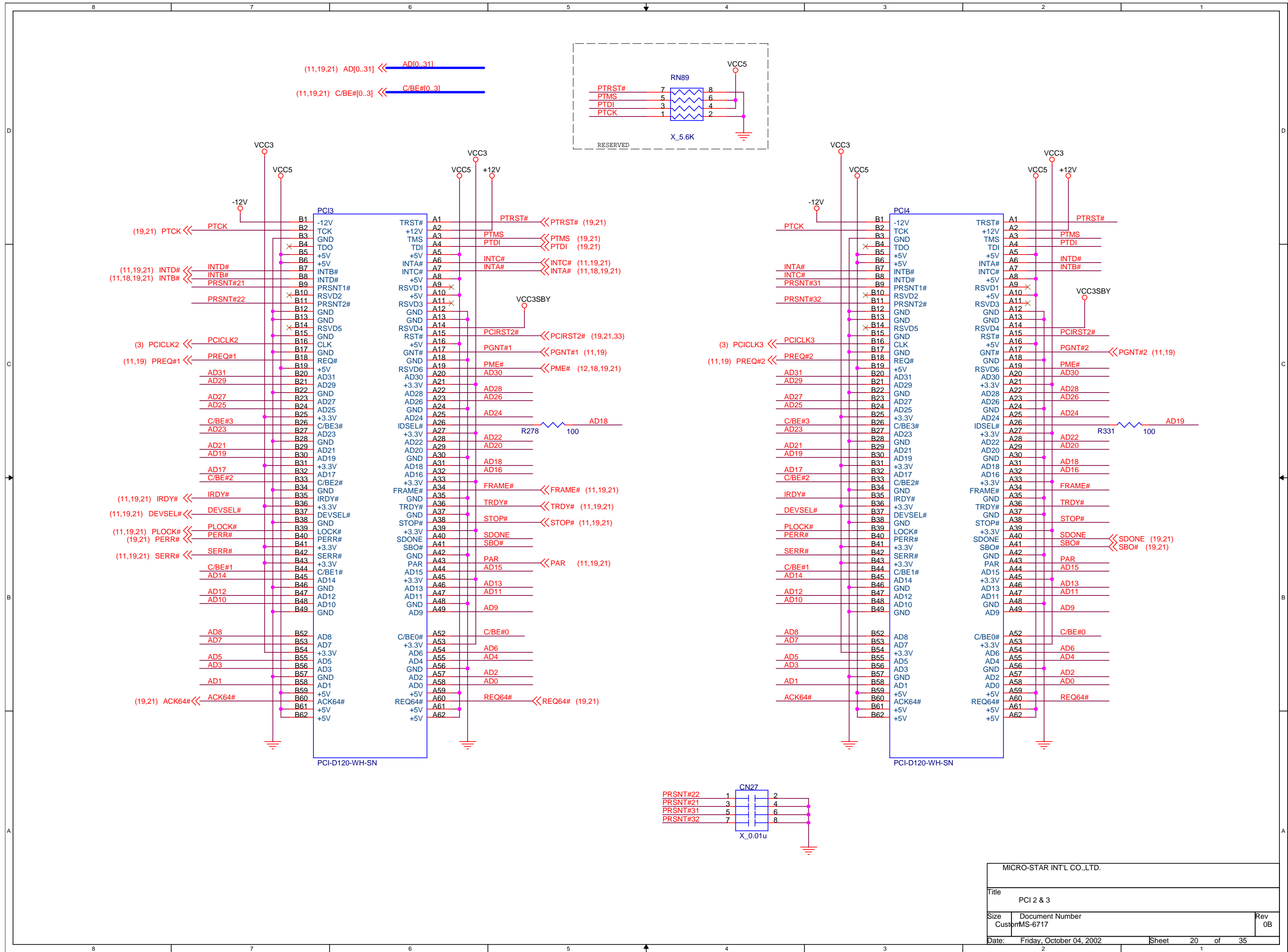
	SDR	Rs	DDR	Rs	Rtt
MD/DQM(/DQS)	LV-CMOS	0/10/-	SSTL-2	10	47
MA/Control	LV-CMOS	10	SSTL-2	0	47
CS	LV-CMOS	0	SSTL-2	0	47
CKE	OD 3.3V		OD 2.5V		

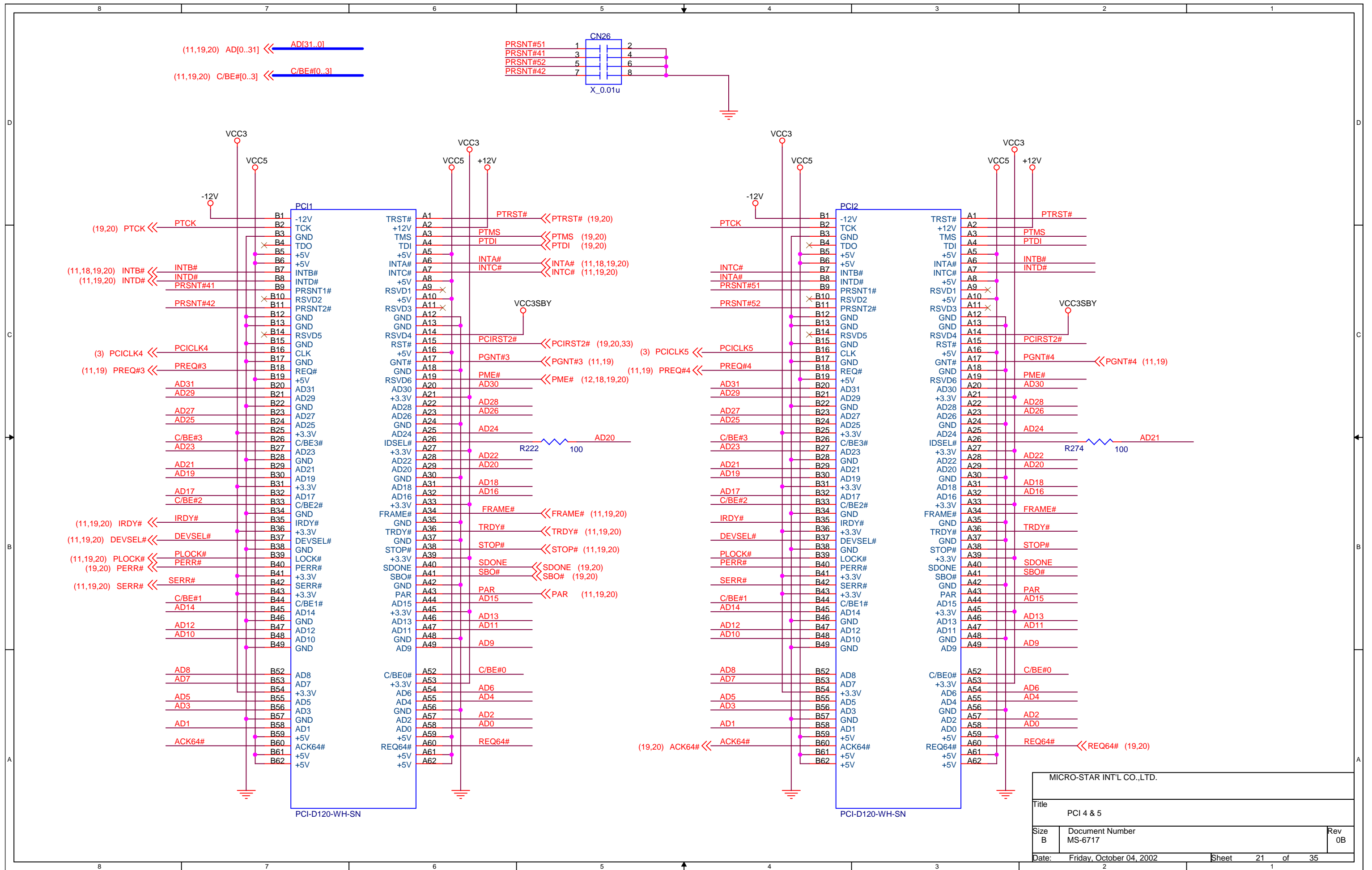




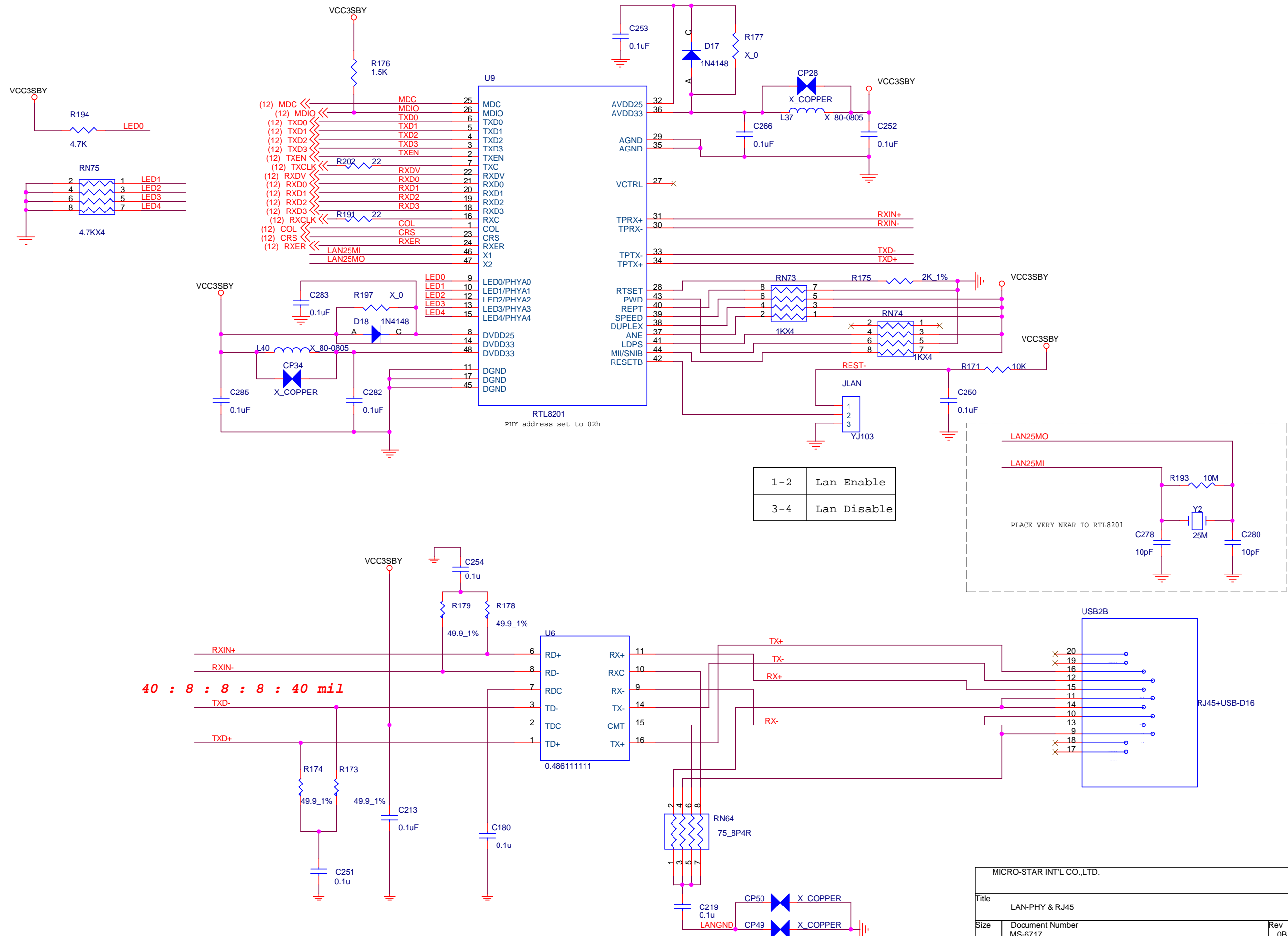
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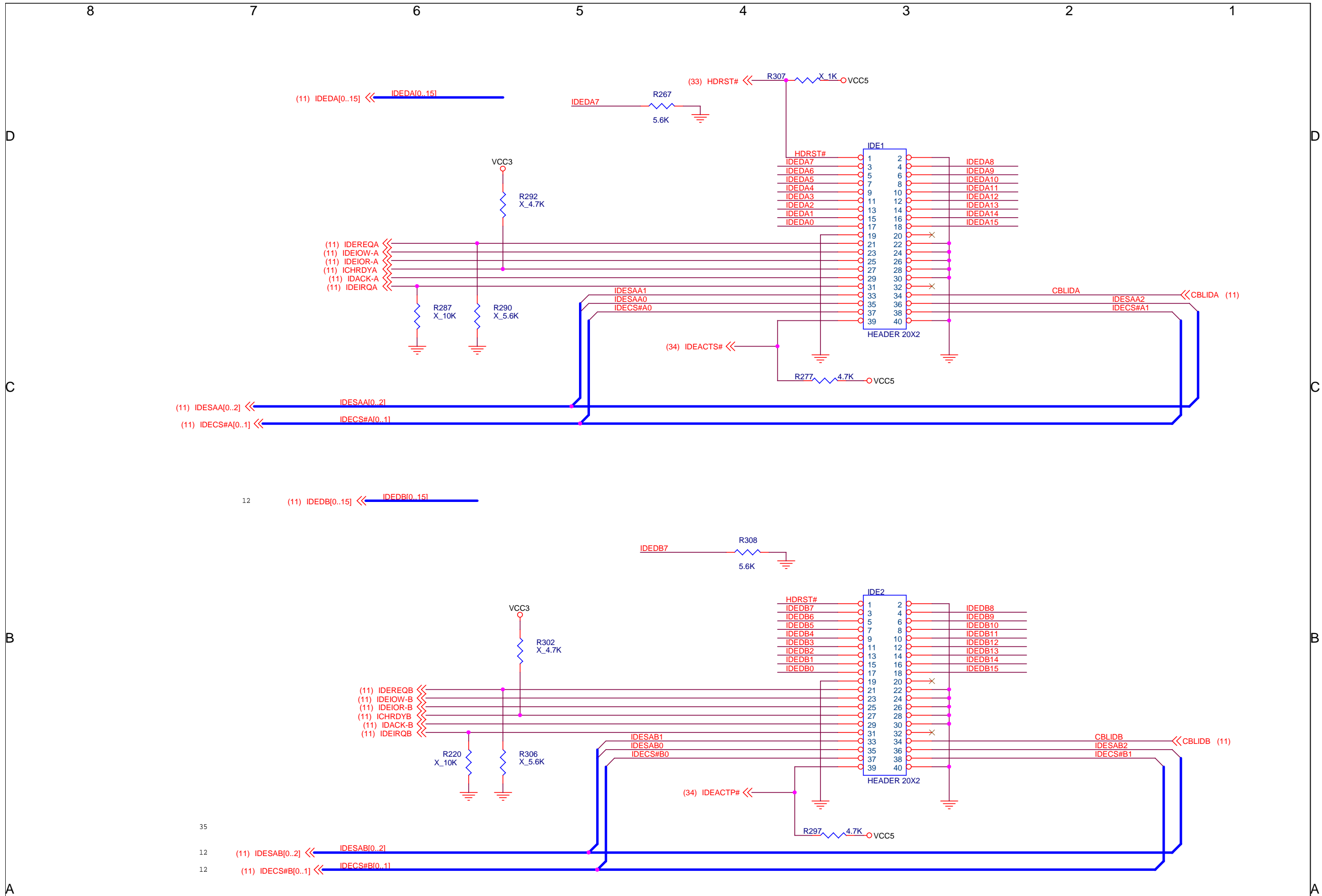




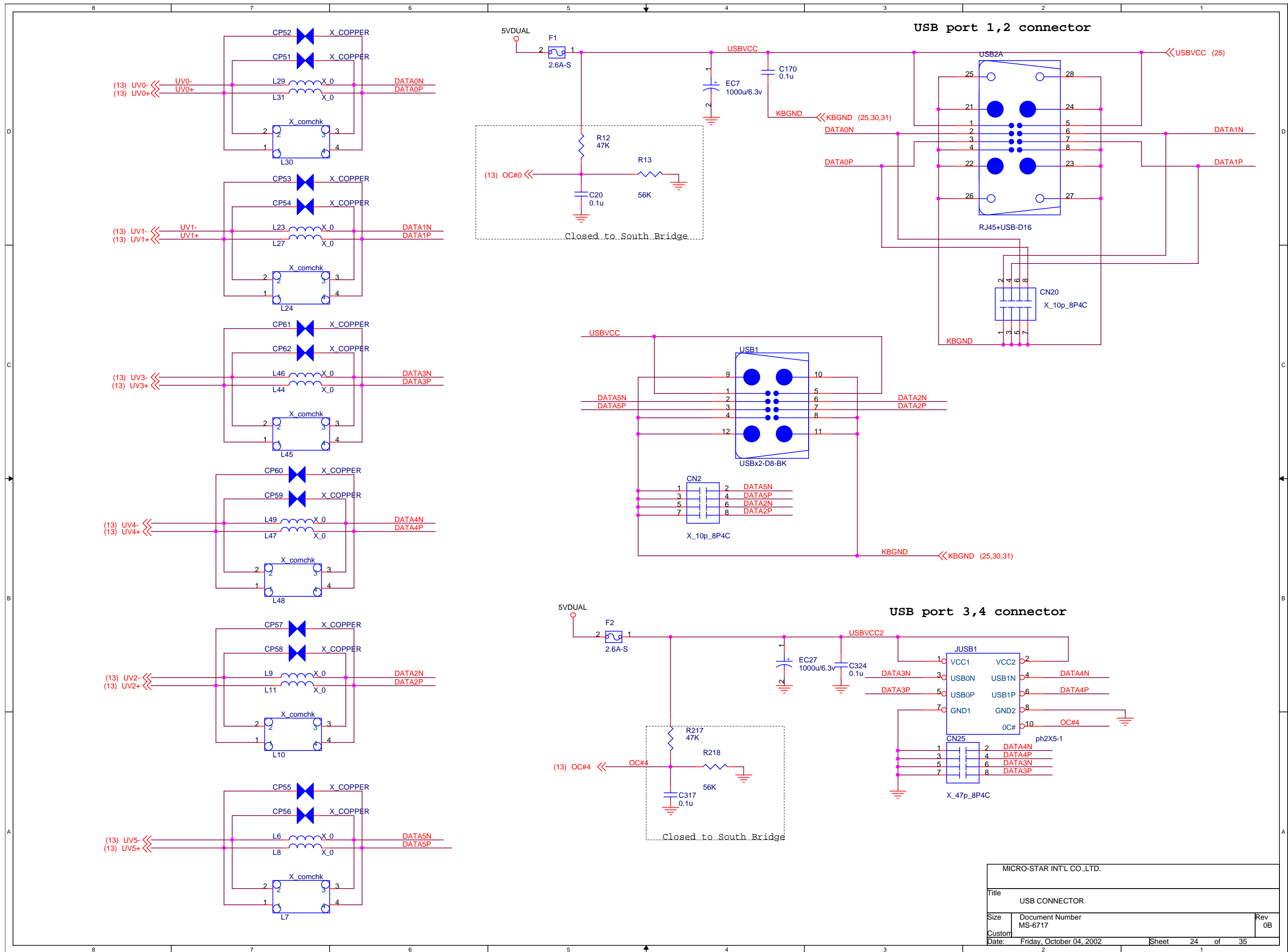


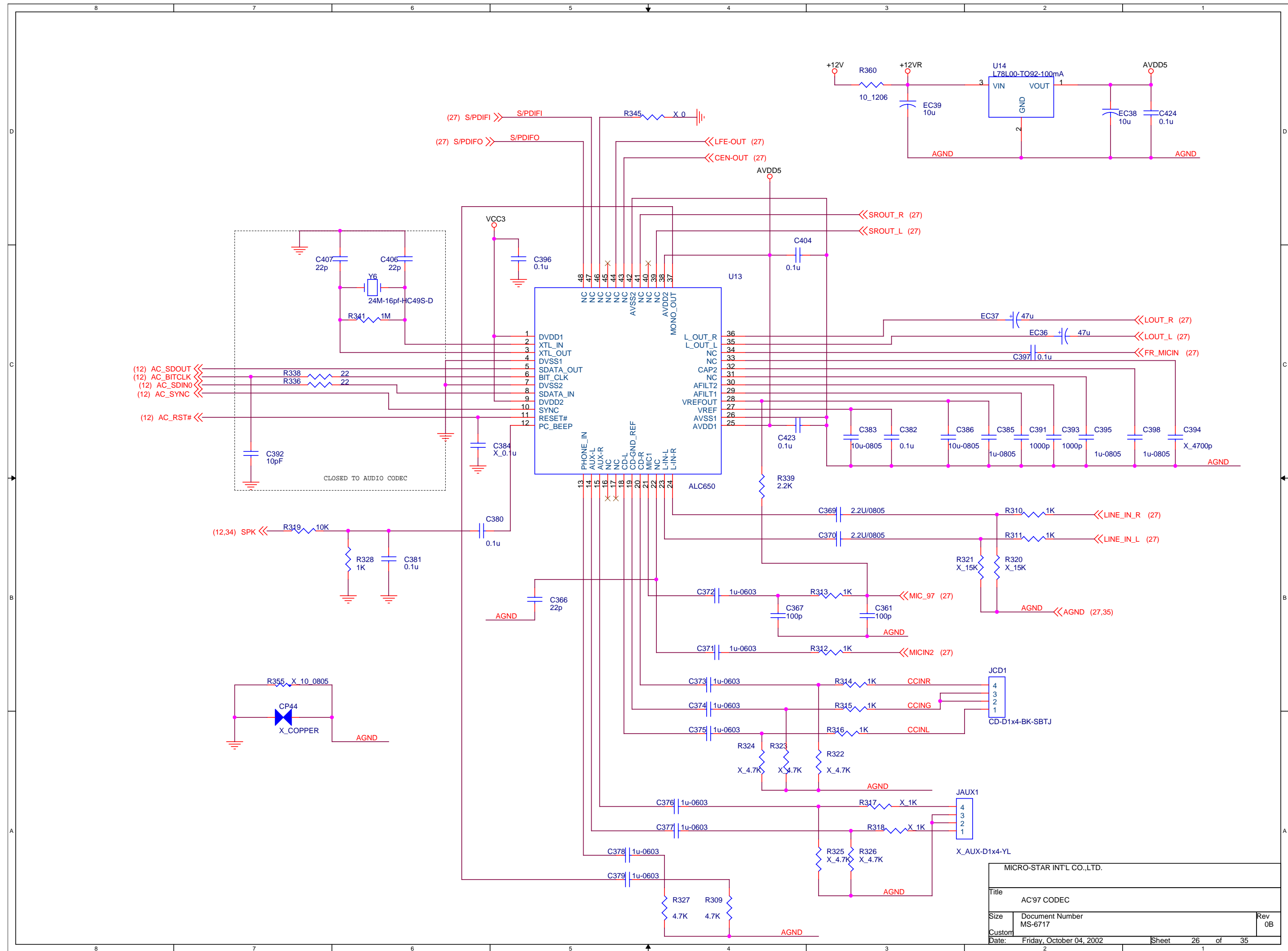
RTL8201

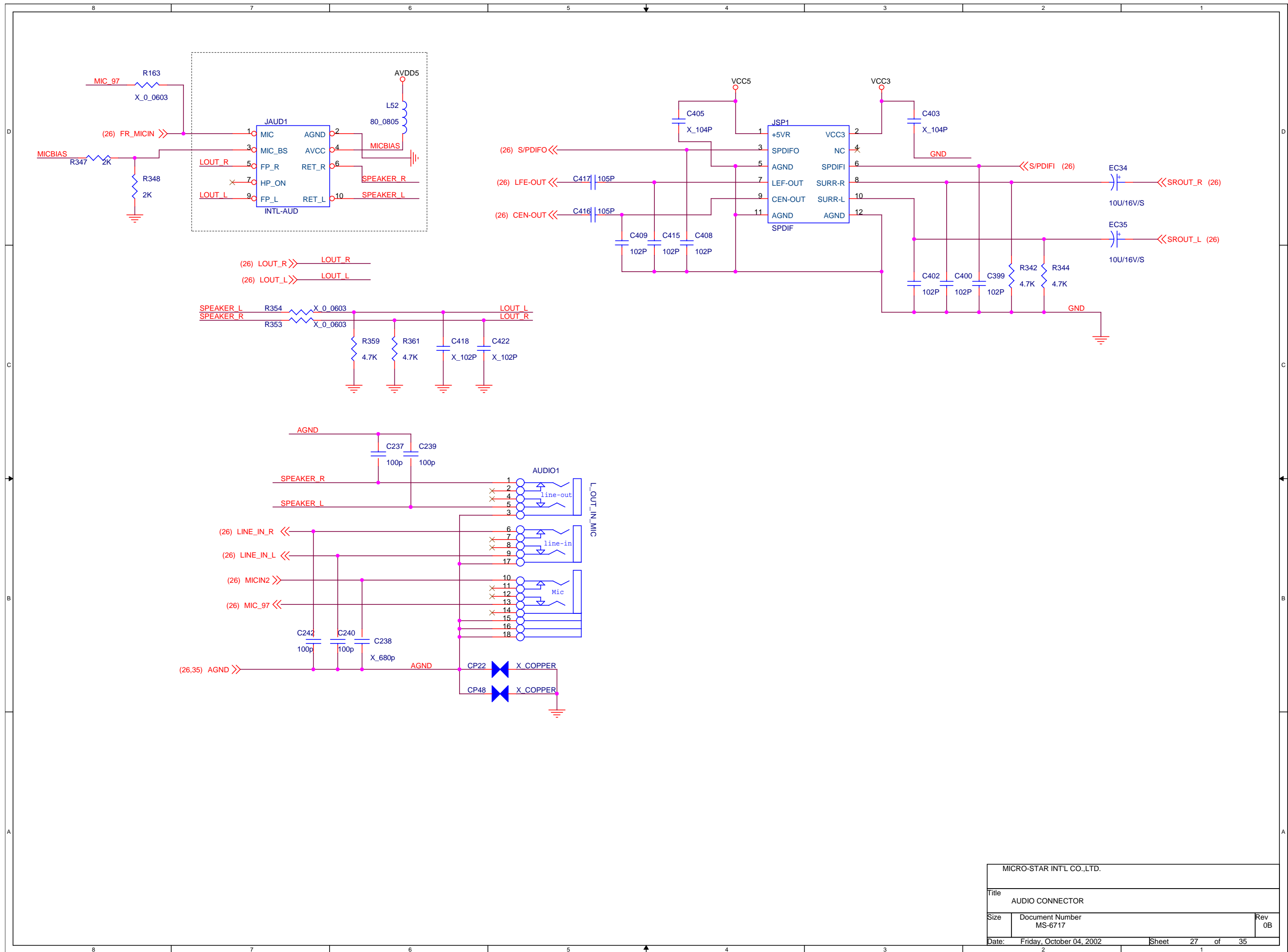


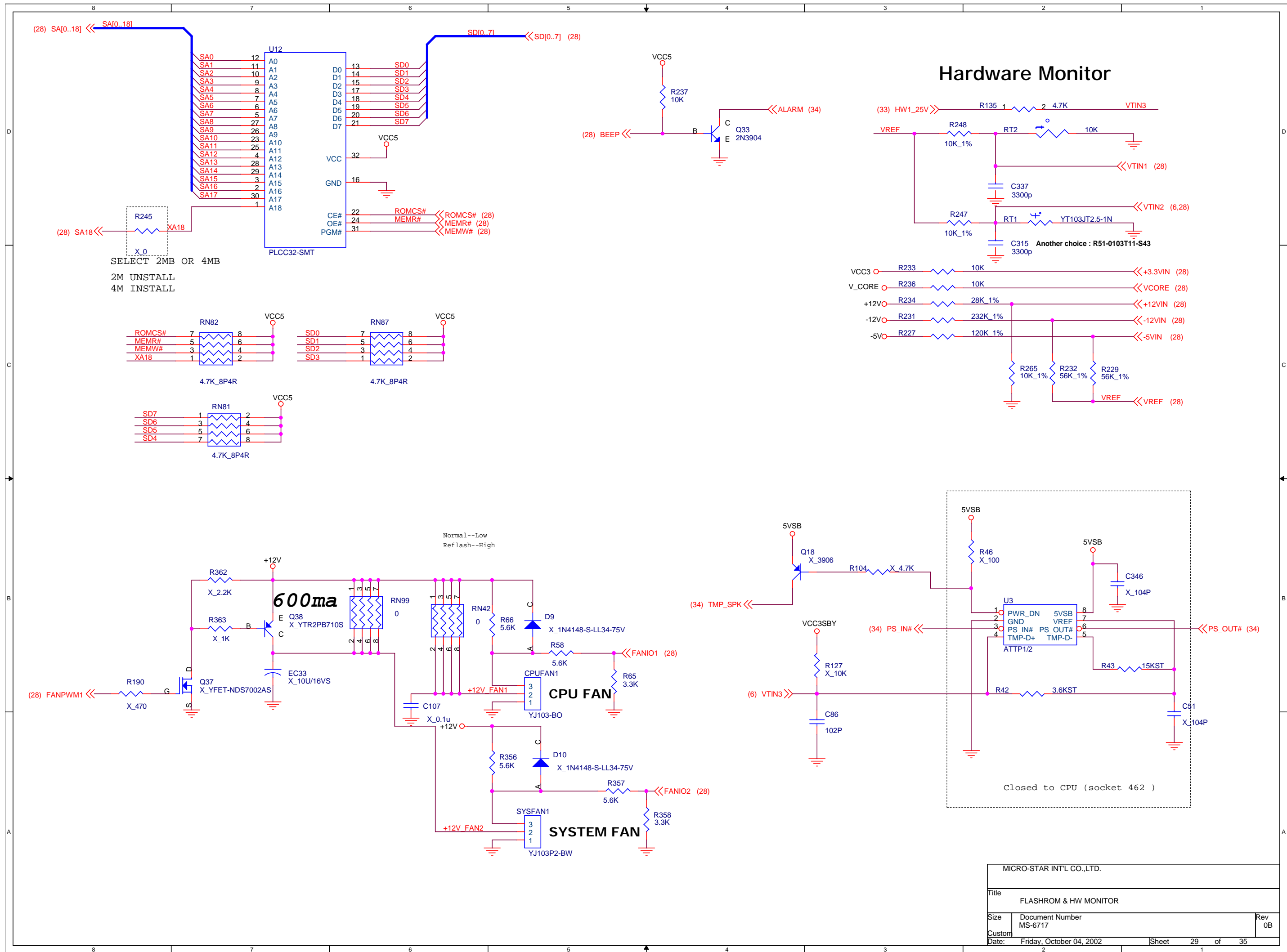


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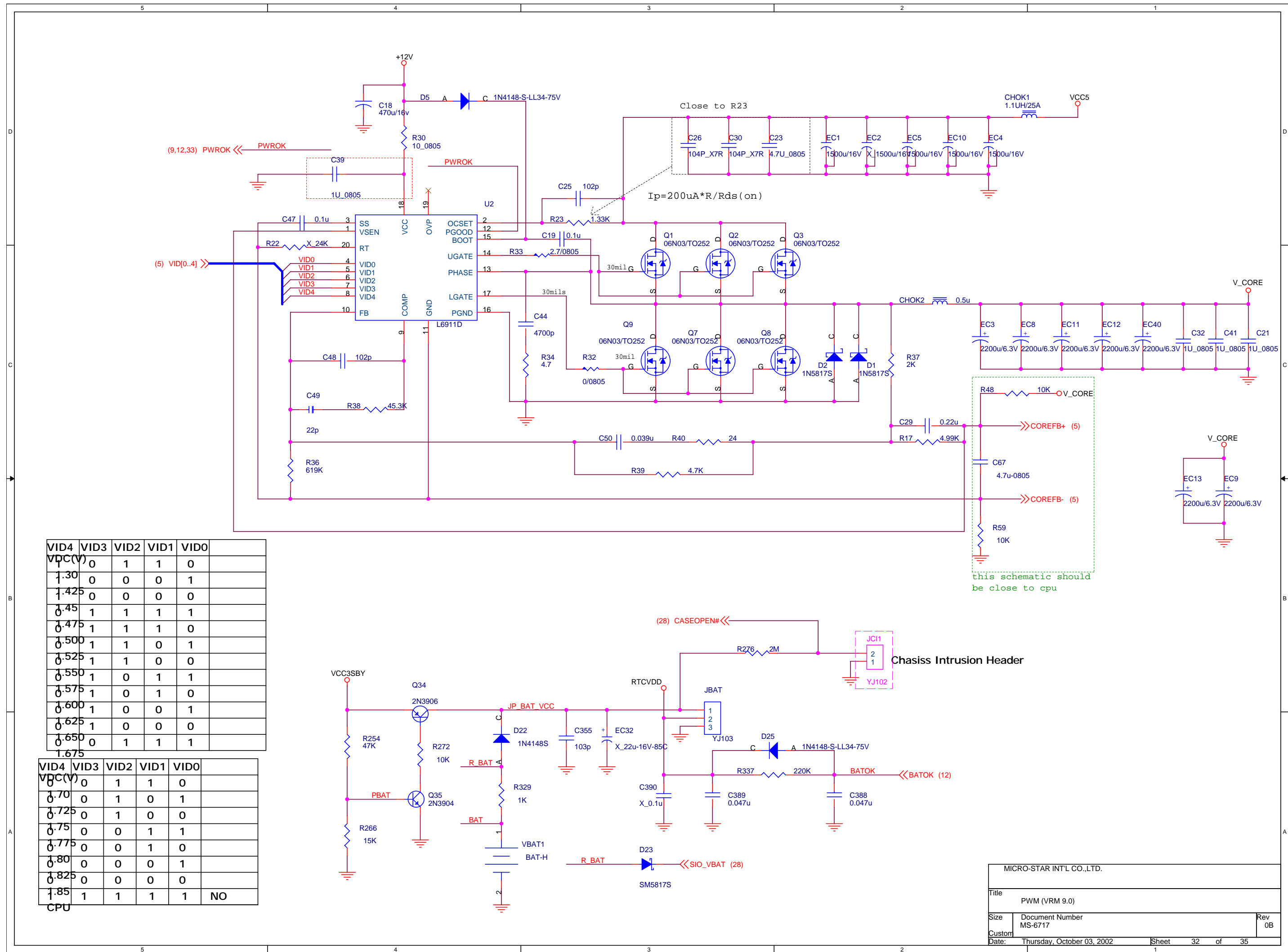






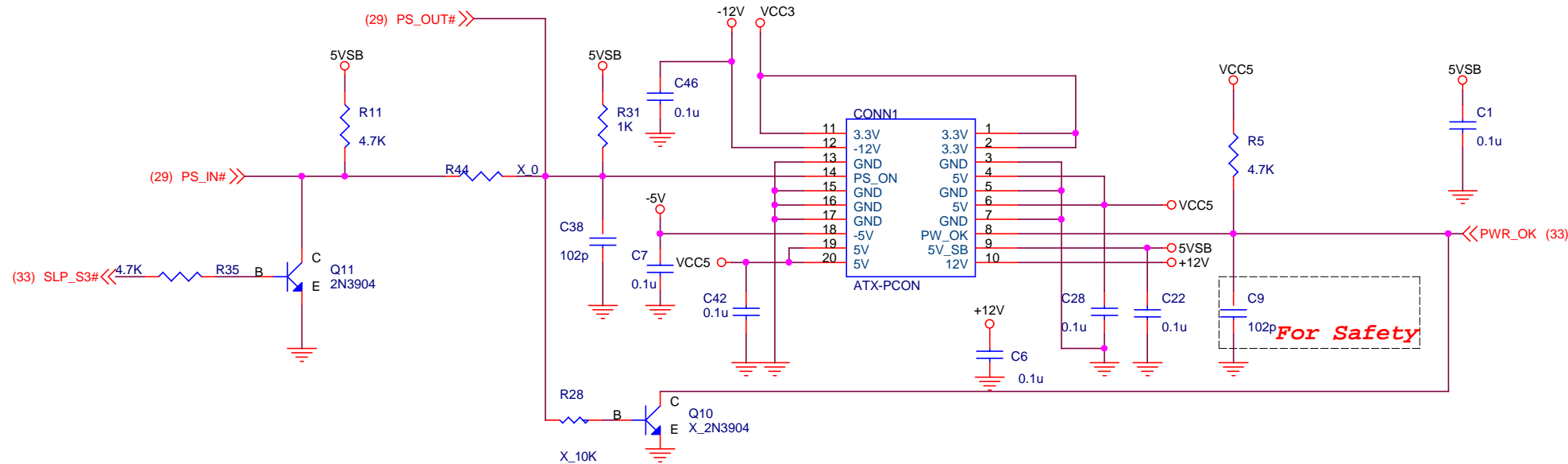
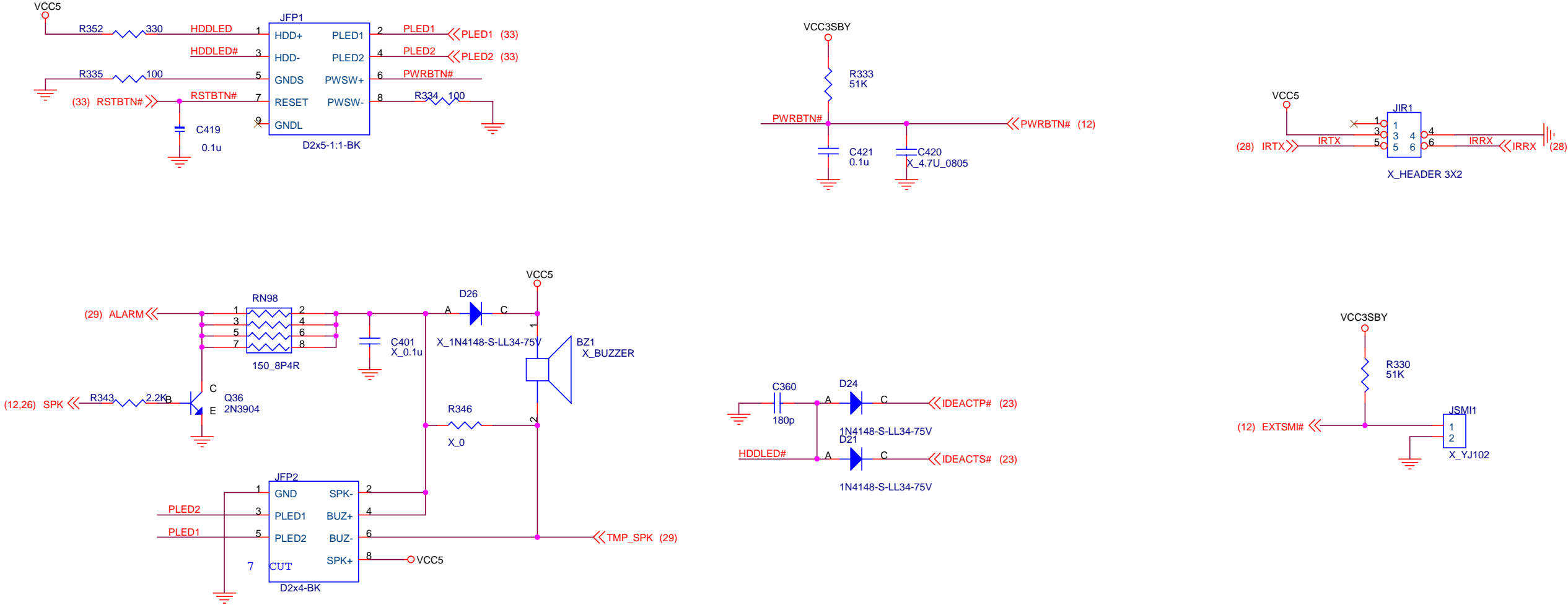








INTEL / MSI CONN



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FRONT PANNEL/ATX CONN			
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