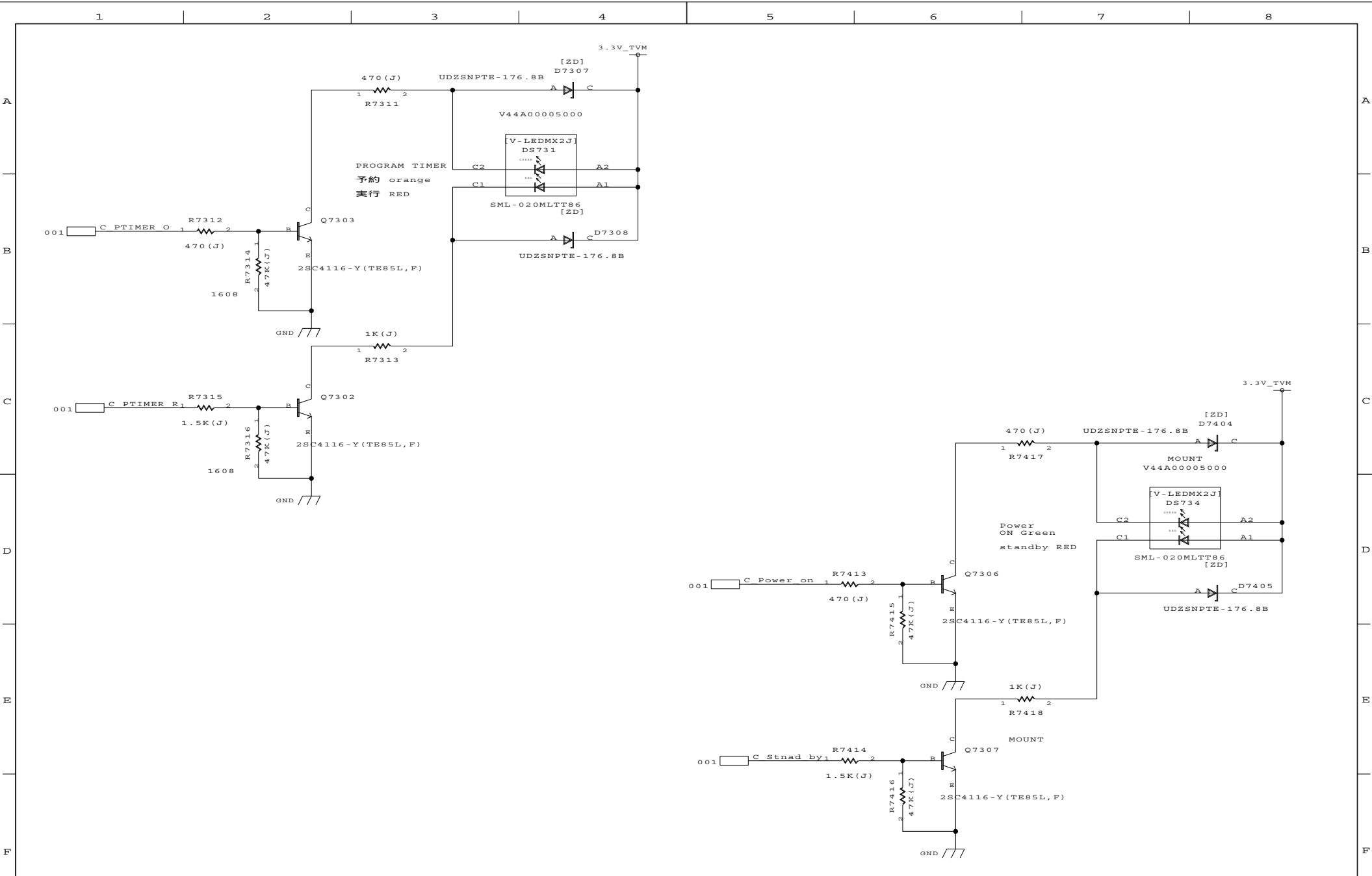


DESIGNED BY T. IMURA	TITLE 2010_LED_RMT	FUNCTION RMT	SH. NO. 1	PAGE NO. 1	REV. MARK 00	DRAWING. NO. ???
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2012.03.27 09:58



DESIGNED BY

T. IMURA

TITLE

2010_LED_RMT

FUNCTION

LED Driver

SH. NO.

3

PAGE NO.

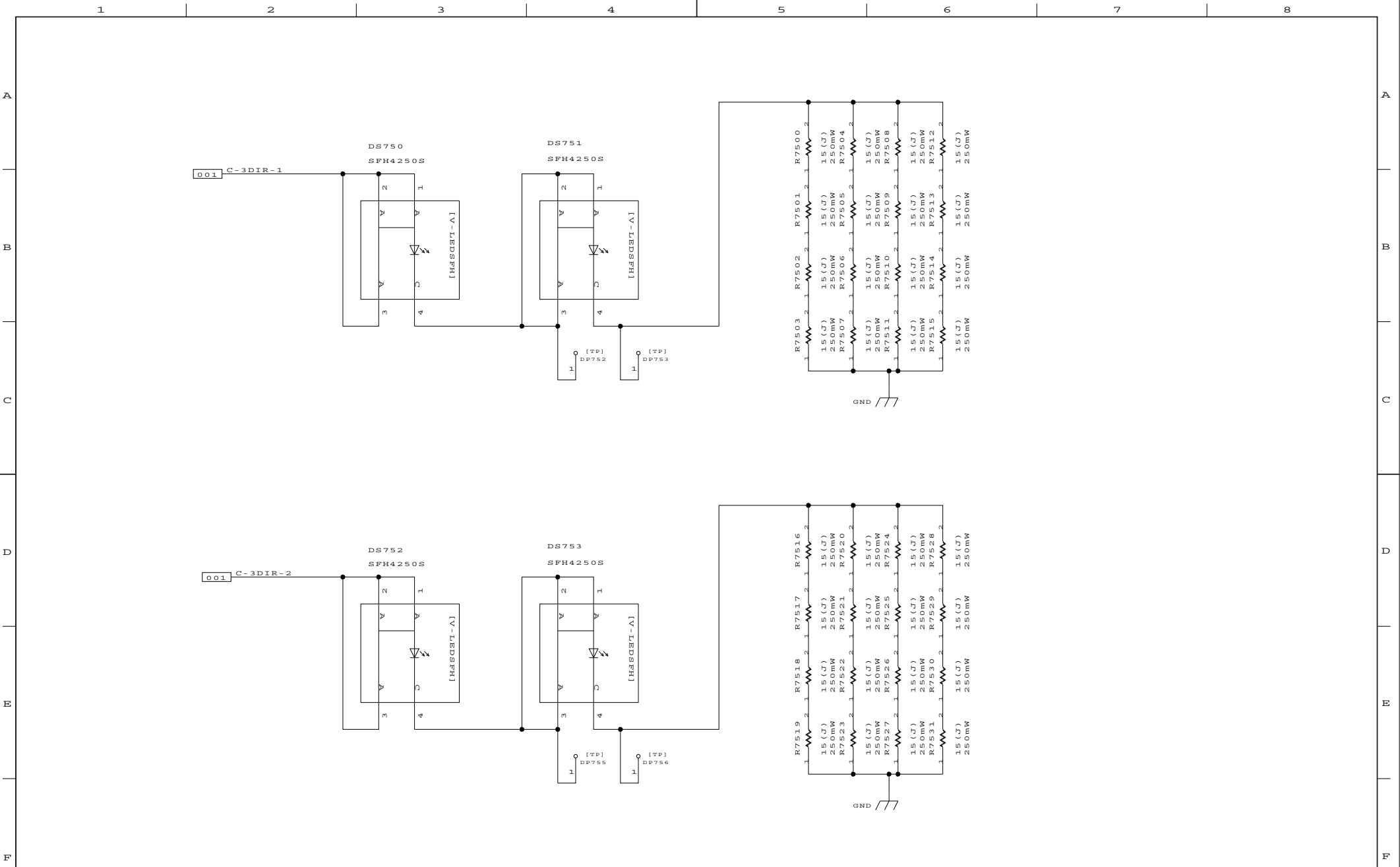
3

REV. MARK

00

DRAWING. NO.

**



DESIGNED BY

M. SATO

TITLE

2011_LED_RMT

FUNCTION

3D-IR

SH. NO.

4

PAGE NO.

4

REV. MARK

00

DRAWING. NO.

SCHEMATIC DIAGRAMS

PRODUCT NAME TL933/963

UNIT NAME MAIN

UNIT NO. PE1063ZA

PWA NAME

PWA NO.

PWB NAME

PWB NO.

PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV	PAGE	SH.	FUNCTION	REV
1	---	COVER SHEET	MP	26	129	DDR3 SDRAM	MP	51	658	-	MP	76	903	FRC POWER 1.26V	MP
2	101	MStar (Component Video)	MP	27	130	DDR3 Series Resistors	MP	52	660	HDMI CONNECTOR	MP	77	904	FRC POWER 1.5V	MP
3	102	MStar (CVBS, VIF)	MP	28	131	EBUS RESISTOR	MP	53	661	HDMI 5VPWR	MP	78	905	POWER 3.3VD	MP
4	103	MStar (Audio, SIF)	MP	29	132	SPI FLASH	MP	54	662	HDMI HPD(1)	MP	79	906	POWER 6V	MP
5	104	Mstar(CI)	MP	30	134	NAND FLASH	MP	55	663	HDMI HPD(2)	MP	80	914	PANEL I/F (POWER)	MP
6	105	Mstar(TS)	MP	31	150	ETHER PHY	MP	56	664	HDMI DDC	MP	81	950	POWER CONNECTOR	MP
7	106	Mstar(HDMI 1/2)	MP	32	151	RJ45 CONNECTOR	MP	57	665	3.3V_HDMI	MP	82	960	EMC CONTROL	MP
8	107	Mstar(HDMI 2/2)	MP	33	160	USB CONNECTOR(side)	MP	58	669	HDCP KEY ROM	MP	83			
9	108	MStar(LVDS 1/2)	MP	34	310	AUDIO TV OUTPUT	MP	59	700	LED/IR IF	MP	84			
10	109	MStar(LVDS 2/2)	MP	35	321	Headphone AMP	MP	60	701	5V LED/KEY	MP	85			
11	110	MStar (DDR A)	MP	36	360	OPTICAL OUTPUT	MP	61	704	TVM FAN CONTROL	MP	86			
12	111	MStar (DDR B)	MP	37	370	AUDIO D-AMP (MAIN)	MP	62	750	TUNER	MP	87			
13	112	MStar (DDR C)	MP	38	371	AUDIO D-AMP OUT (MAIN)	MP	63	755	TUNER DVB-S/S2	MP	88			
14	113	Mstar (NAND, FRC)	MP	39	373	AUDIO MUTE	MP	64	765	DEMOD DVB-T2/T/C	MP	89			
15	114	MStar (GPIO)	MP	40	600	EURO SCART	MP	65	770	DEMOD DVB-S2/S	MP	90			
16	115	Mstar (USB, MISC)	MP	41	601	FRONT AV	MP	66	775	DiSEqC.LNB POWER	MP	91			
17	116	MStar (Power 1/3)	MP	42	602	DSUB PC IN	MP	67	780	TUNER POWER	MP	92			
18	117	MStar (Power 2/3)	MP	43	603	EURO COMPONENT IN	MP	68	790	CI TS RESISTOR	MP	93			
19	118	MStar (Power 3/3)	MP	44	605	EURO TV OUT	MP	69	791	CI POWER/CONNECTOR	MP	94			
20	120	PERI (XTAL, STRAPS)	MP	45	606	AV INPUT1	MP	70	793	CI ADDRESS BUFFER	MP	95			
21	121	RESET, I2C	MP	46	607	AV INPUT2	MP	71	794	CI DATA RESISTOR	MP	96			
22	122	PERI (SERIAL)	MP	47	608	AV INPUT3	MP	72	795	CI CONTROL BUFFER	MP	97			
23	126	DDR3 SDRAM	MP	48	650	LVDS CONNECTOR	MP	73	796	CI TS OUTPUT BUFFER	MP	98			
24	127	DDR3 Damping Resistors	MP	49	651	LVDS CONNECTOR	MP	74	901	3.3V STANDBY	MP	99			
25	128	DDR3 Damping Resistors	MP	50	652	BL_CONTROL & DIMMING	MP	75	902	POWER 1.1V	MP	100			

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CHECKED BY

DESIGNED BY

TITLE

TOTAL

PAGE NO.

REV. MARK

DRAWING. NO.

IWANO

OSANAI

MAIN UNIT

82

1

MP

PE1063ZA

2012.01.19

13:52

@plotDe

TOSHIBA CORPORATION

V46A00065300

[V-U388USV]
1/18
IC100

[ANALOG VIDEO INTERFACE]

750	M_ADCIM	AE2	IM
750	M_ADCIP	AE1	IP
608	M_HSYNCO	M4	HSYNCO
608	M_VSYNCO	M5	VSYNCO
606	M_BINOM	L3	BINOM
608	M_BINOP	K1	BINOP
606	M_GINOM	M3	GINOM
608	M_GINOP	L1	GINOP
608	M_SOGINO	L2	SOGINO
606	M_RINOM	M1	RINOM
608	M_RINOP	M2	RINOP
600	M_HSYNC1	P4	HSYNC1
600	M_VSYNC1	N4	VSYNC1
606	M_BIN1M	N2	BIN1M
608	M_BIN1P	N1	BIN1P
606	M_GIN1M	P2	GIN1M
608	M_GIN1P	P1	GIN1P
606	M_SOGIN1	N3	SOGIN1
606	M_RIN1M	R1	RIN1M
608	M_RIN1P	P3	RIN1P
		R4	HSYNC2 (NC when not in use)
606	M_BIN2M	R3	BIN2M
606	M_BIN2P	R2	BIN2P
606	M_GIN2M	T3	GIN2M
606	M_GIN2P	T2	GIN2P
606	M_SOGIN2	T1	SOGIN2
606	M_RIN2M	U2	RIN2M
606	M_RIN2P	U1	RIN2P

MSD388USV-V6

DESIGNED BY
OSANAI

TITLE
MAIN UNIT

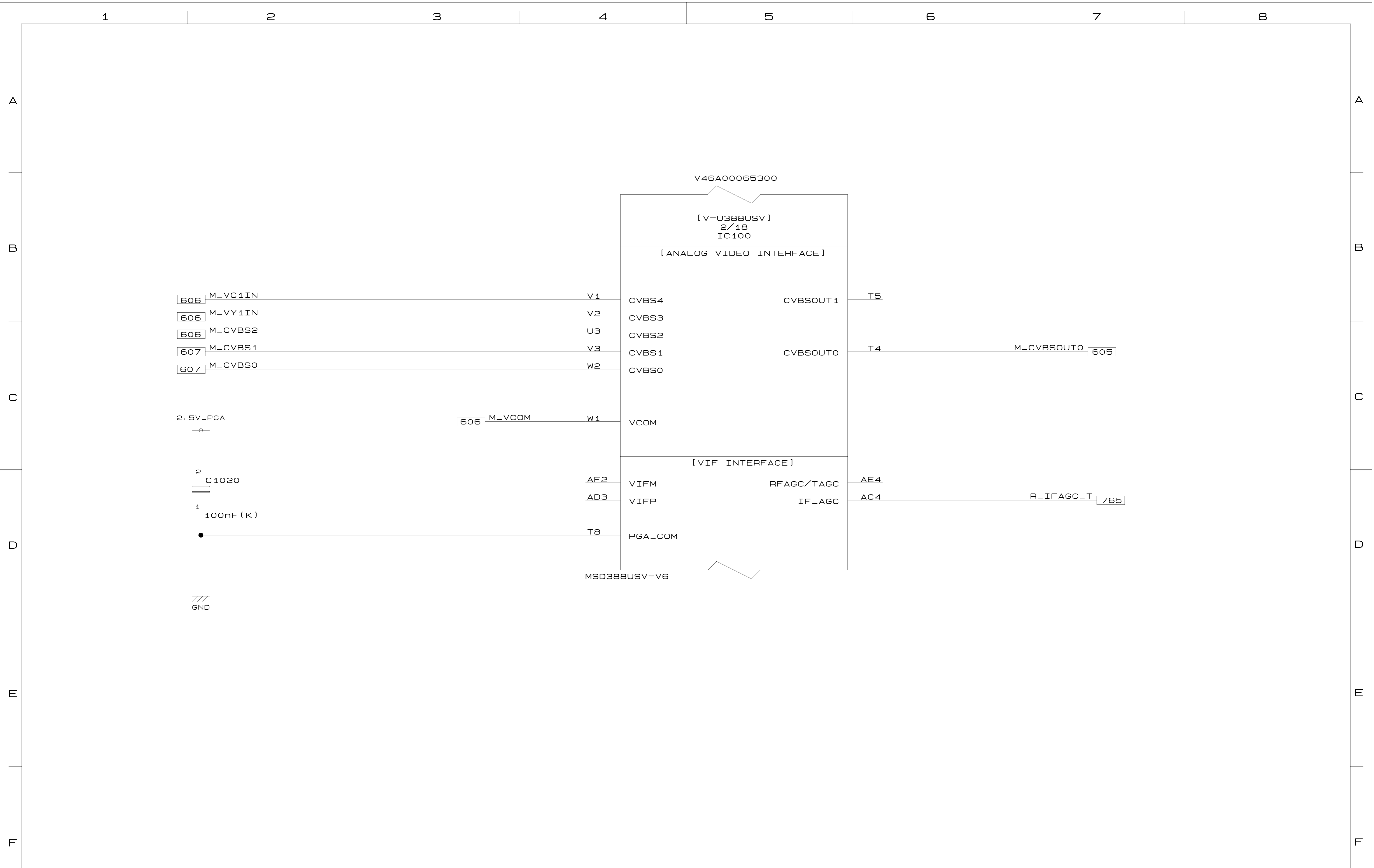
FUNCTION
MStar (Component Video)

SH. NO.
101

PAGE NO.
2

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
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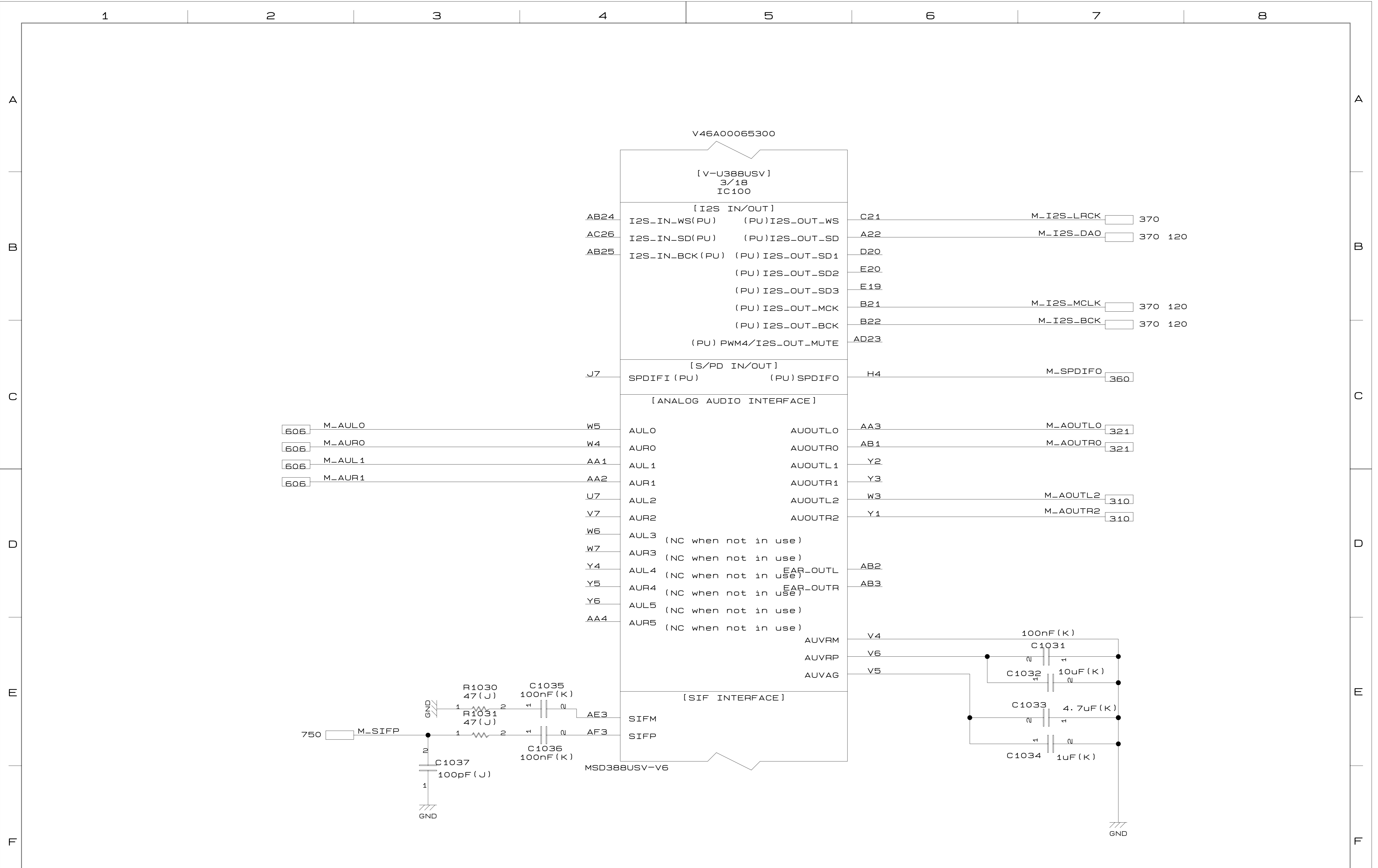
FUNCTION
MStar (CVBS, VIF)

SH. NO.
102

PAGE NO.
3

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

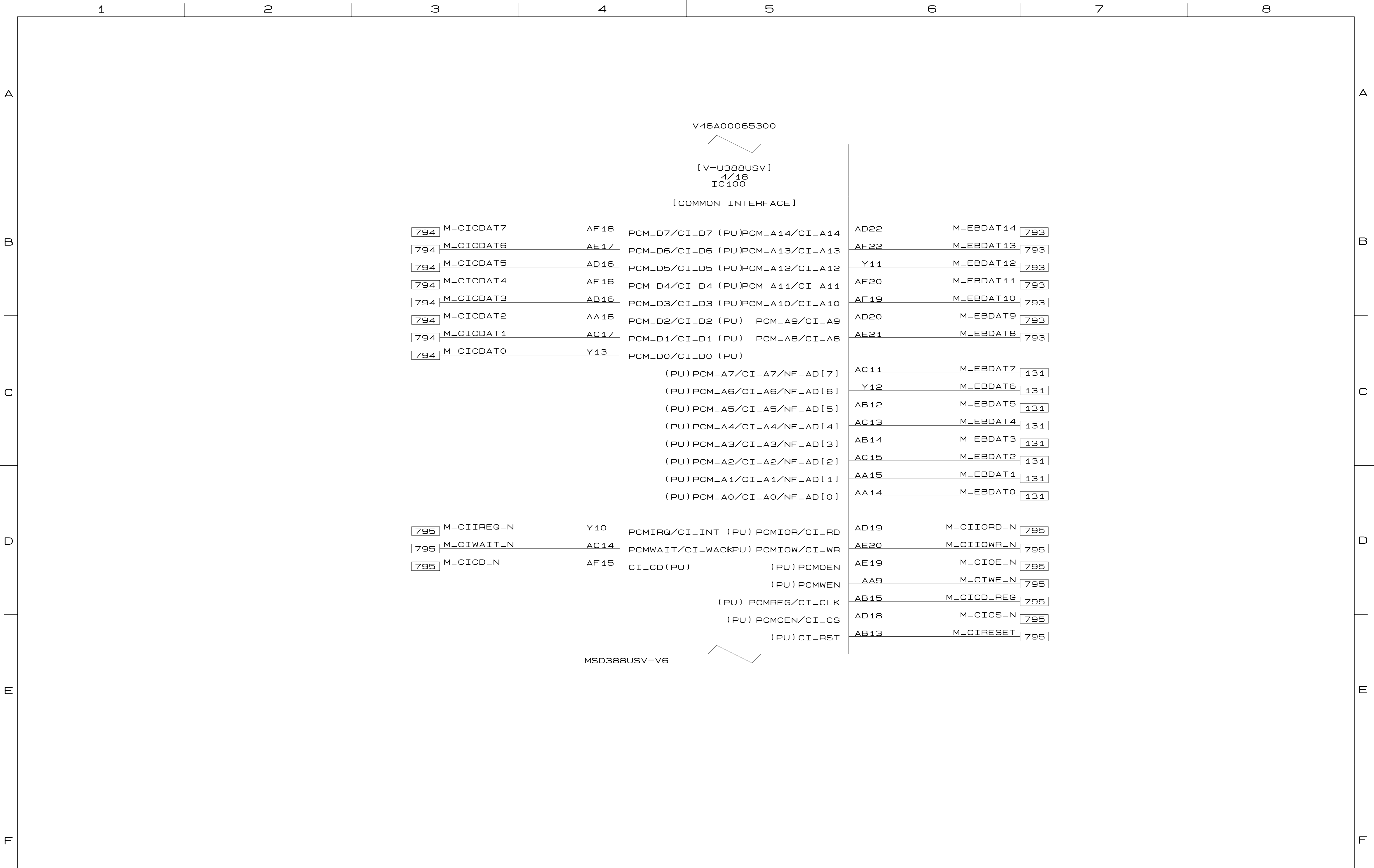
TITLE
MAIN UNIT

FUNCTION
MStar (Audio, SIF) 103

SH. NO. PAGE NO.
4

REV. MARK
MP

DRAWING. NO.
PE1063ZA



MSD388USV-V6

DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION Mstar(CI)	SH. NO. 104	PAGE NO. 5	REV. MARK MP	DRAWING. NO. PE1063ZA
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1

2

3

4

5

6

7

8

A

A

B

B

C

C

D

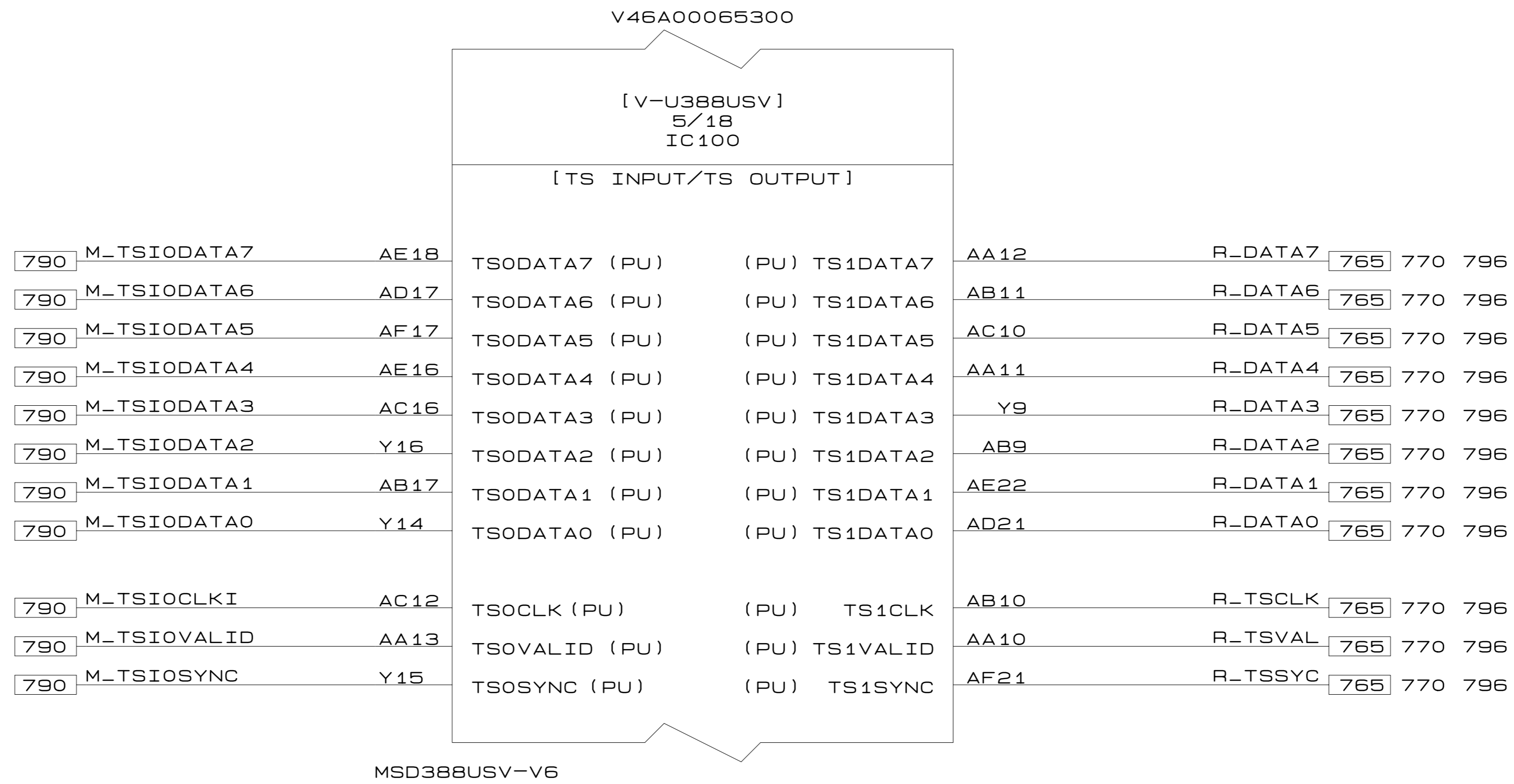
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E

E

F

F



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OSANAI

TITLE
MAIN UNIT

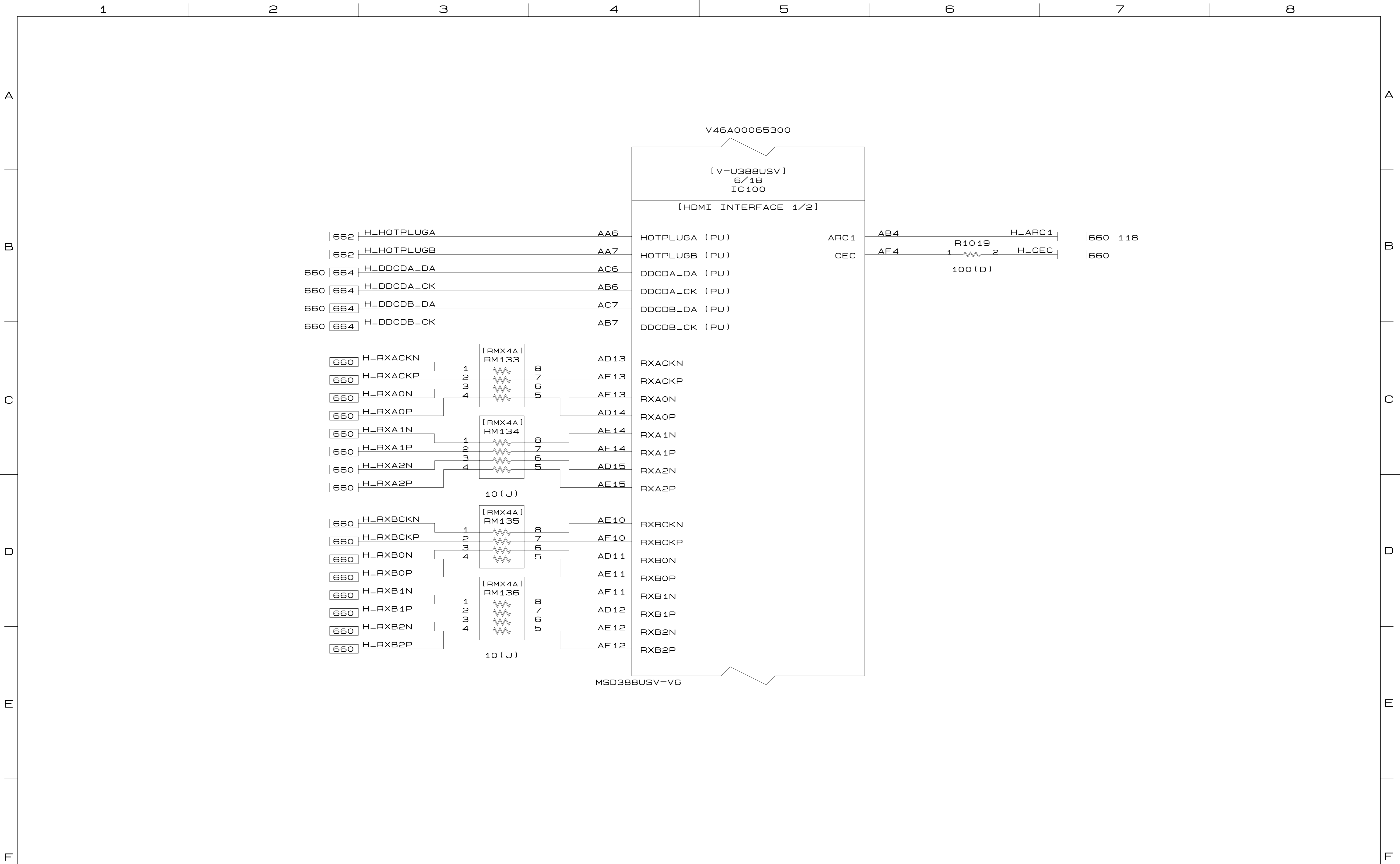
FUNCTION
Mstar (TS)

SH. NO.
105

PAGE NO.
6

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

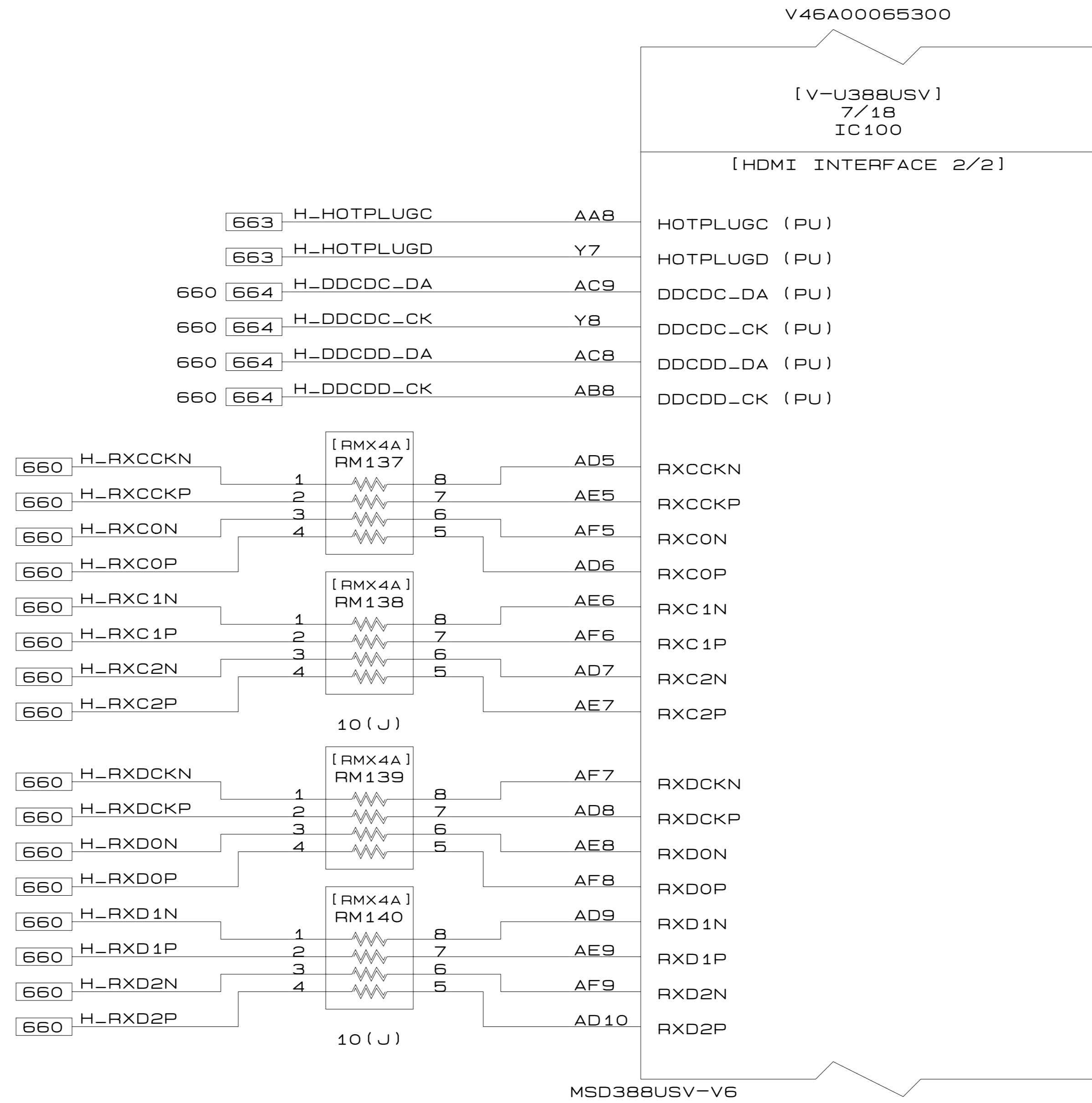
FUNCTION
Mstar (HDMI 1/2)

SH. NO.
106

PAGE NO.
7

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

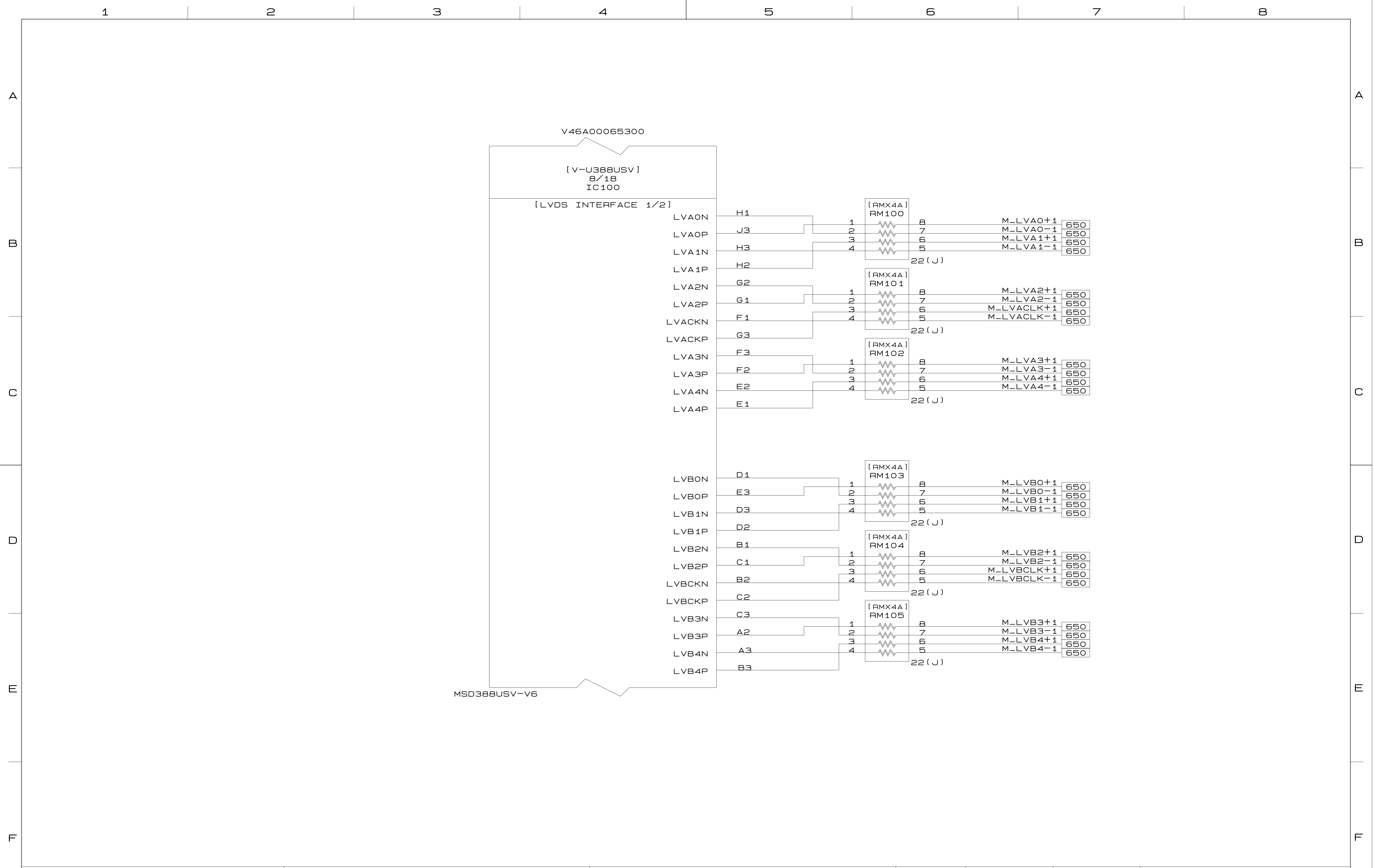
FUNCTION
Mstar (HDMI 2/2)

SH. NO.
107

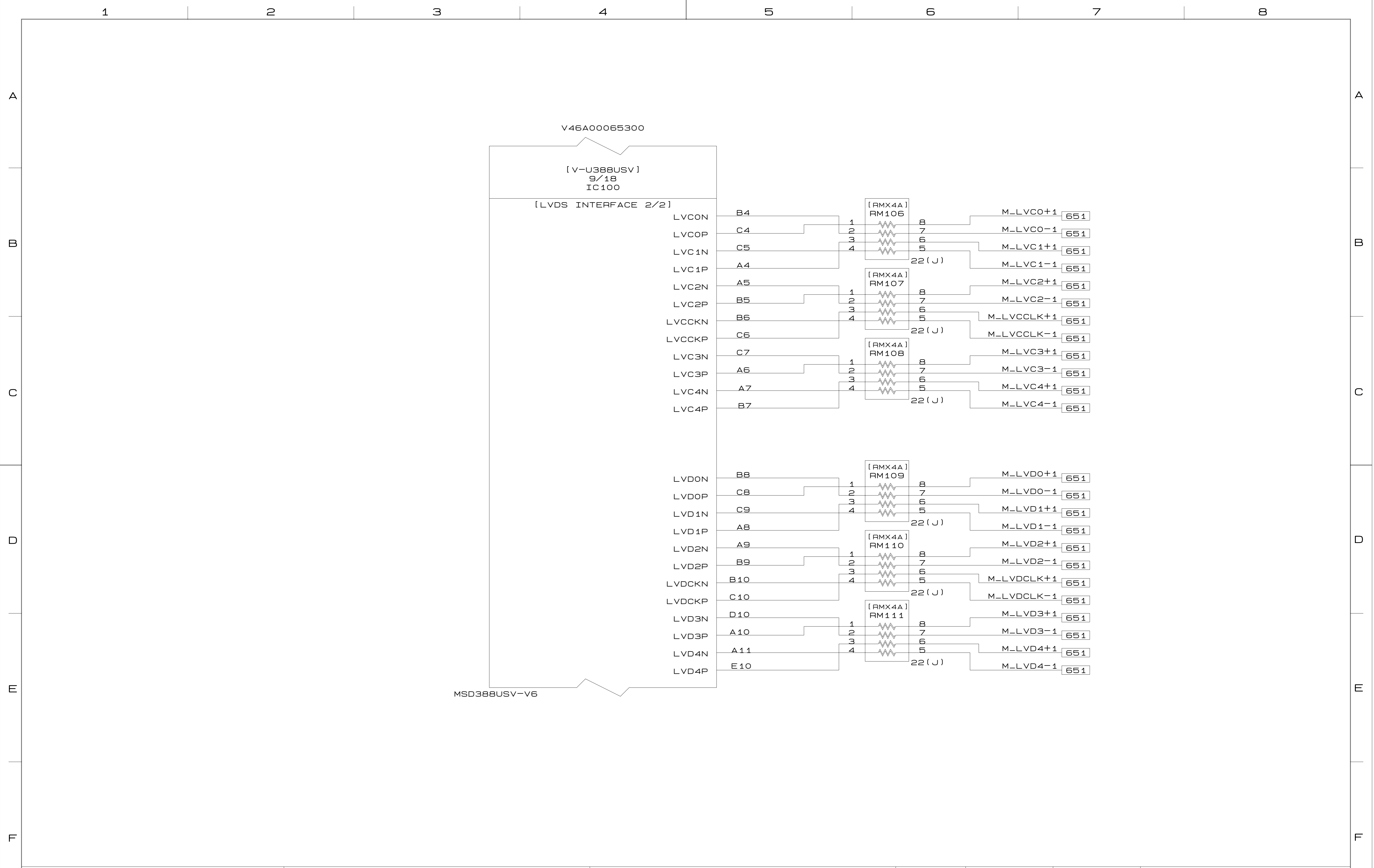
PAGE NO.
8

REV. MARK
MP

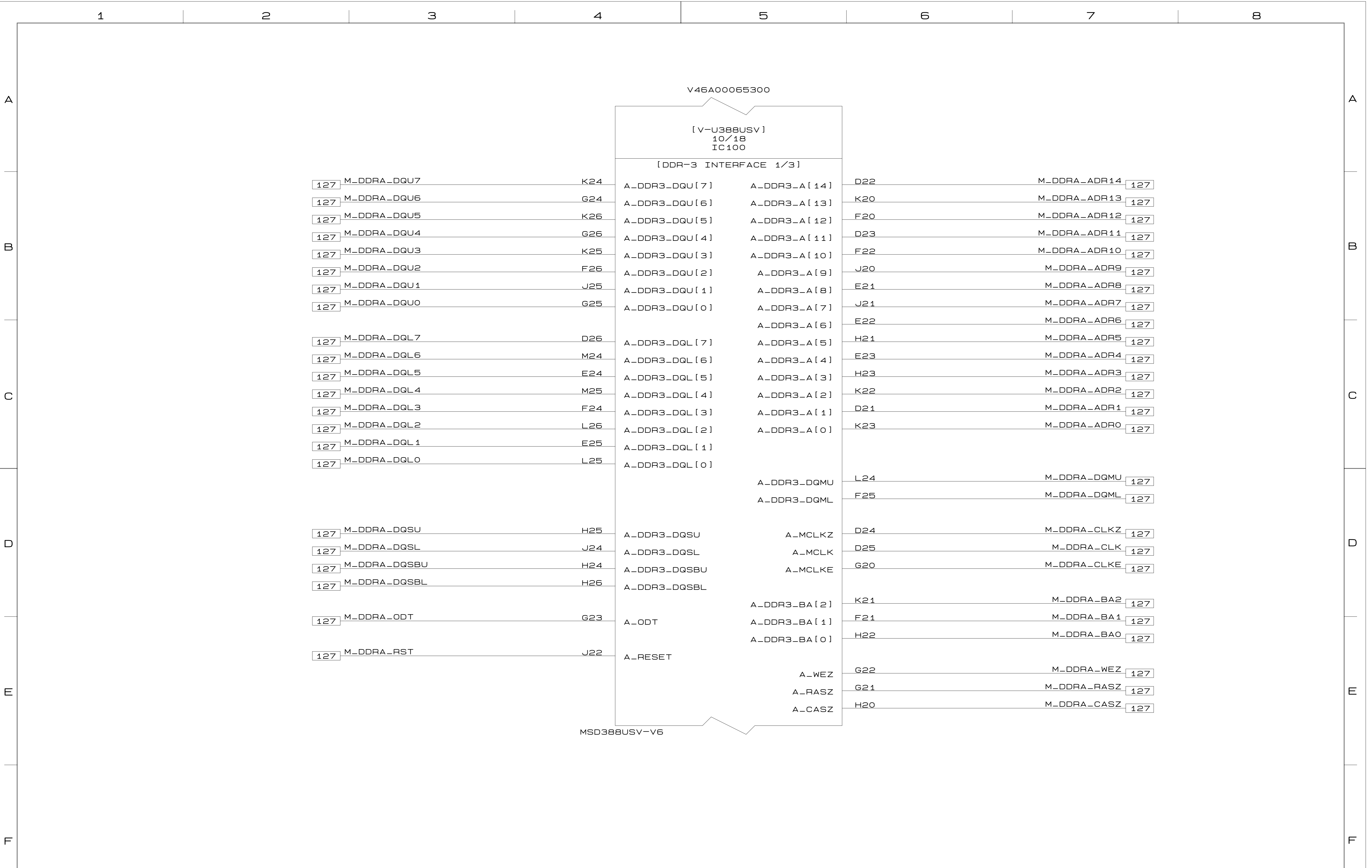
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PE1063ZA



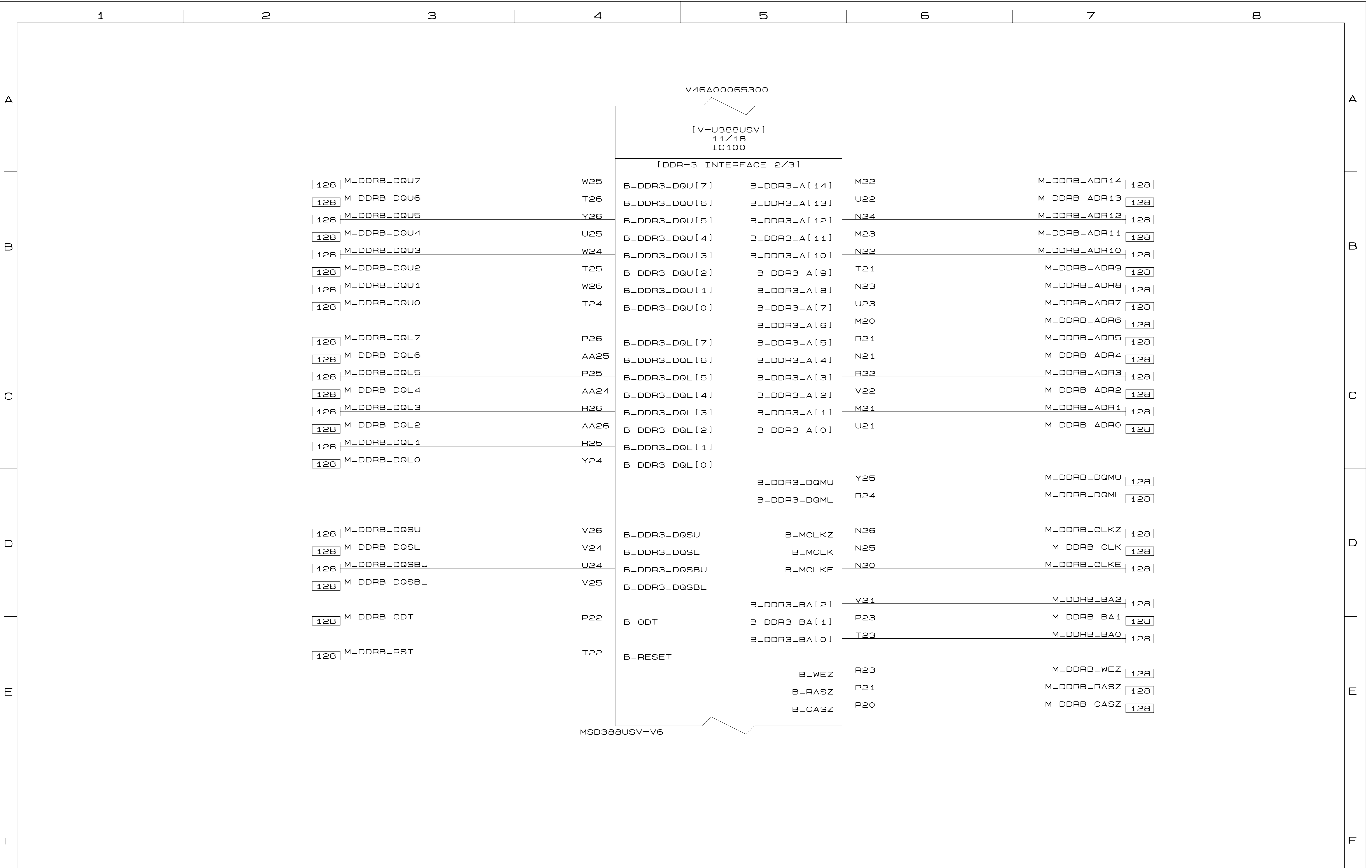
DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (LVDS 1/2)	SH. NO. 108	PAGE NO. 9	REV. MARK MP	DRAWING. NO. PE1063ZA
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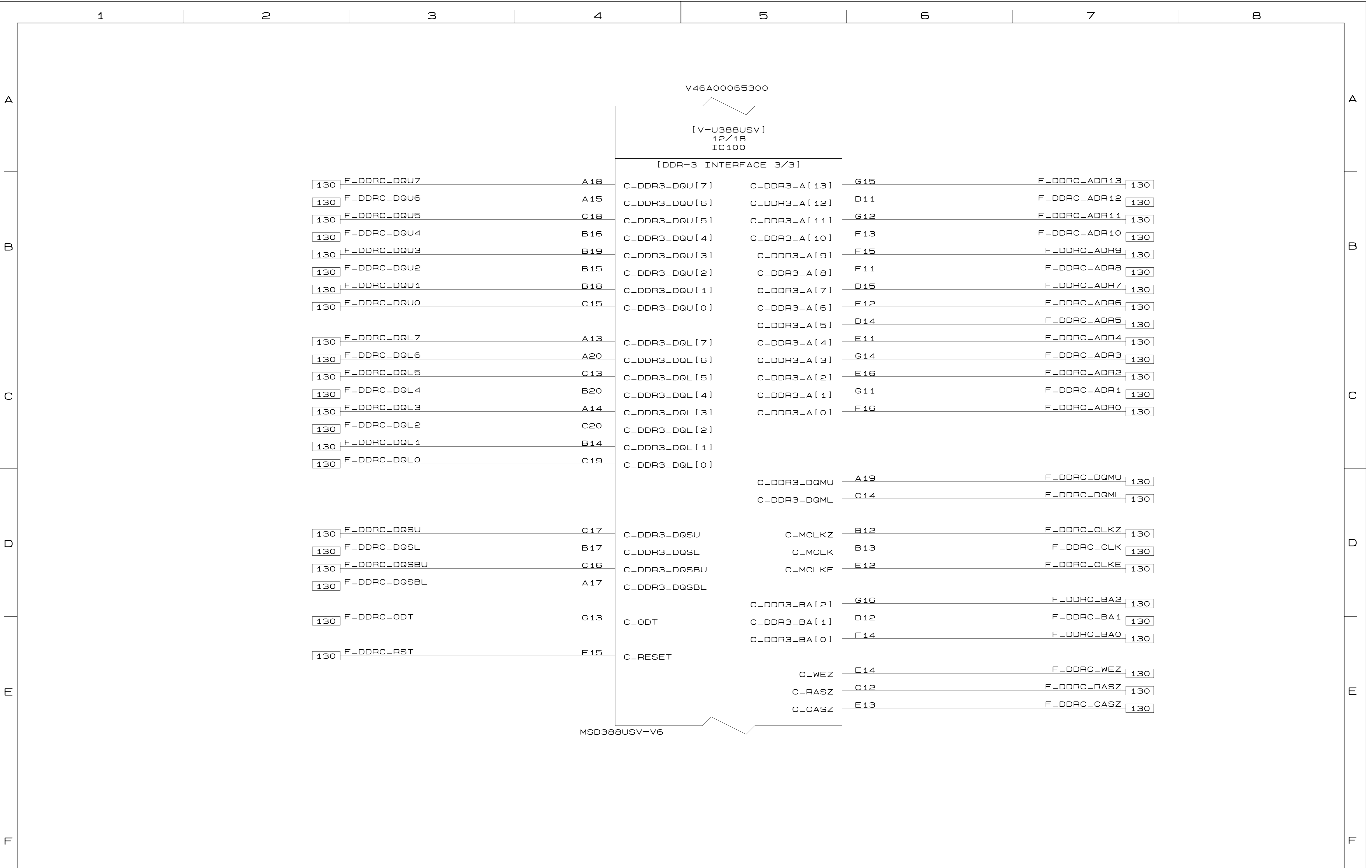
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (DDR A)	SH. NO. 110	PAGE NO. 11	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION MStar (DDR B)	SH. NO. 111	PAGE NO. 12	REV. MARK MP	DRAWING. NO. PE1063ZA
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MSD388USV-V6

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OSANAI

TITLE
MAIN UNIT

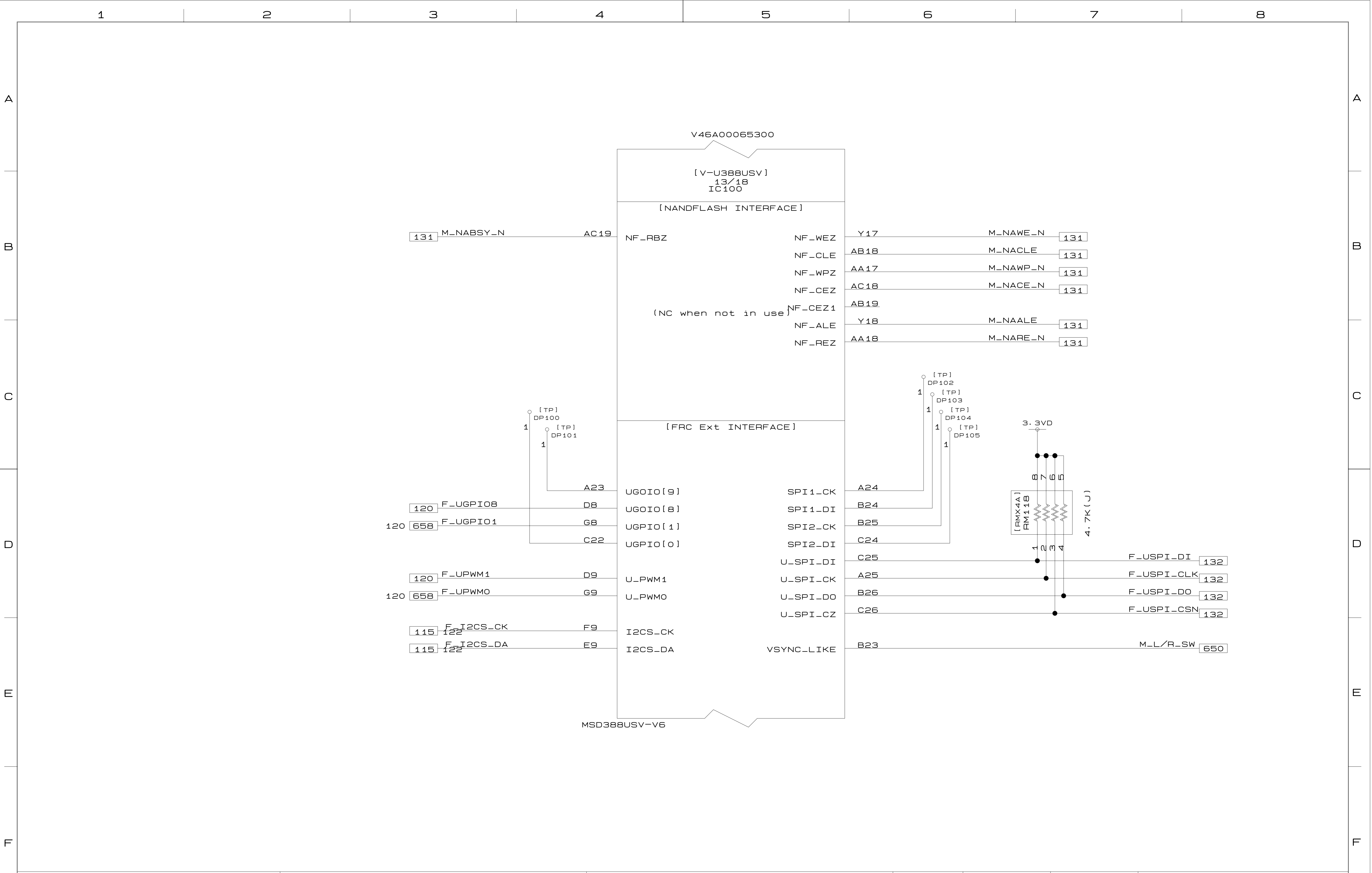
FUNCTION
MStar (DDR C)

SH. NO.
112

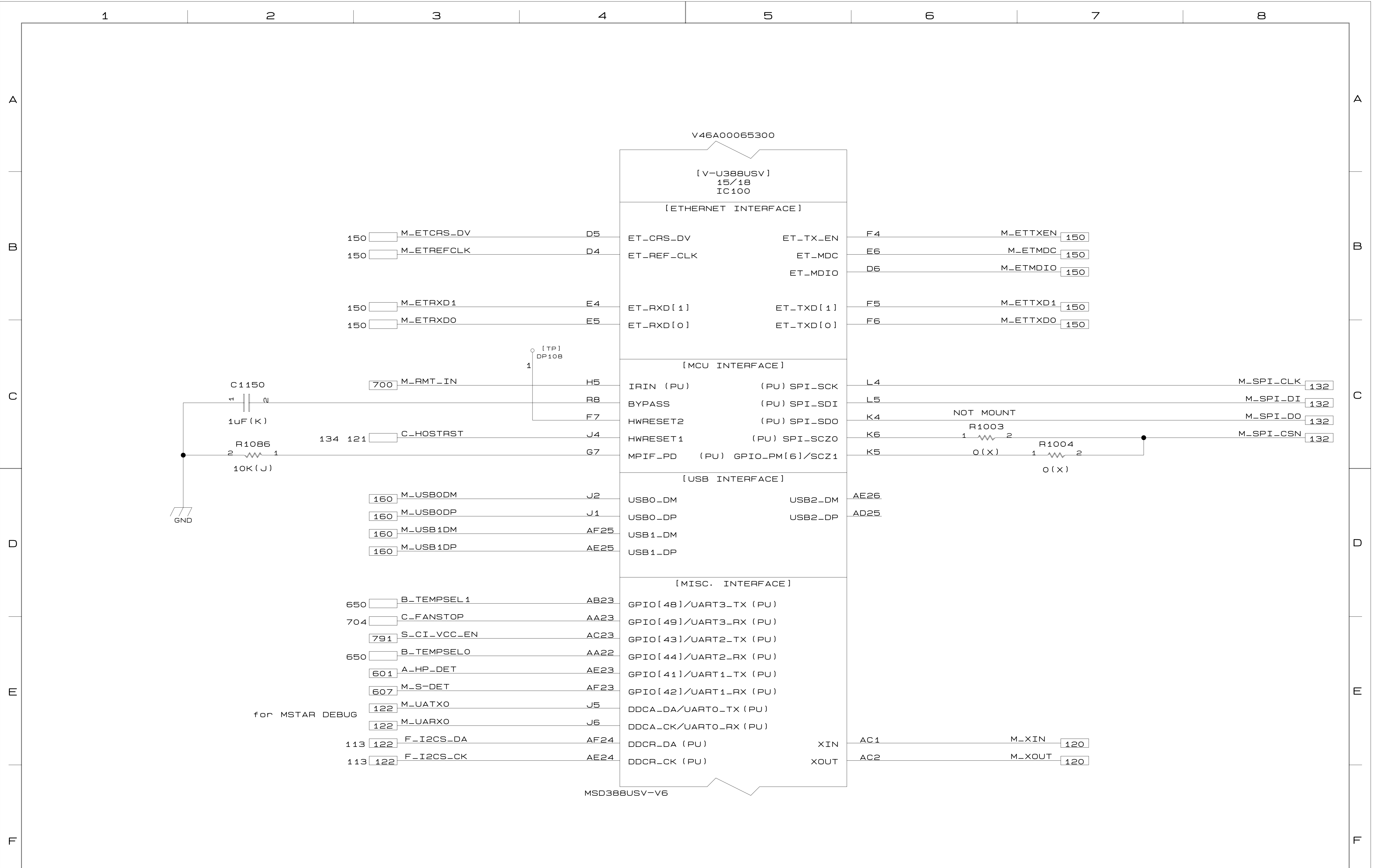
PAGE NO.
13

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION Mstar (NAND, FRC)	SH. NO. 113	PAGE NO. 14	REV. MARK MP	DRAWING. NO. PE1063ZA
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OSANAI

TITLE
MAIN UNIT

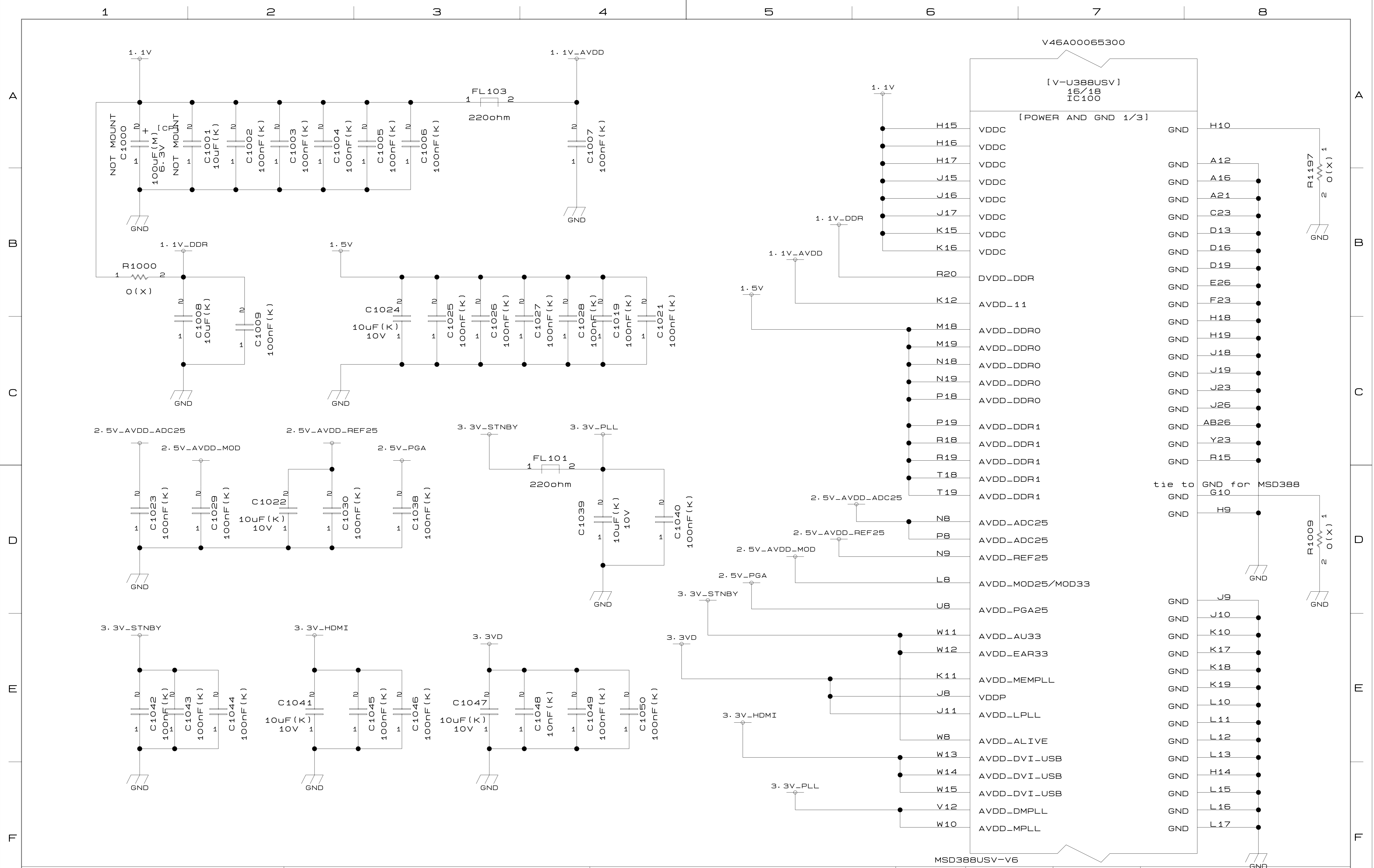
FUNCTION
Mstar (USB, MISC) 115

SH. NO.

PAGE NO.
16

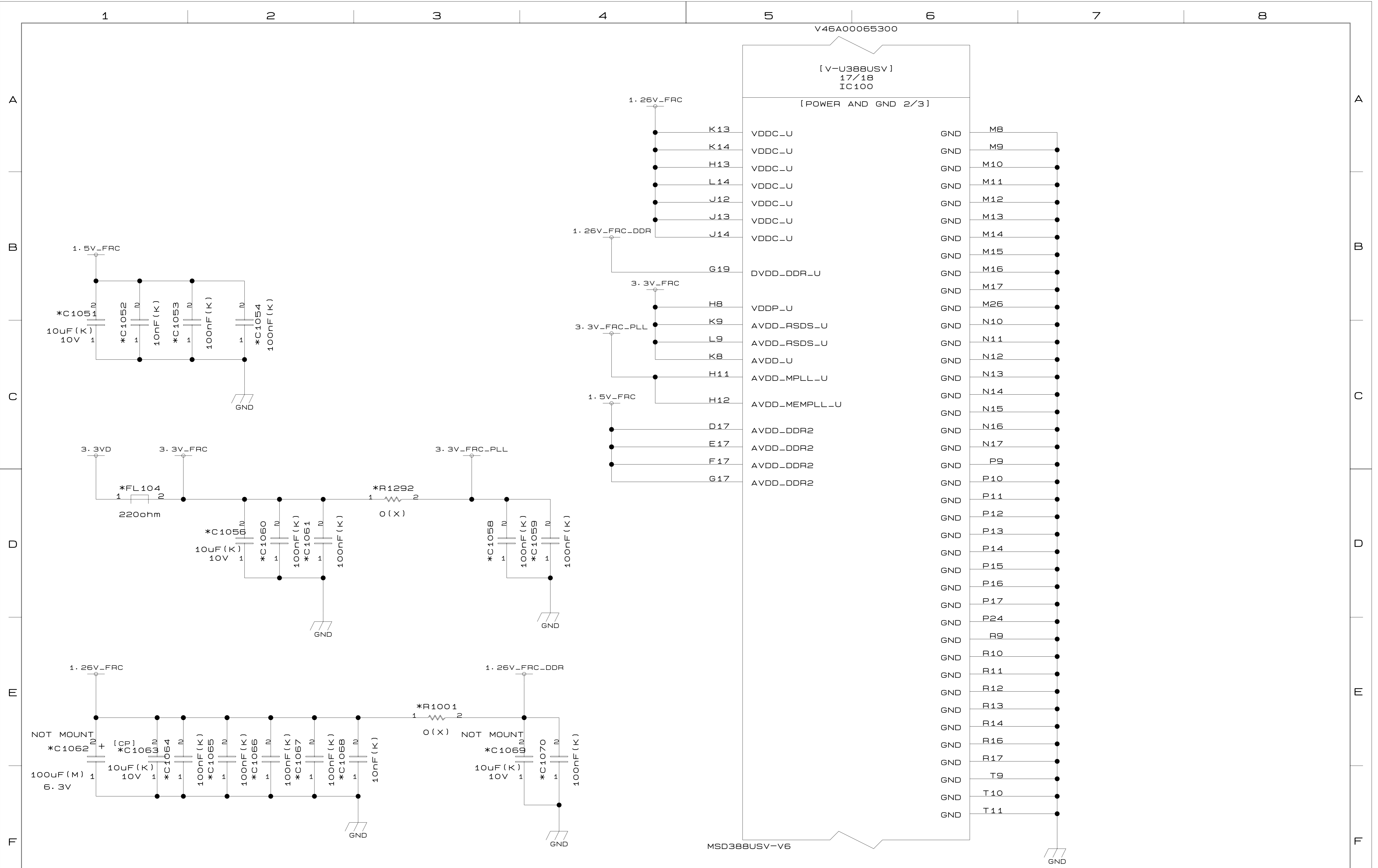
REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
OSANAI	MAIN UNIT	MStar (Power 1/3)	116	17	MP	PE1063ZA

2012.01.19 13:52



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OSANAI

TITLE
MAIN UNIT

FUNCTION
MStar (Power 2/3)

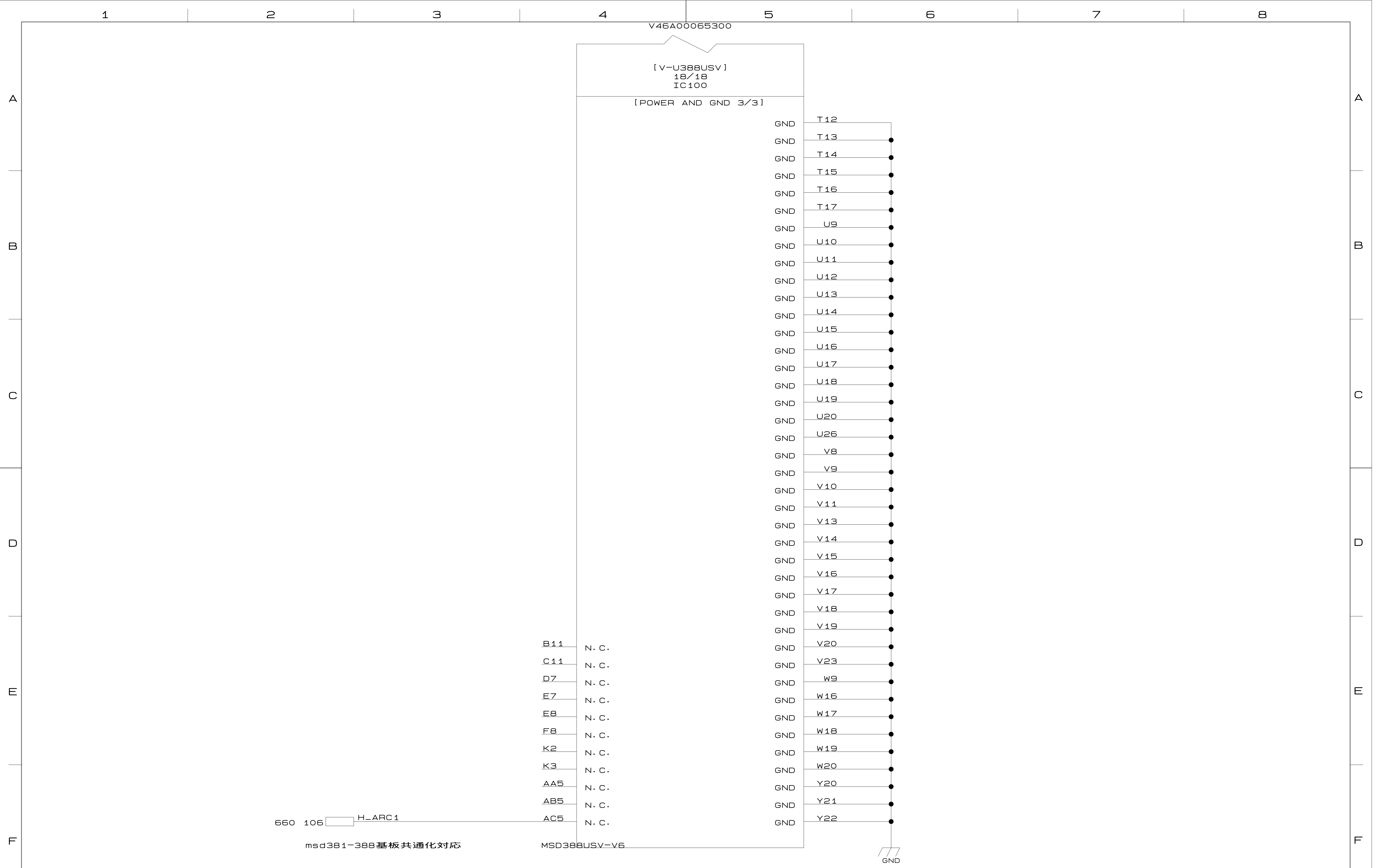
SH. NO.
117

PAGE NO.
18

REV. MARK
MP

DRAWING. NO.
PE1063ZA

2012.01.19 13:52



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OSANAI

TITLE
MAIN UNIT

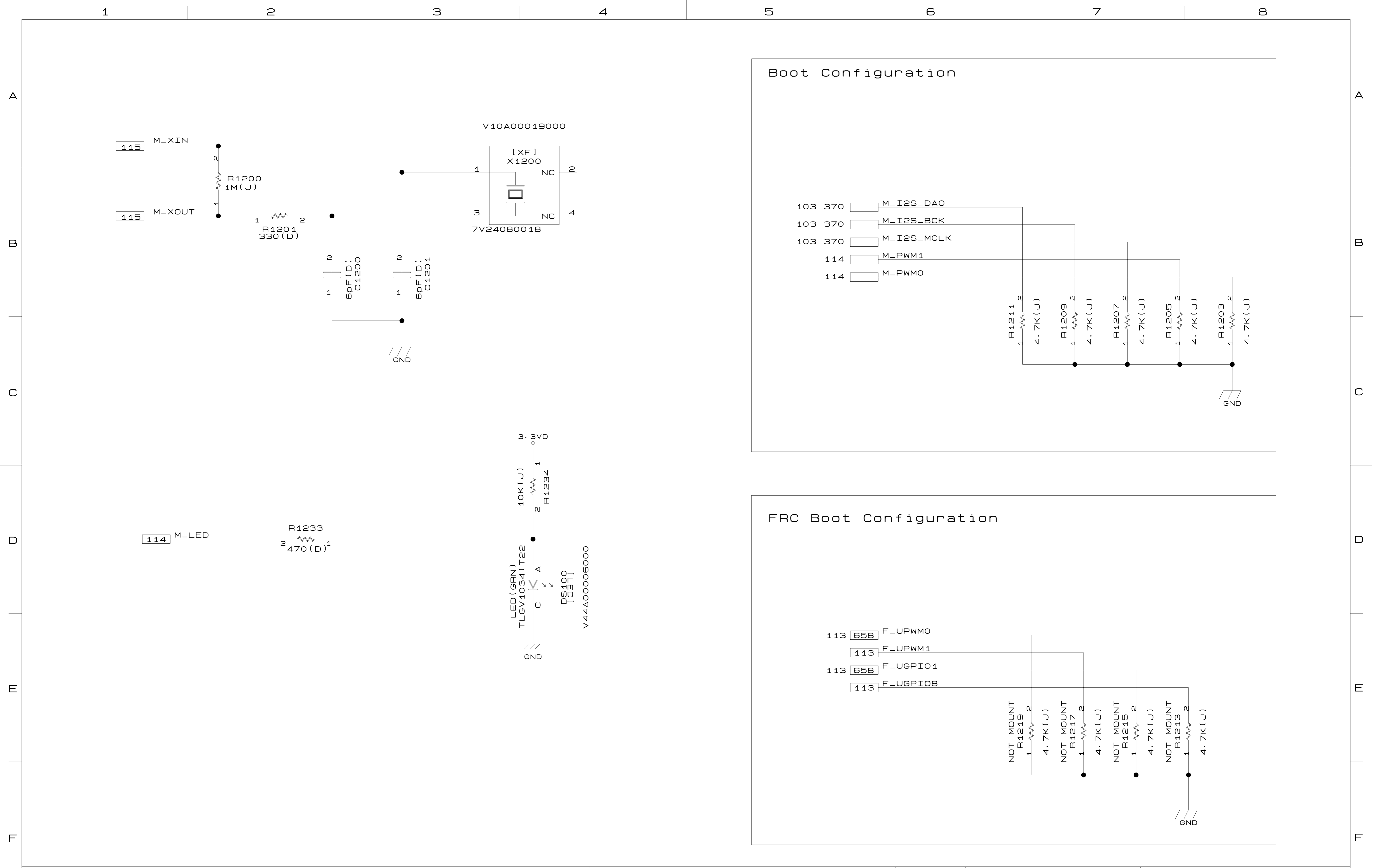
FUNCTION
MStar (Power 3/3) 118

SH. NO.
118

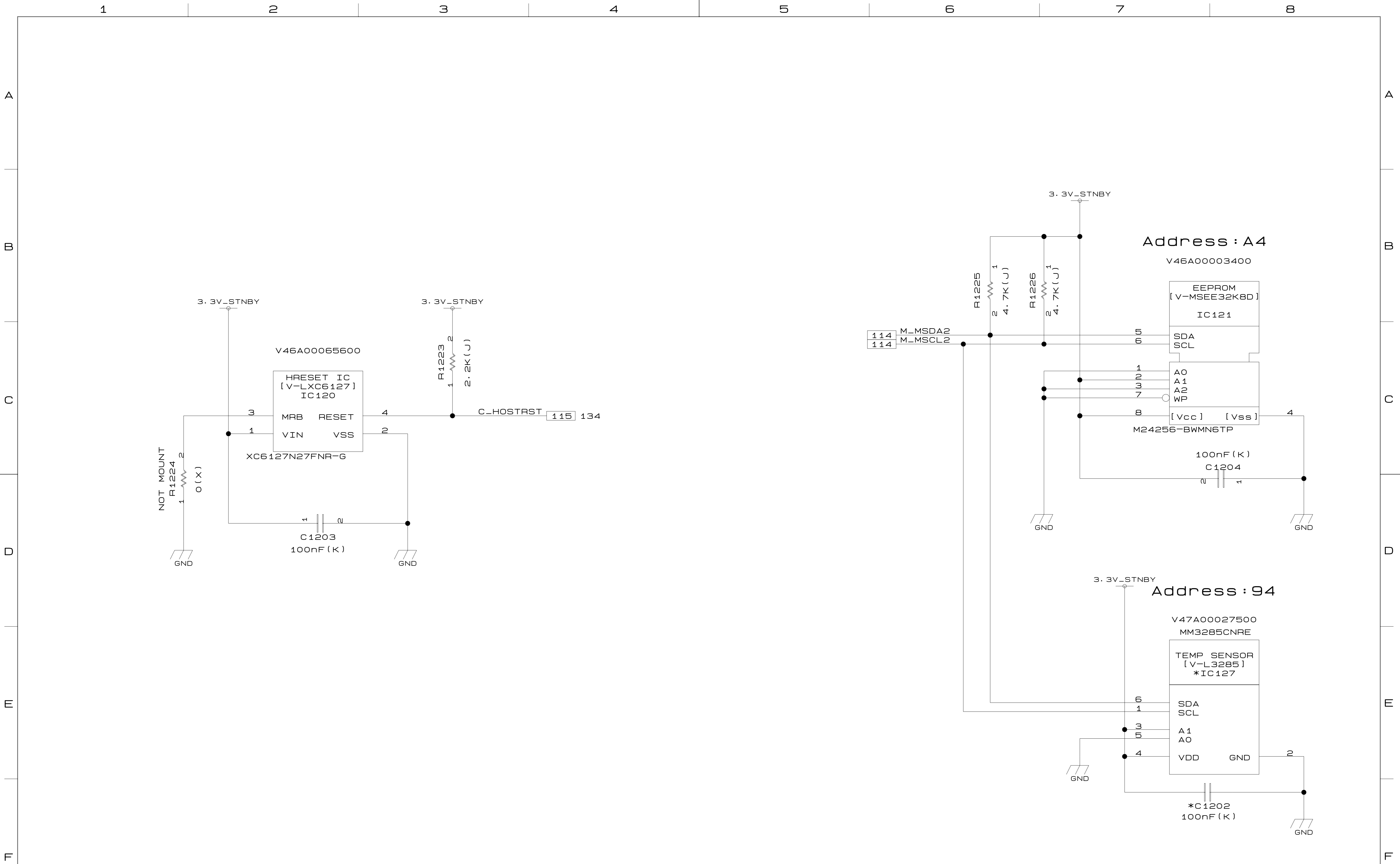
PAGE NO.
19

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION PERI (XTAL, STRAPS)	SH. NO. 120	PAGE NO. 20	REV. MARK MP	DRAWING. NO. PE1063ZA
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OSANAI

TITLE
MAIN UNIT

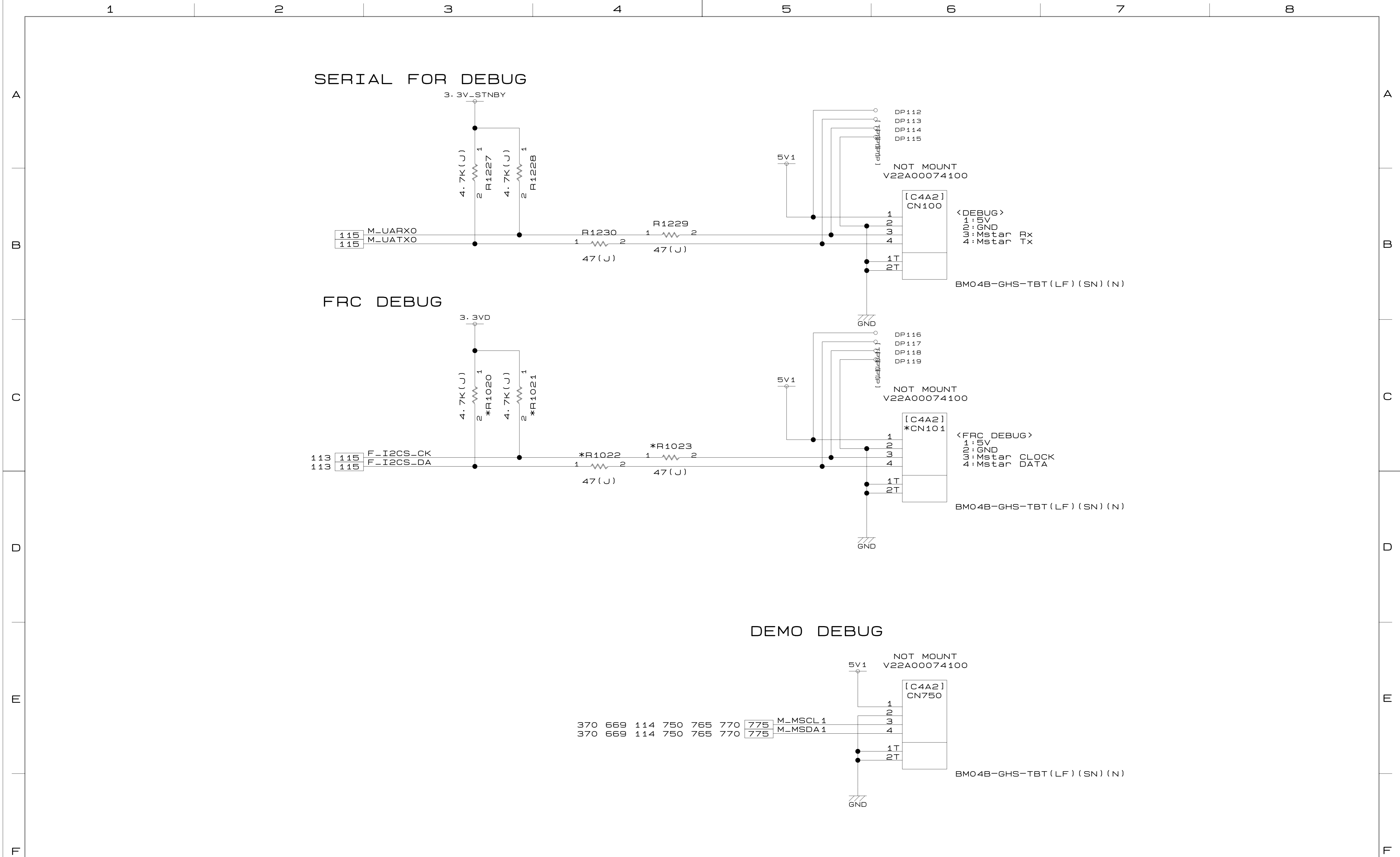
FUNCTION
RESET, I2C

SH. NO.
121

PAGE NO.
21

REV. MARK
MP

DRAWING. NO.
PE1063ZA



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OSANAI

TITLE
MAIN UNIT

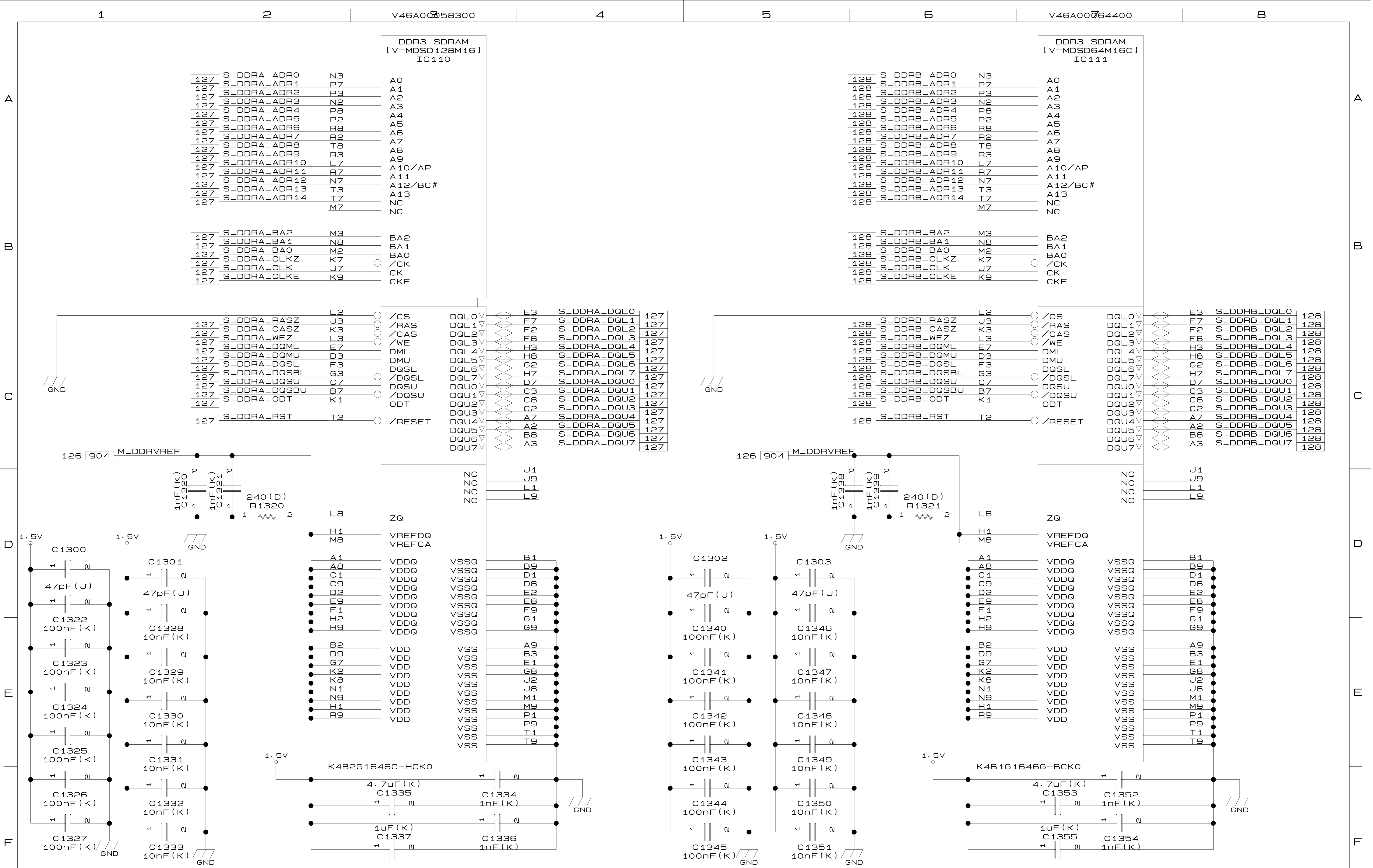
FUNCTION
PERI (SERIAL)

SH. NO.
122

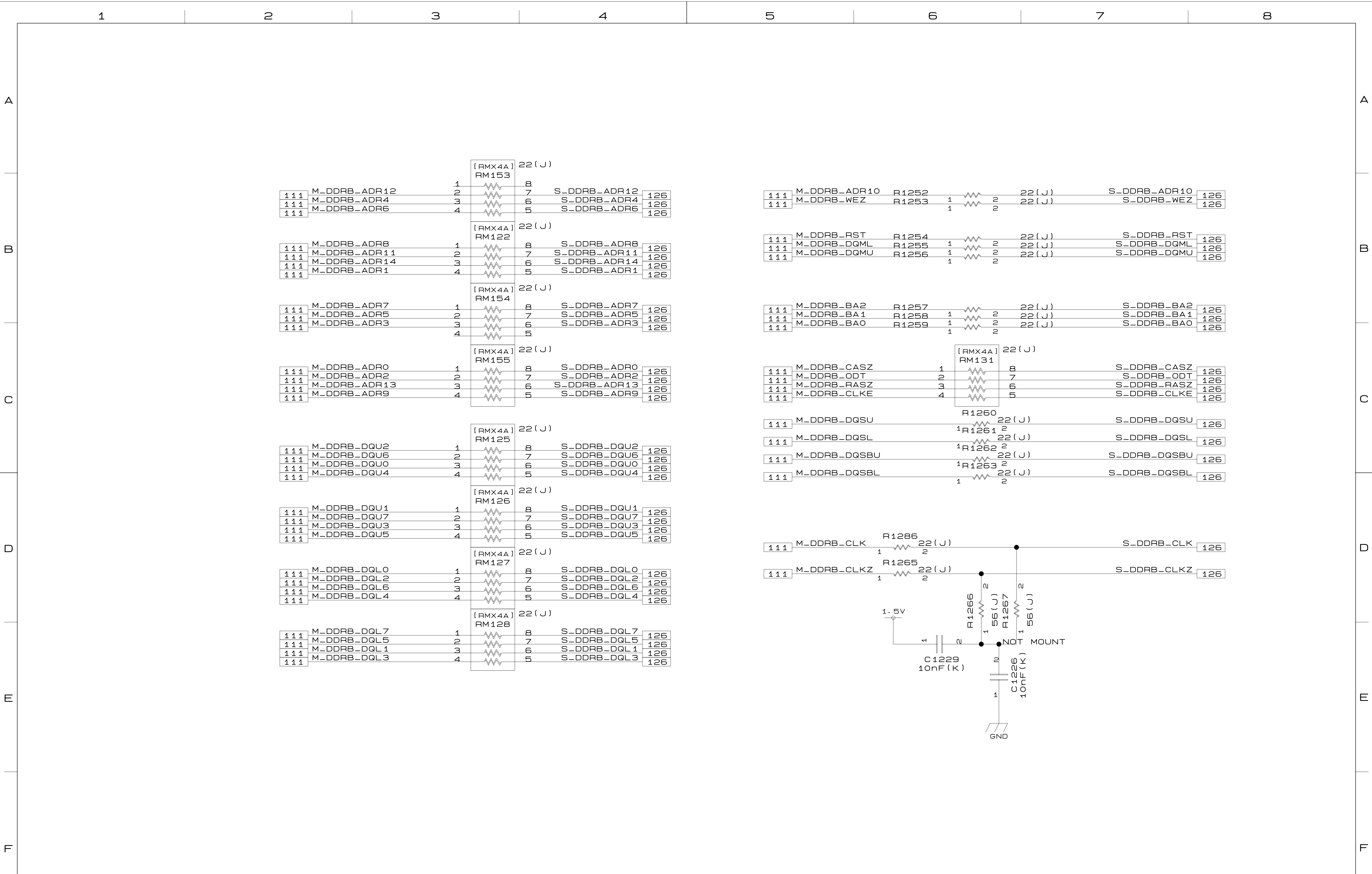
PAGE NO.
22

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY	2011/11/21	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
OSANAI		MAIN UNIT	DDR3 SDRAM	126	23	MP	PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

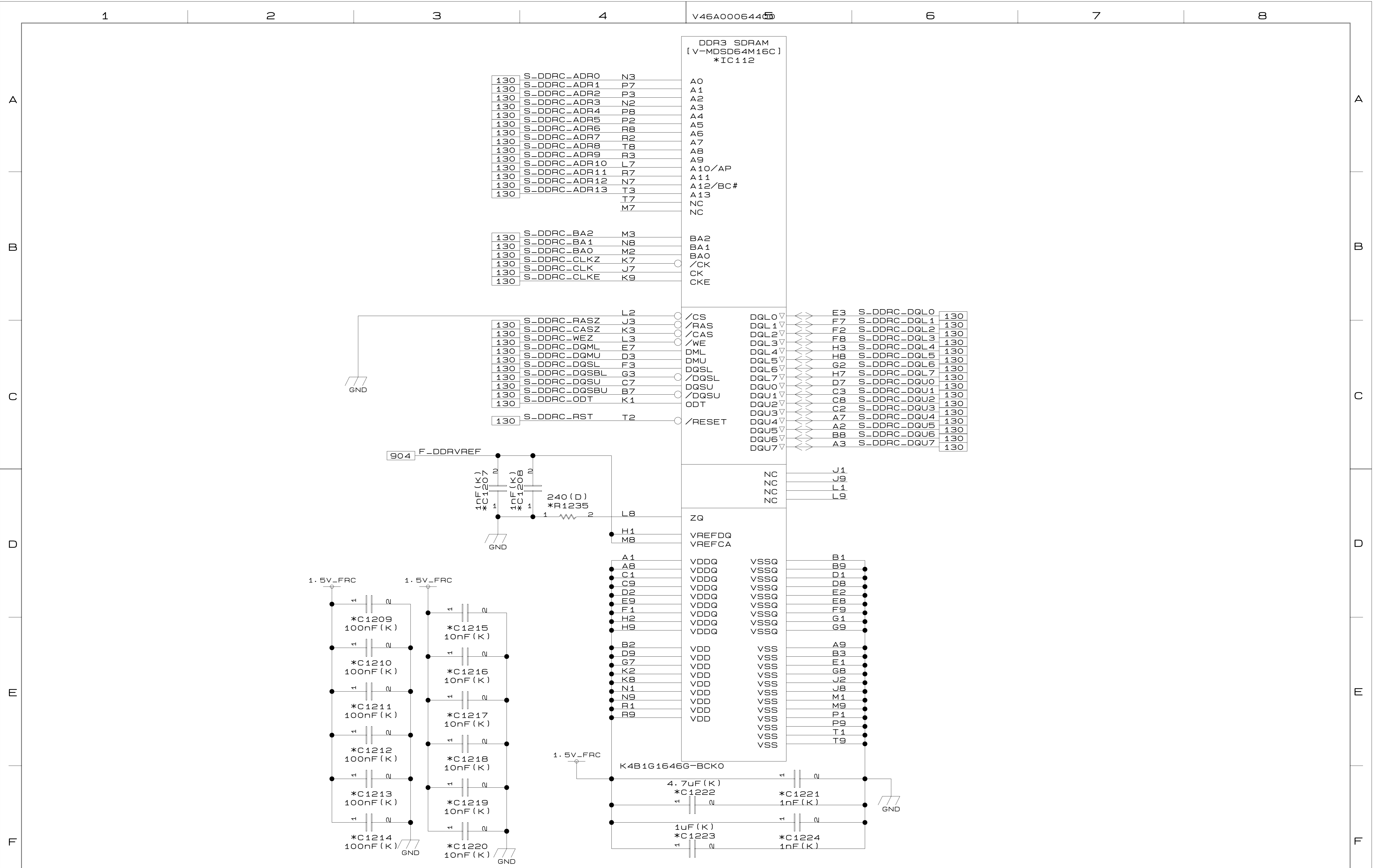
FUNCTION
DDR3 Damping Resistors

SH. NO.
128

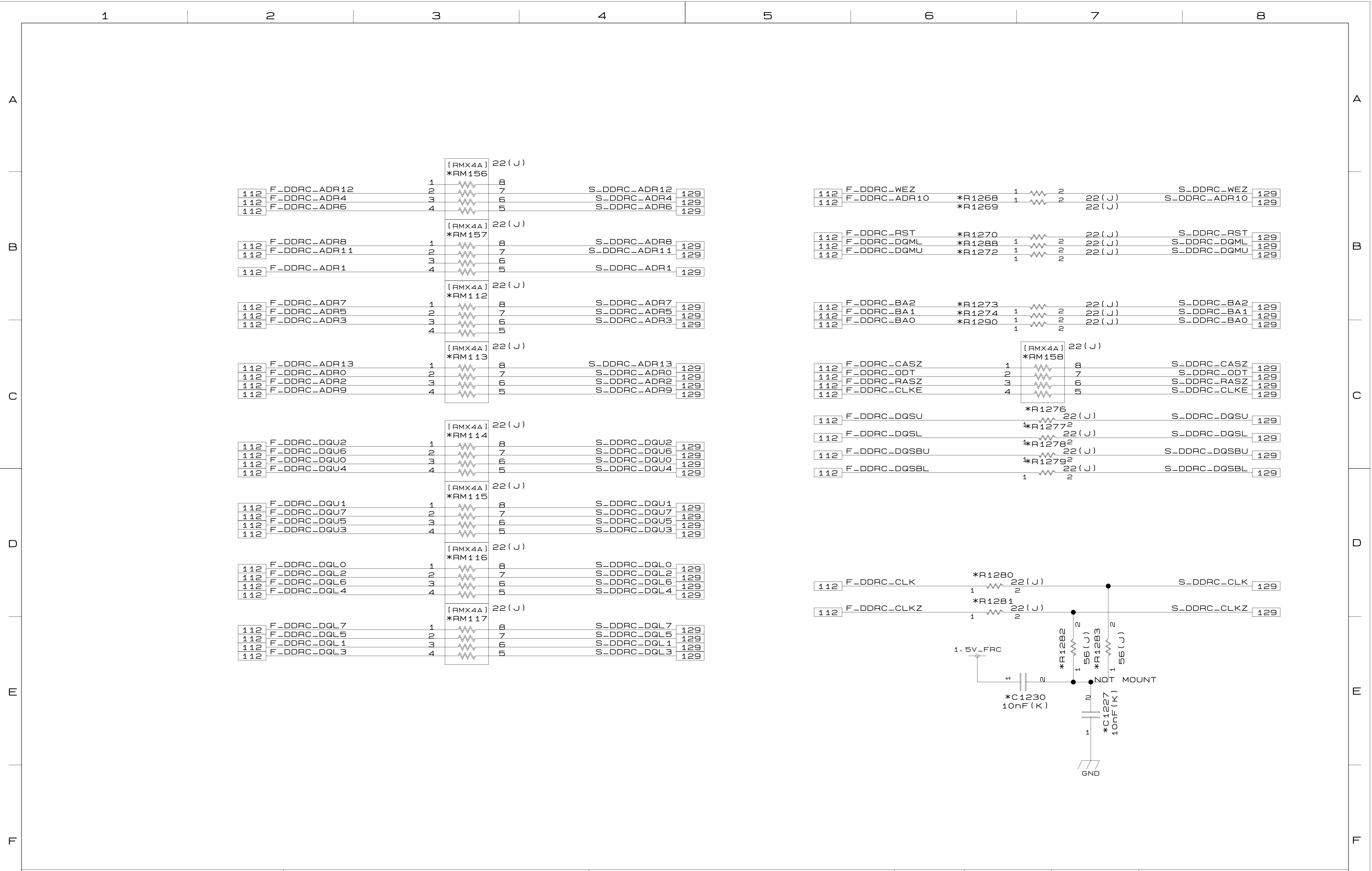
PAGE NO.
25

REV. MARK
MP

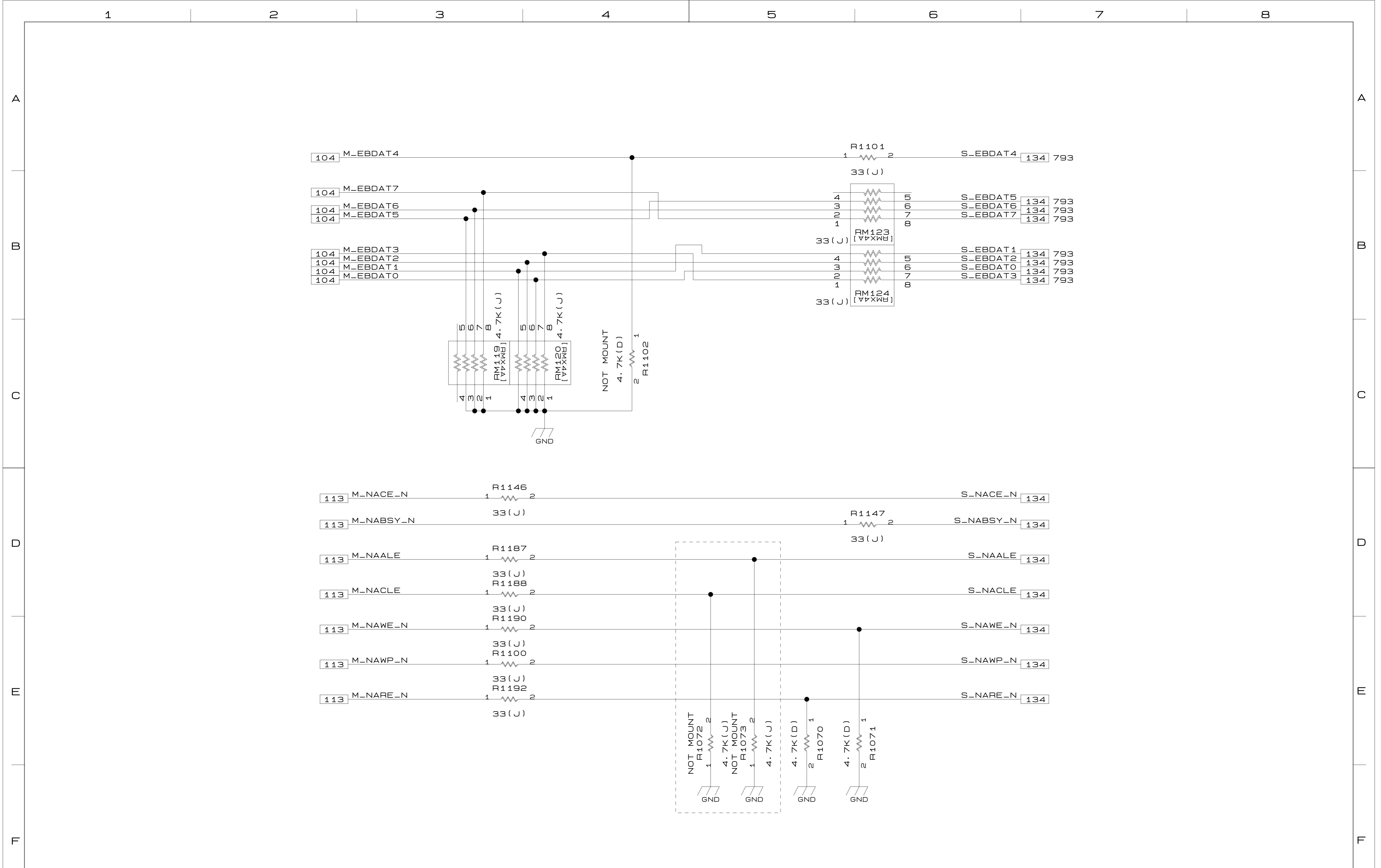
DRAWING. NO.
PE1063ZA



DESIGNED BY	2011/11/21	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
OSANAI		MAIN UNIT	DDR3 SDRAM	129	26	MP	PE1063ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION DDR3 Series Resistors	SH. NO. 130	PAGE NO. 27	REV. MARK MP	DRAWING. NO. PE1063ZA
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OSANAI

TITLE

MAIN UNIT

FUNCTION

EBUS RESISTOR

SH. NO.

131

PAGE NO.

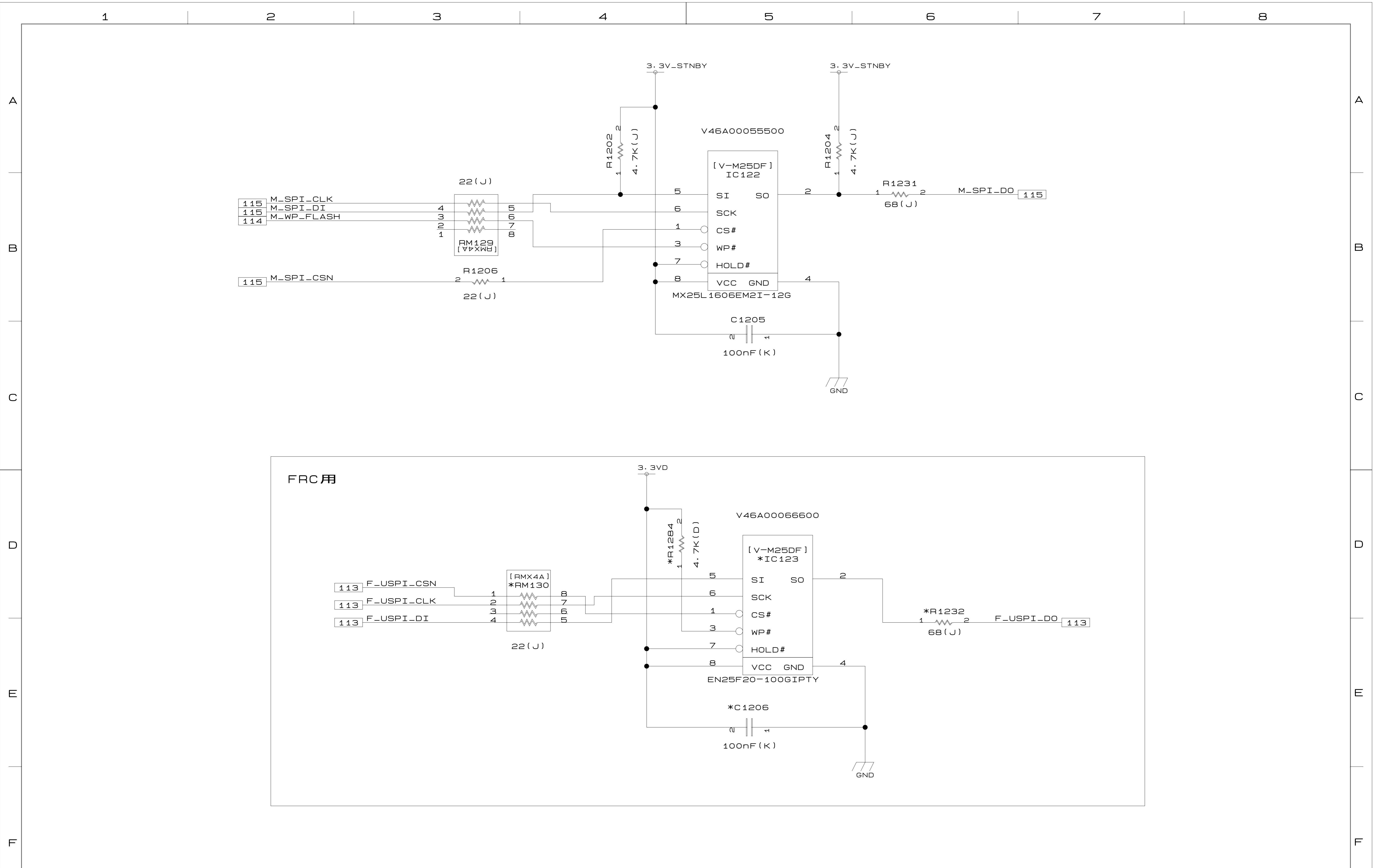
28

REV. MARK

MP

DRAWING. NO.

PE1063ZA



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OSANAI

TITLE
MAIN UNIT

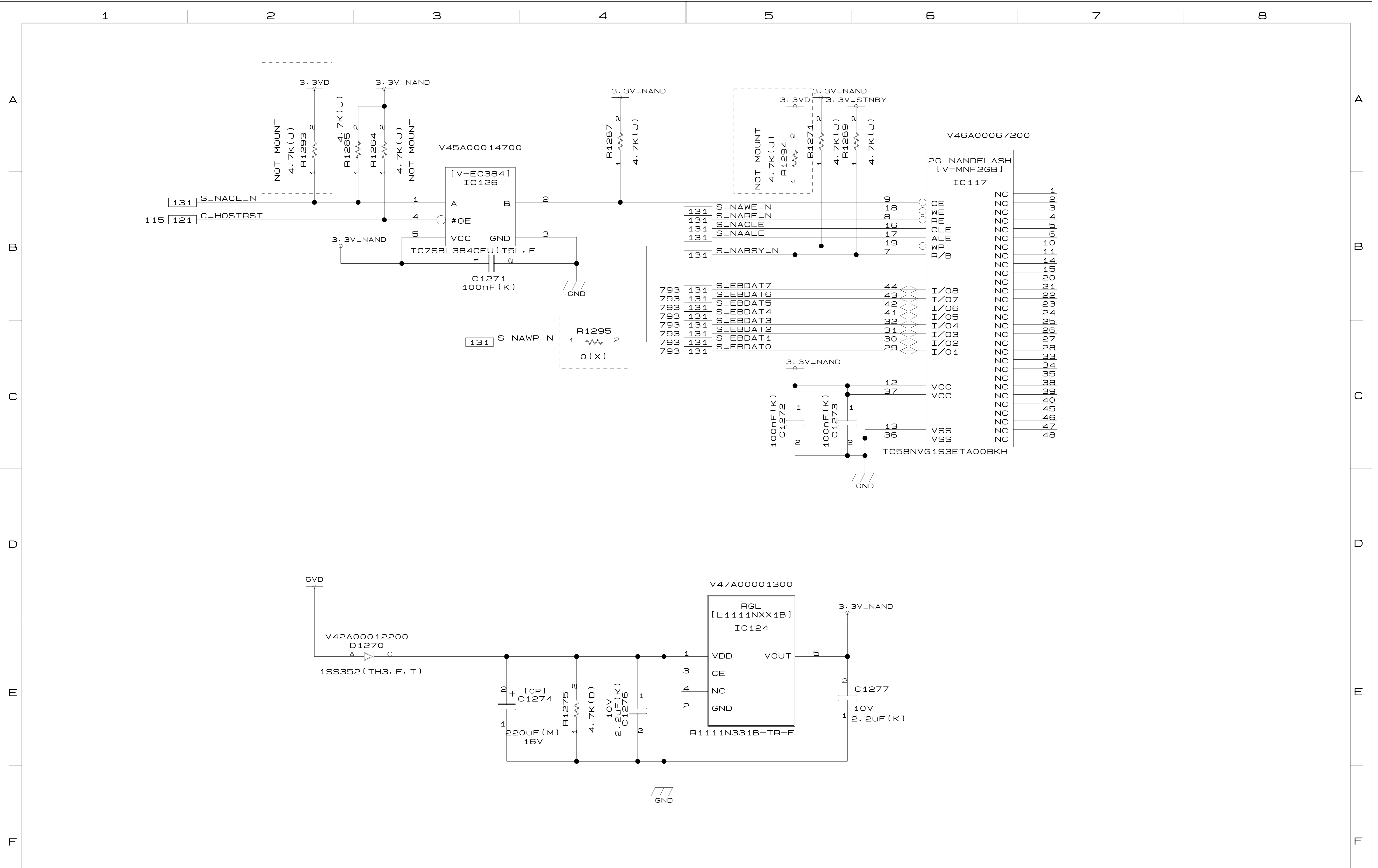
FUNCTION
SPI FLASH

SH. NO.
132

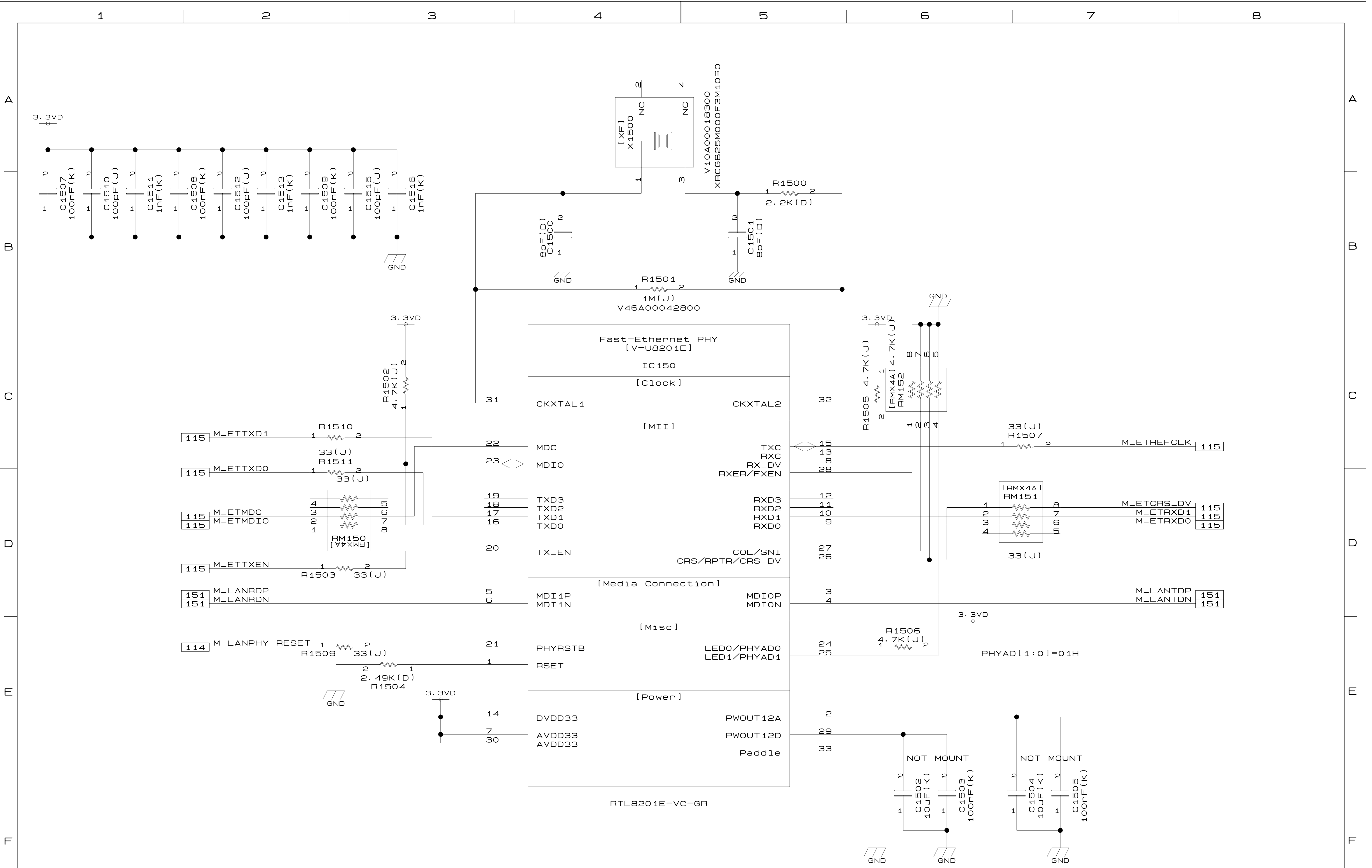
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29

REV. MARK
MP

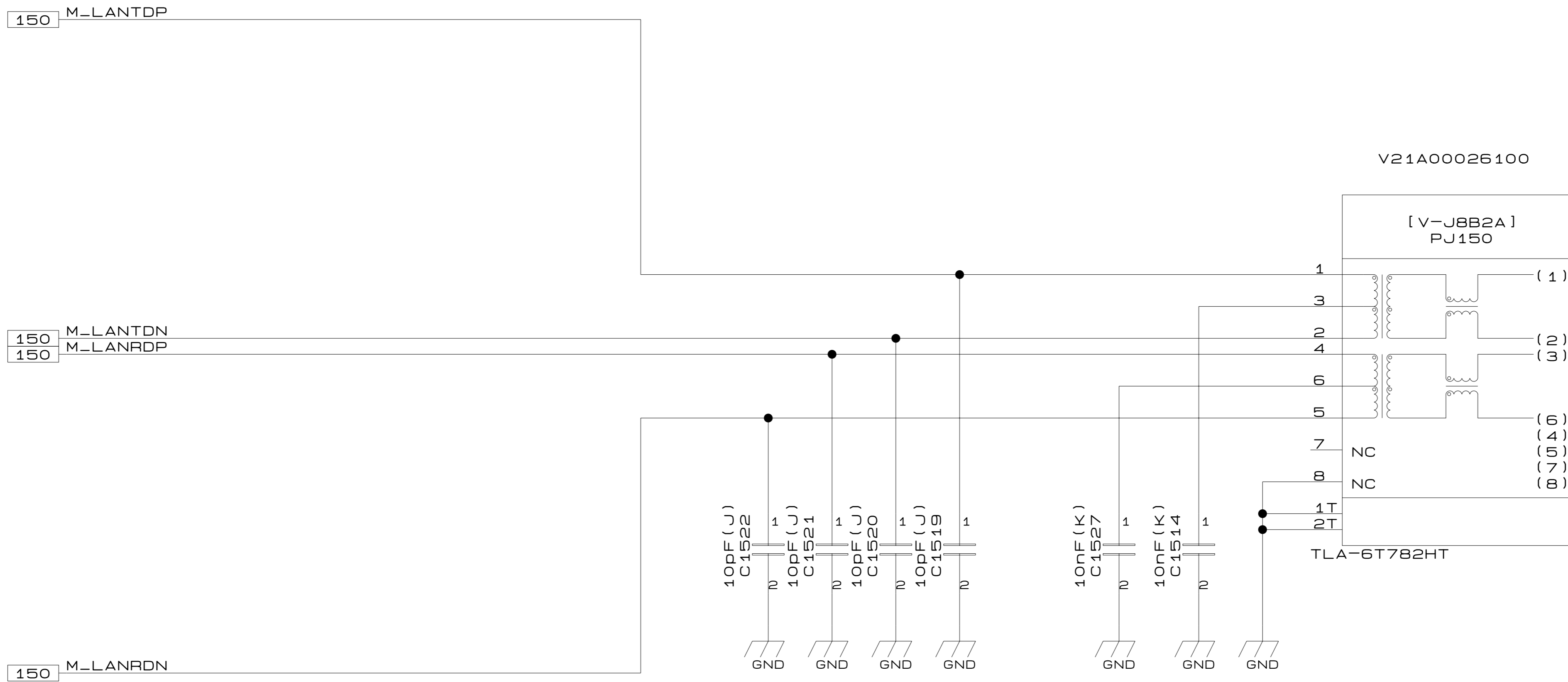
DRAWING. NO.
PE1063ZA



DESIGNED BY OSANAI	2011/11/21	TITLE MAIN UNIT	FUNCTION NAND FLASH	SH. NO. 134	PAGE NO. 30	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY KOJIMA	TITLE MAIN UNIT	FUNCTION ETHER PHY	SH. NO. 150	PAGE NO. 31	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY
KOJIMA

TITLE
MAIN UNIT

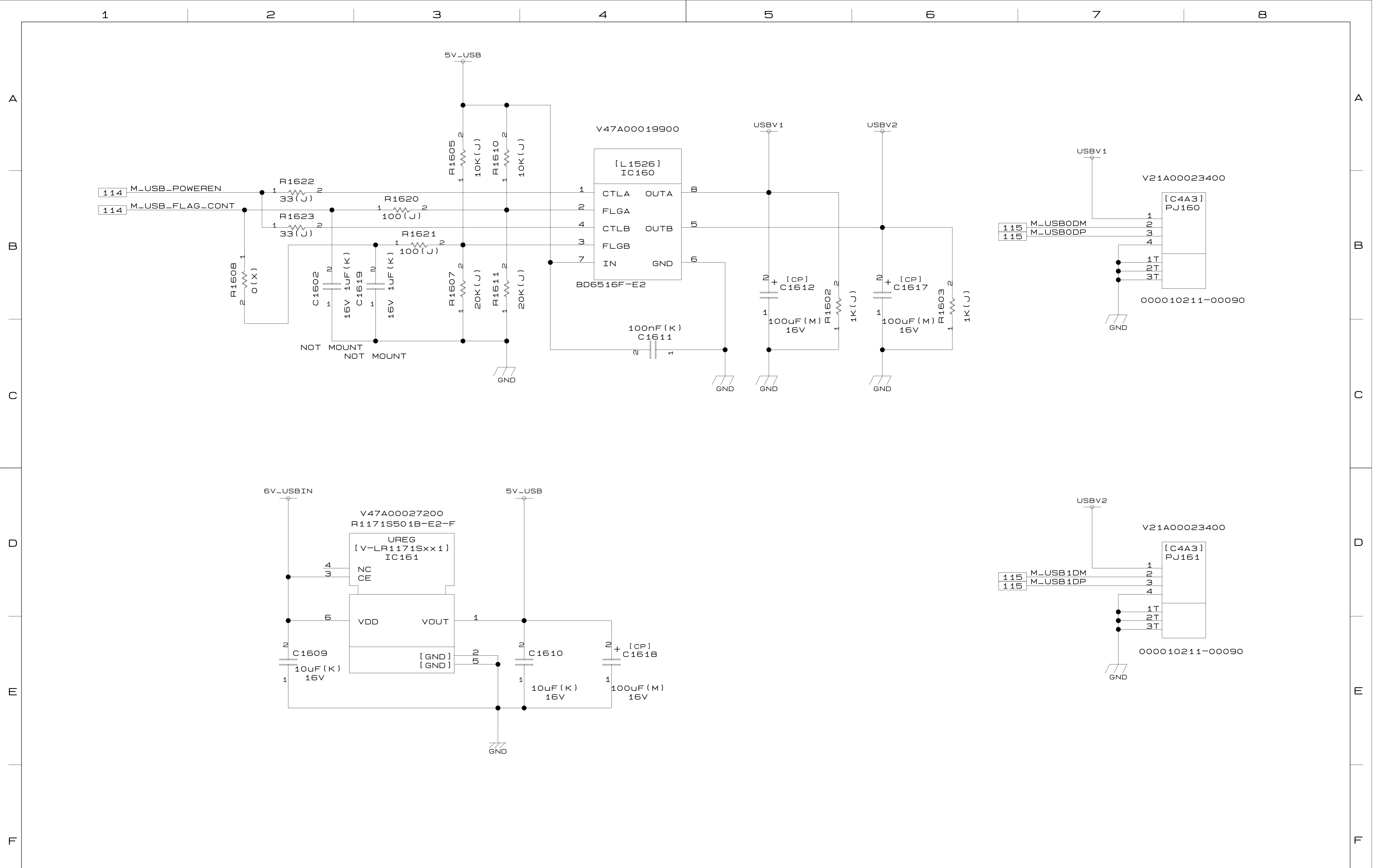
FUNCTION
RJ45 CONNECTOR

SH. NO.
151

PAGE NO.
32

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
KOJIMA

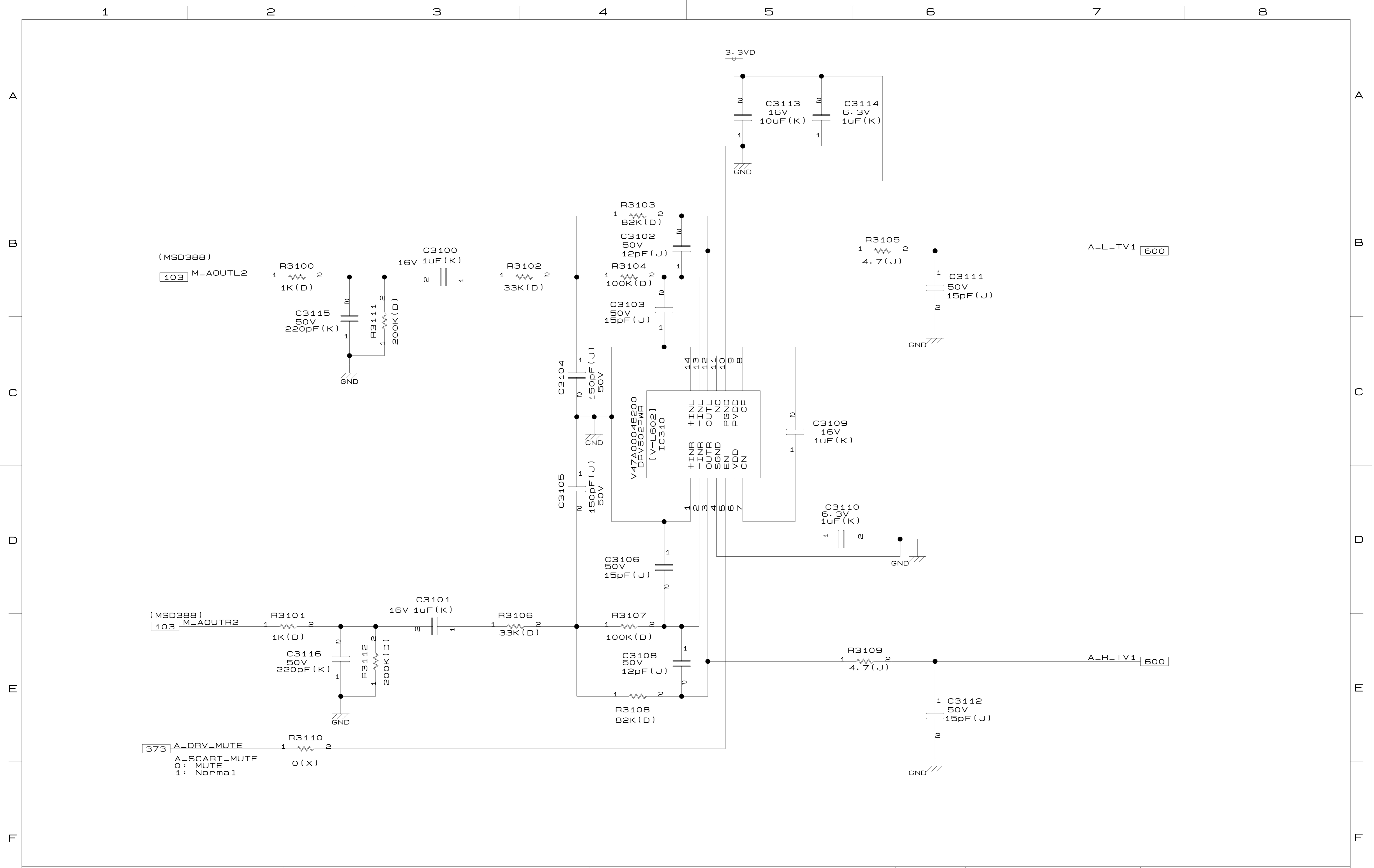
TITLE
MAIN UNIT

FUNCTION
USB CONNECTOR(side) 160

SH. NO.
PAGE NO. 33

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

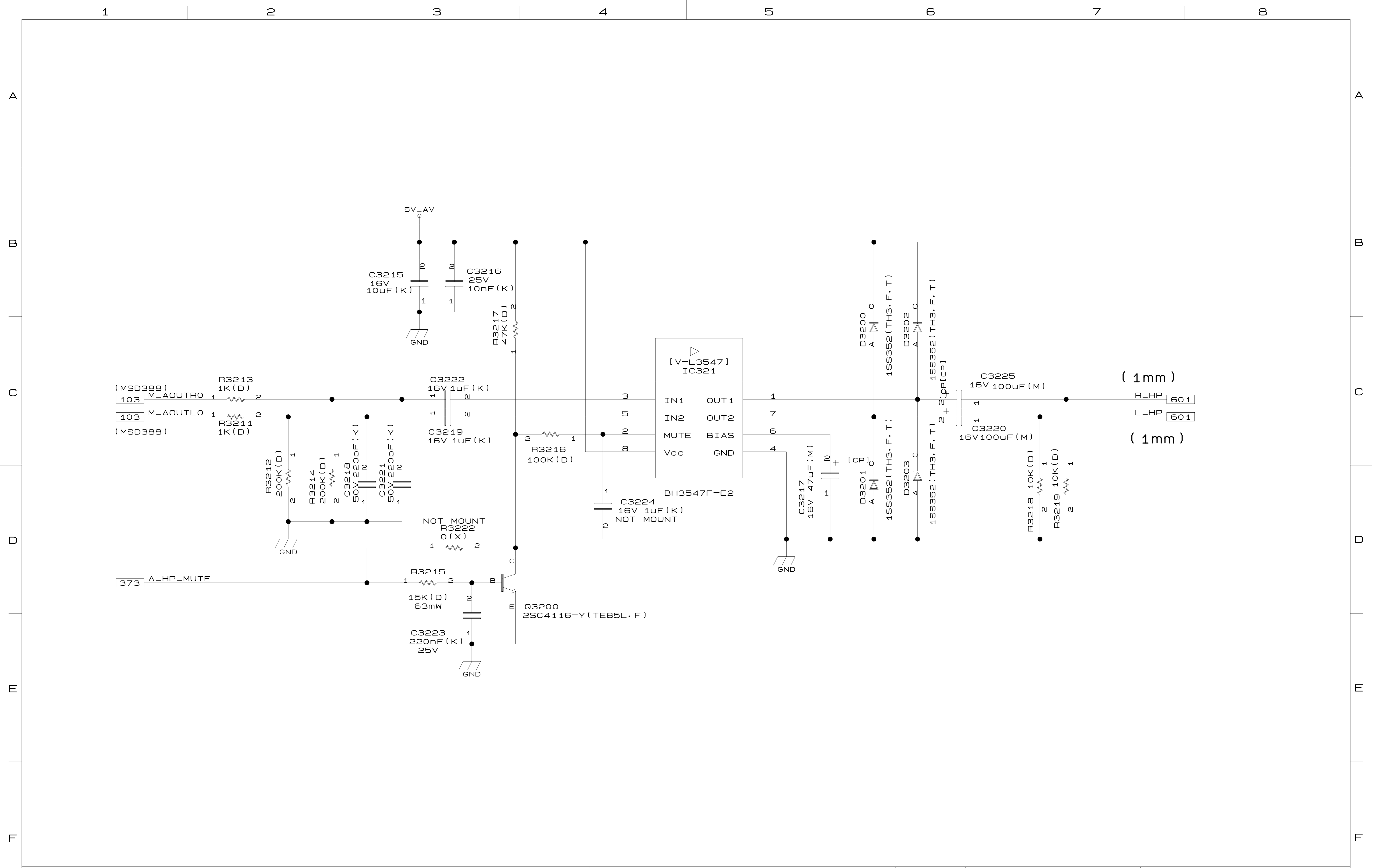
FUNCTION
AUDIO TV OUTPUT

SH. NO.
310

PAGE NO.
34

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

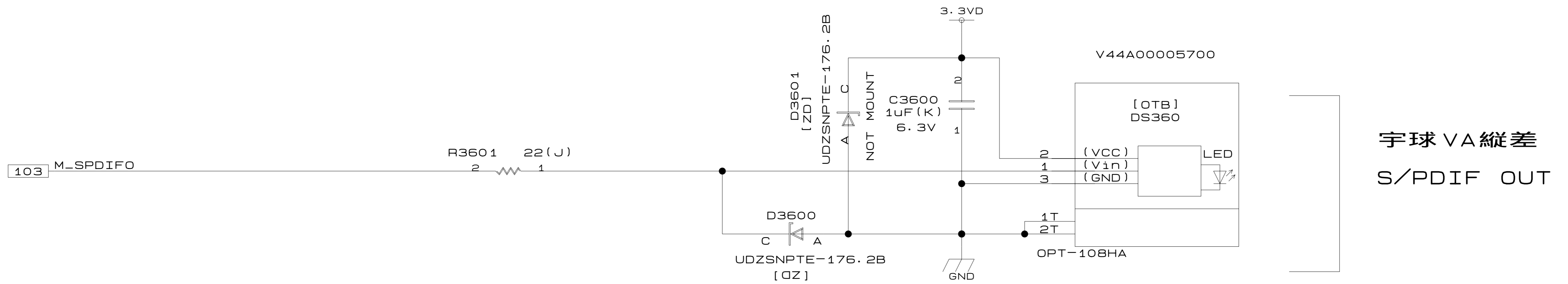
FUNCTION
Headphone AMP

SH. NO.
321

PAGE NO.
35

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

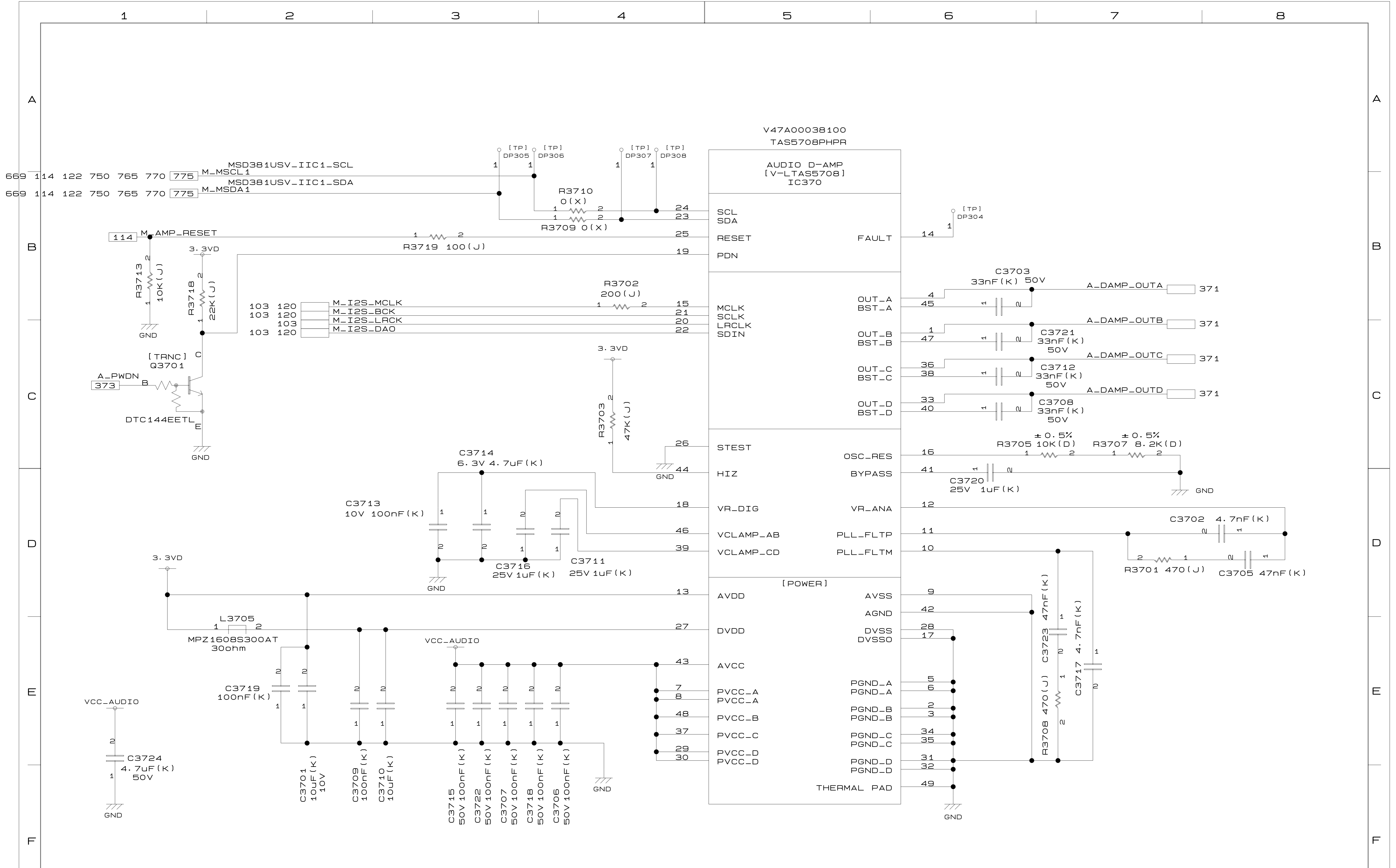
FUNCTION
OPTICAL OUTPUT

SH. NO.
360

PAGE NO.
36

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

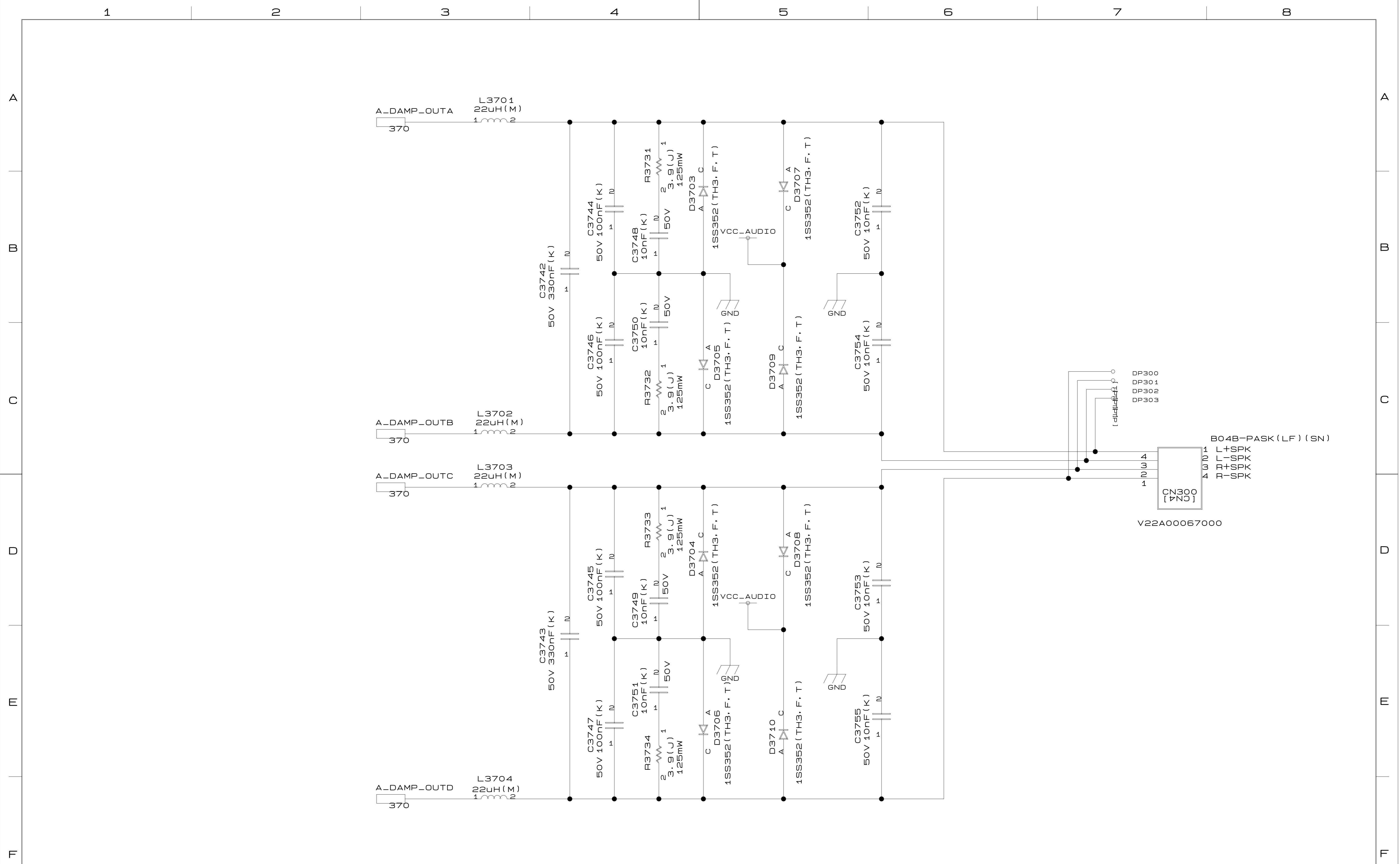
FUNCTION
AUDIO D-AMP (MAIN)

SH. NO.
370

PAGE NO.
37

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

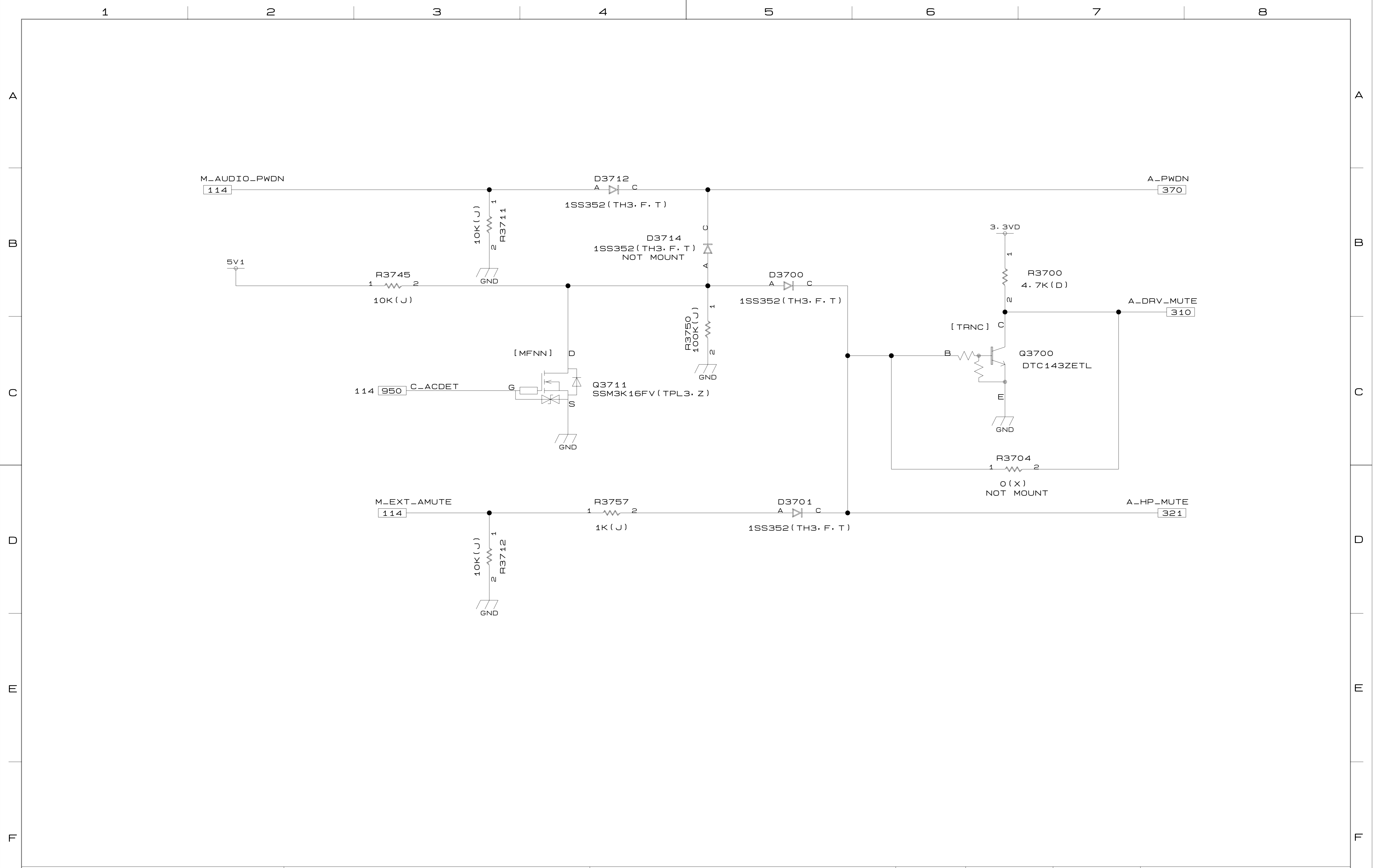
FUNCTION
AUDIO D-AMP OUT (MAIN)

SH. NO.
371

PAGE NO.
38

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
ENDO

TITLE
MAIN UNIT

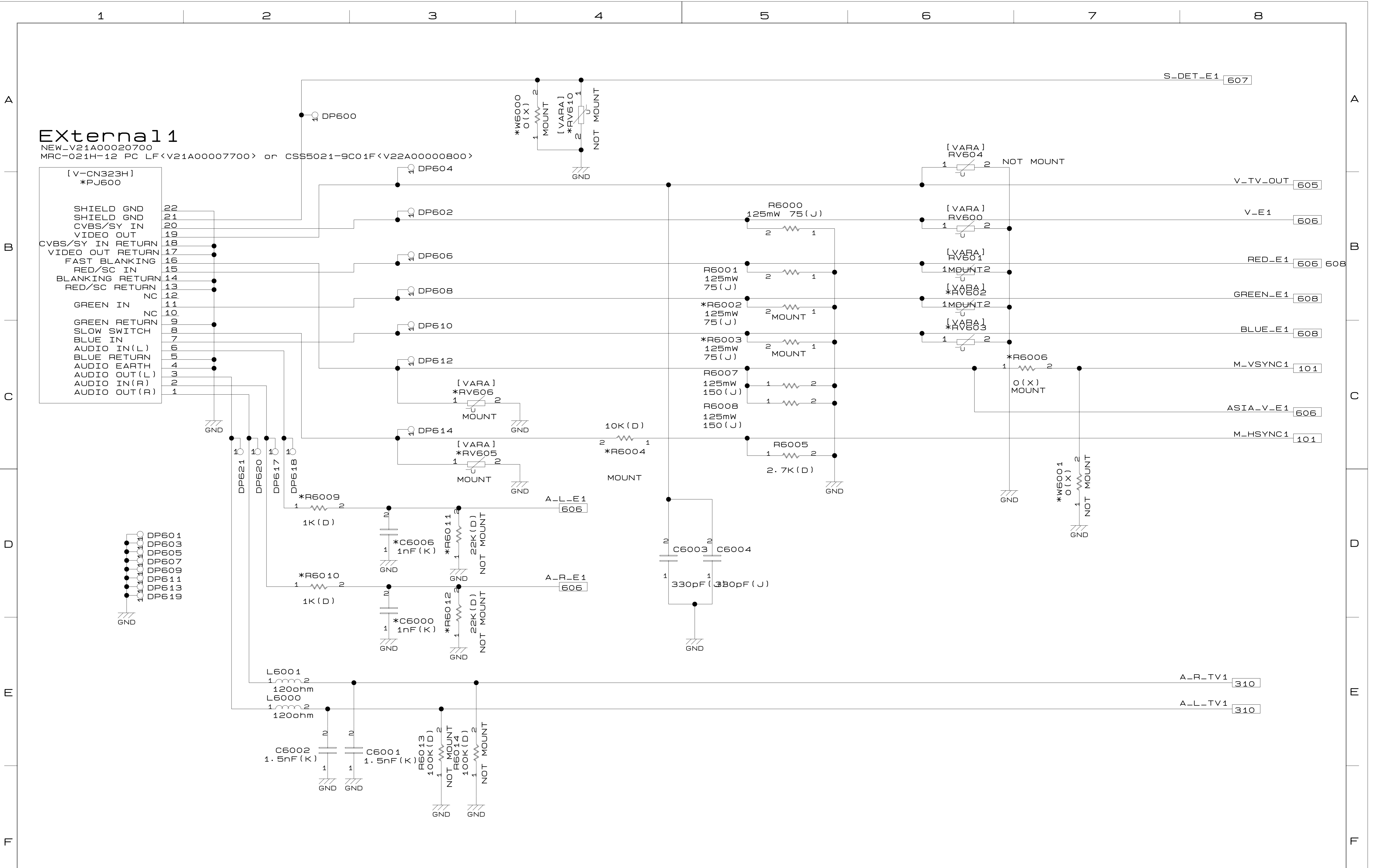
FUNCTION
AUDIO MUTE

SH. NO.
373

PAGE NO.
39

REV. MARK
MP

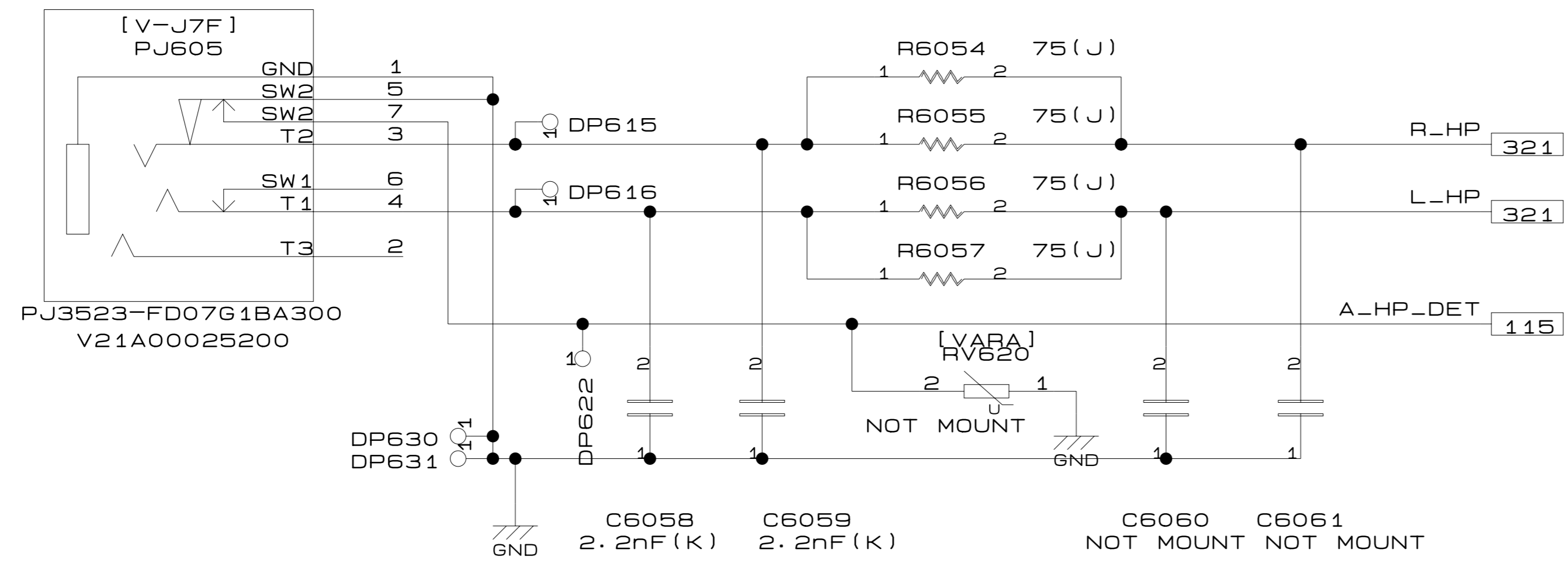
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PE1063ZA



DESIGNED BY SHIBATA/Audio	TITLE MAIN UNIT	FUNCTION EURO SCART	SH. NO. 600	PAGE NO. 40	REV. MARK MP	DRAWING. NO. PE1063ZA
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2012.01.19 13:53

Headphone
(3.5 /SIDE)



DESIGNED BY
SHIBATA/Audio

TITLE
MAIN UNIT

FUNCTION
FRONT AV

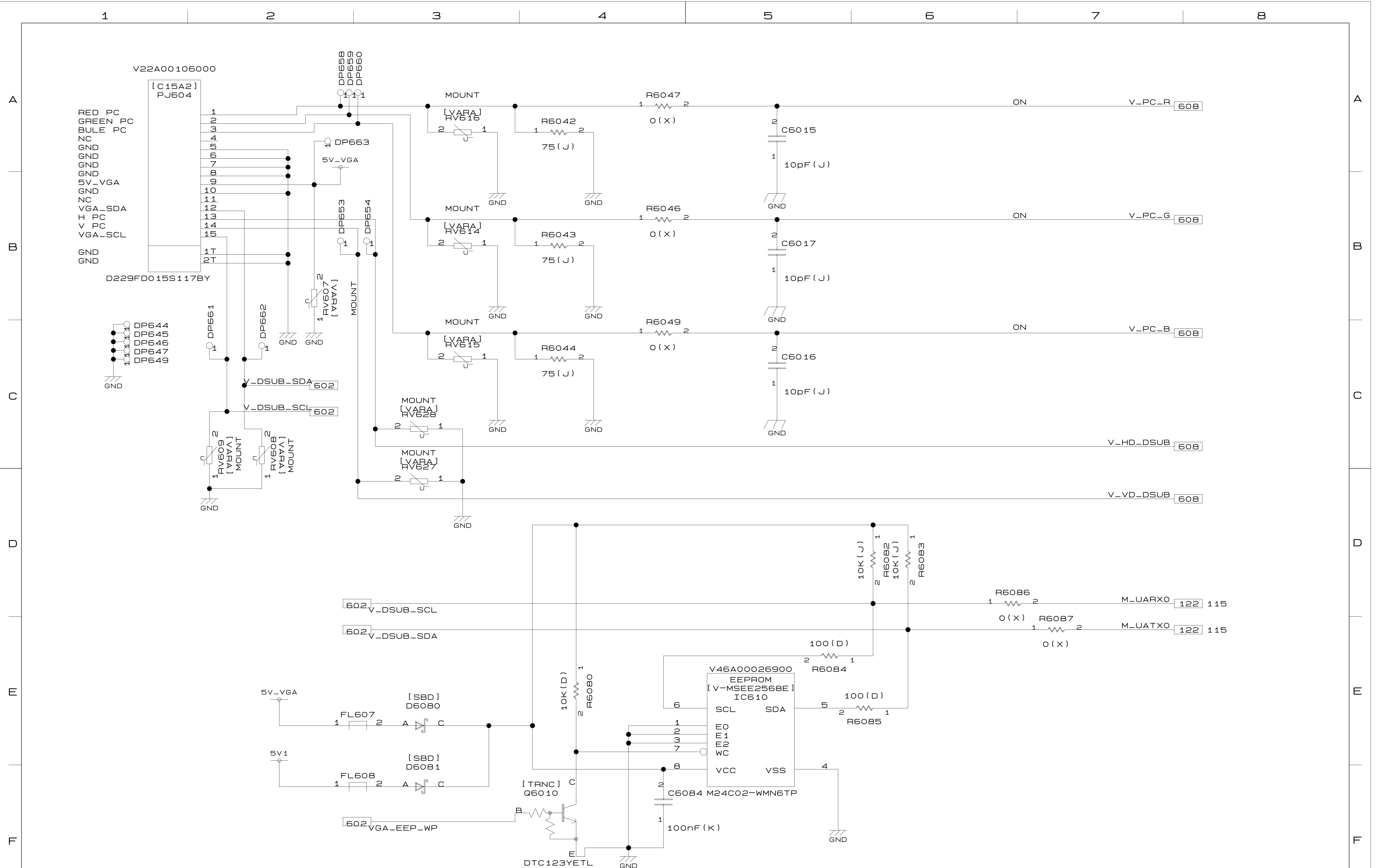
SH. NO.
601

PAGE NO.
41

REV. MARK
MP

DRAWING. NO.
PE1063ZA

2012.01.19 13:53



DESIGNED BY
SHIBATA/Horikoshi

TITLE
MAIN UNIT

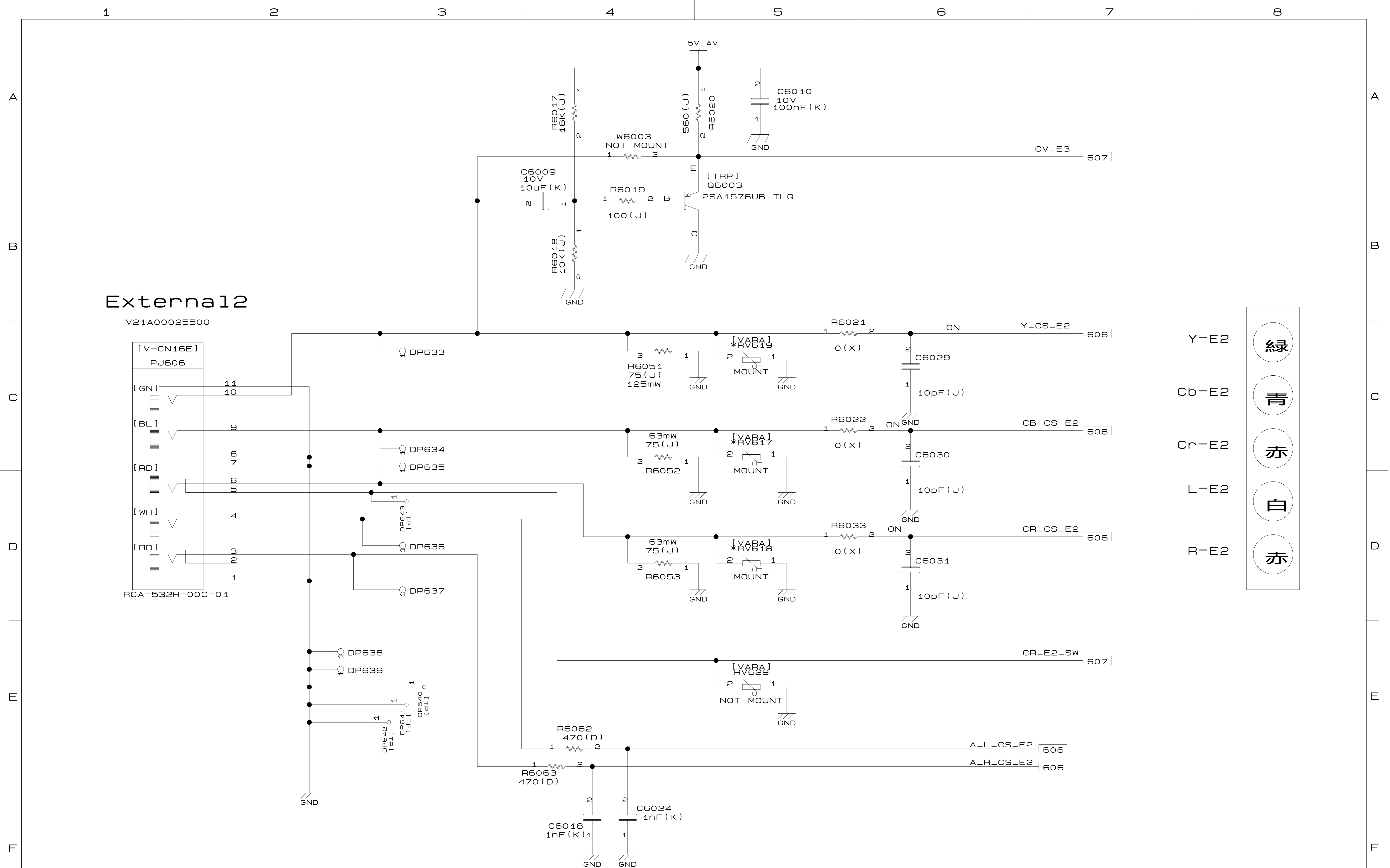
FUNCTION
DSUB PC IN

SH. NO.
602

PAGE NO.
40

REV. MARK
MP

DRAWING. NO.
PE1063ZA



- Y-E2 緑
- Cb-E2 青
- Cr-E2 赤
- L-E2 白
- R-E2 赤

DESIGNED BY
SHIBATA/Audio

TITLE
MAIN UNIT

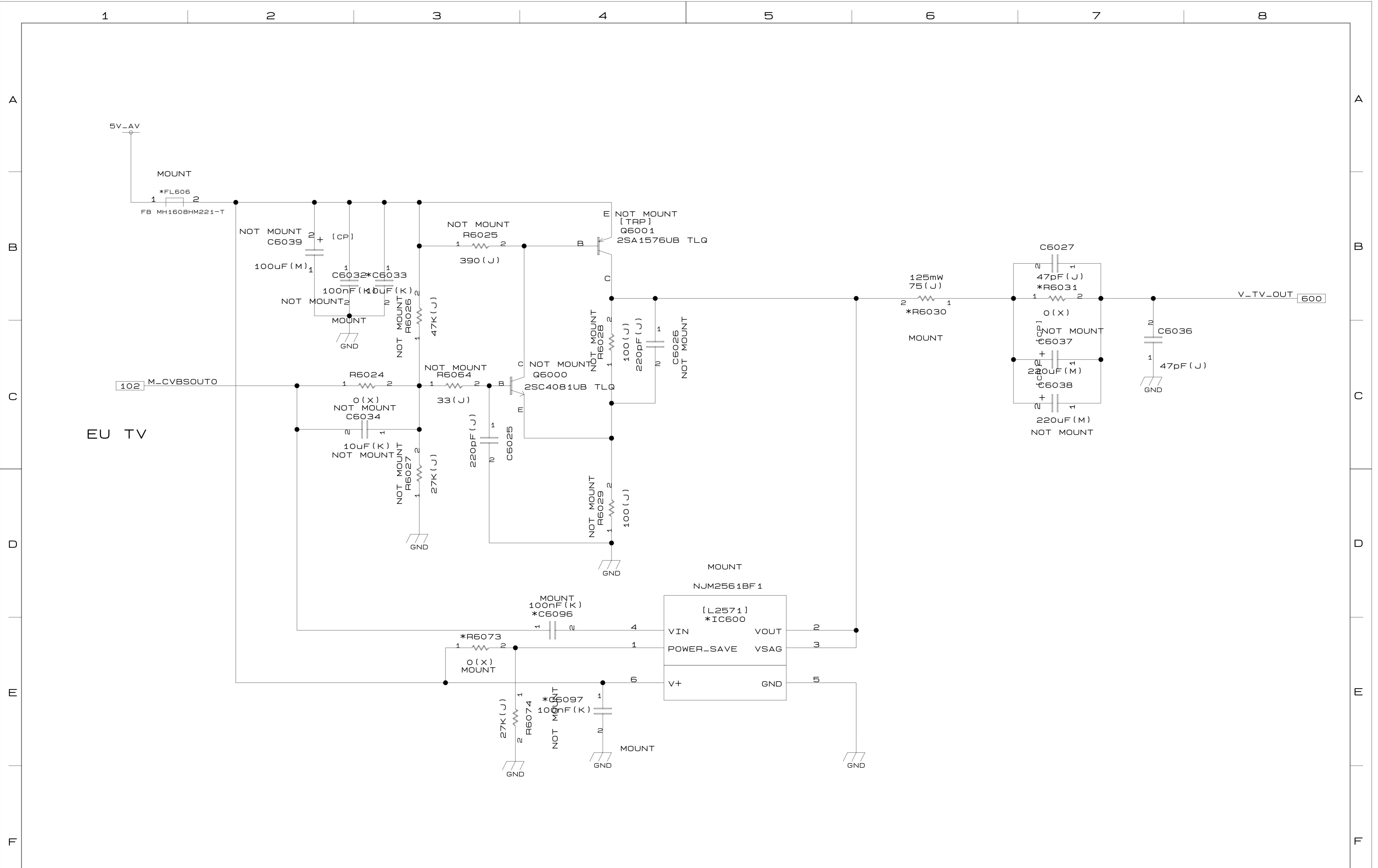
FUNCTION
EURO COMPONENT IN

SH. NO.
603

PAGE NO.
43

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
SHIBATA

TITLE
MAIN UNIT

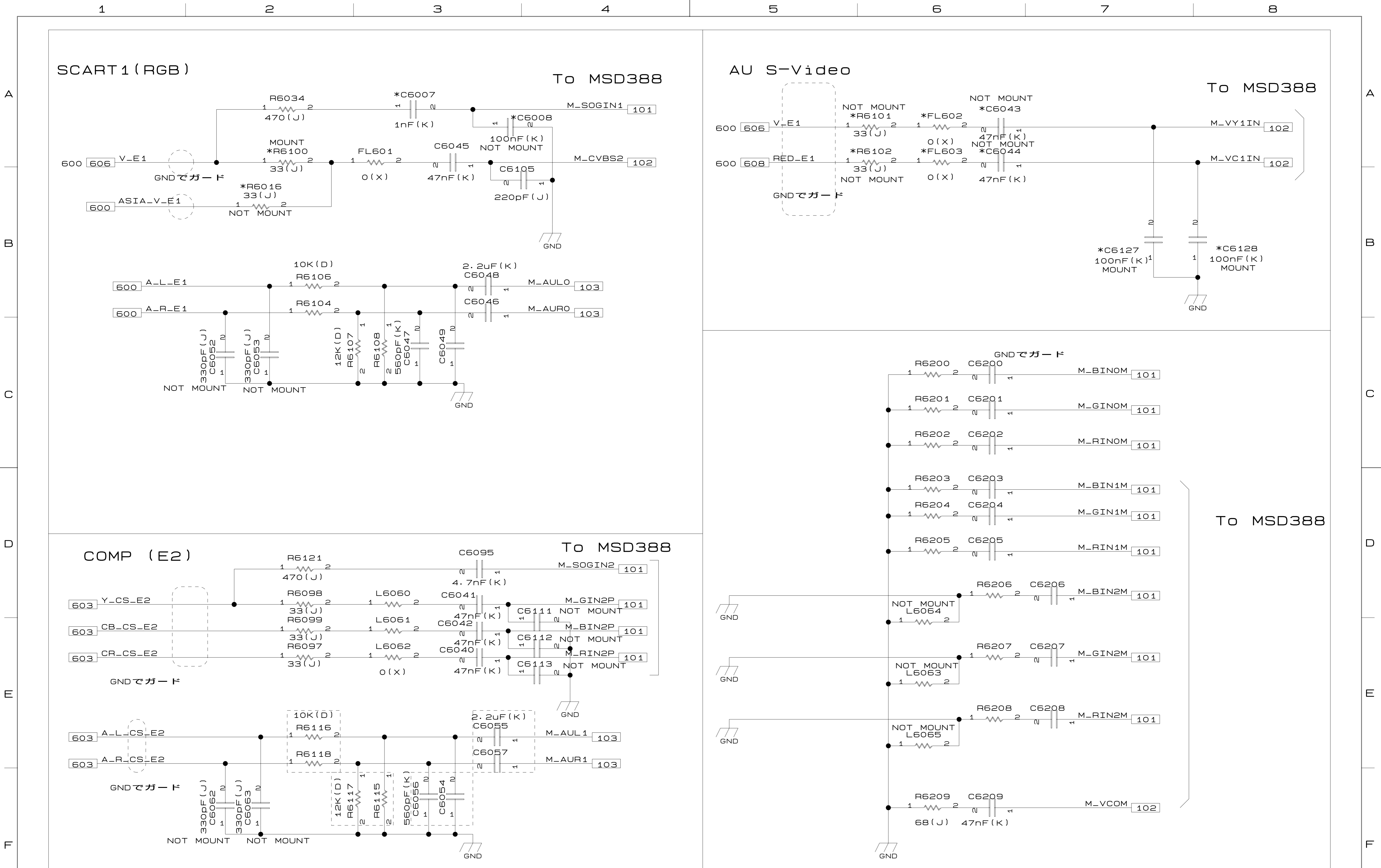
FUNCTION
EURO TV OUT

SH. NO.
605

PAGE NO.
44

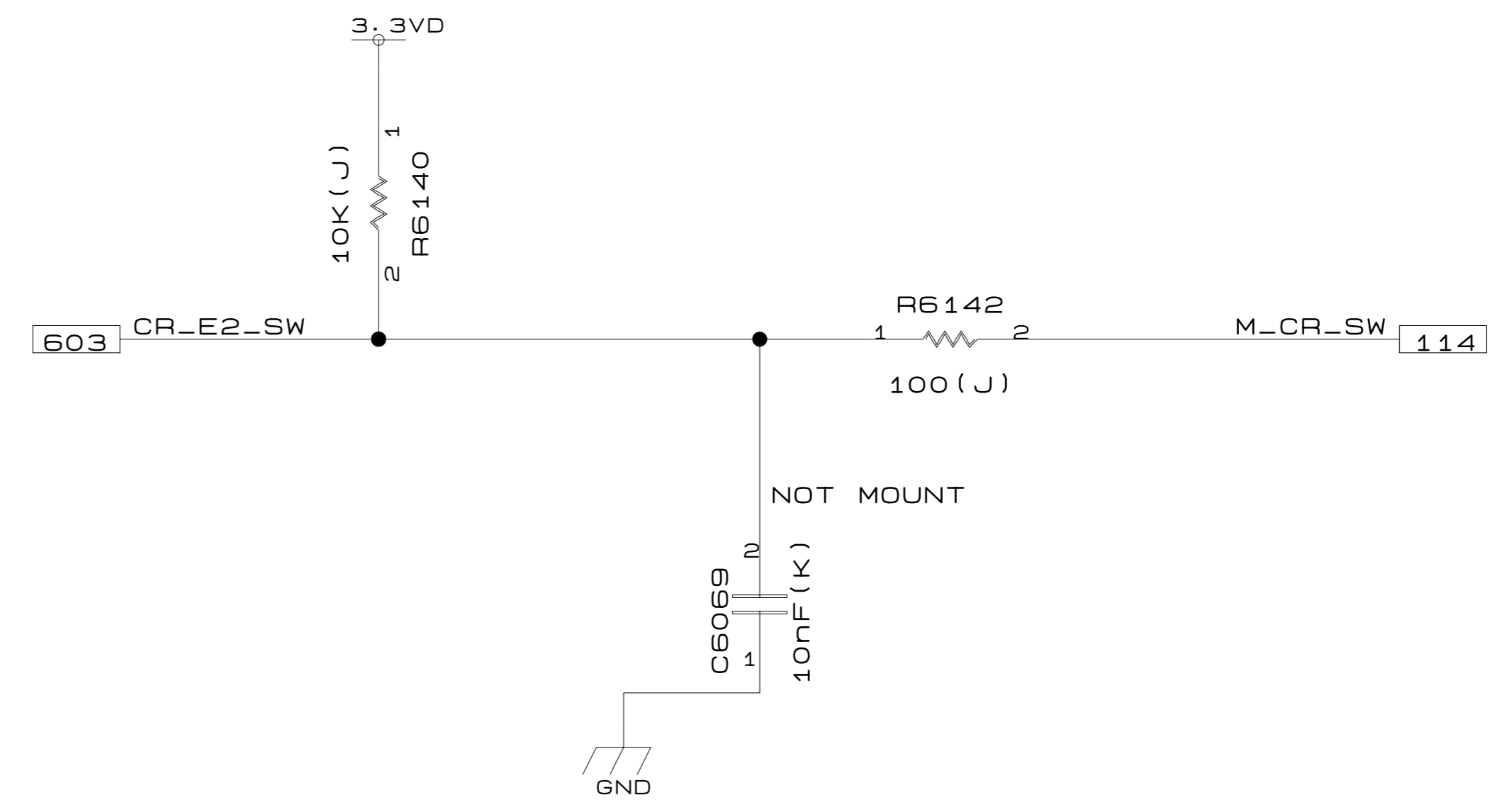
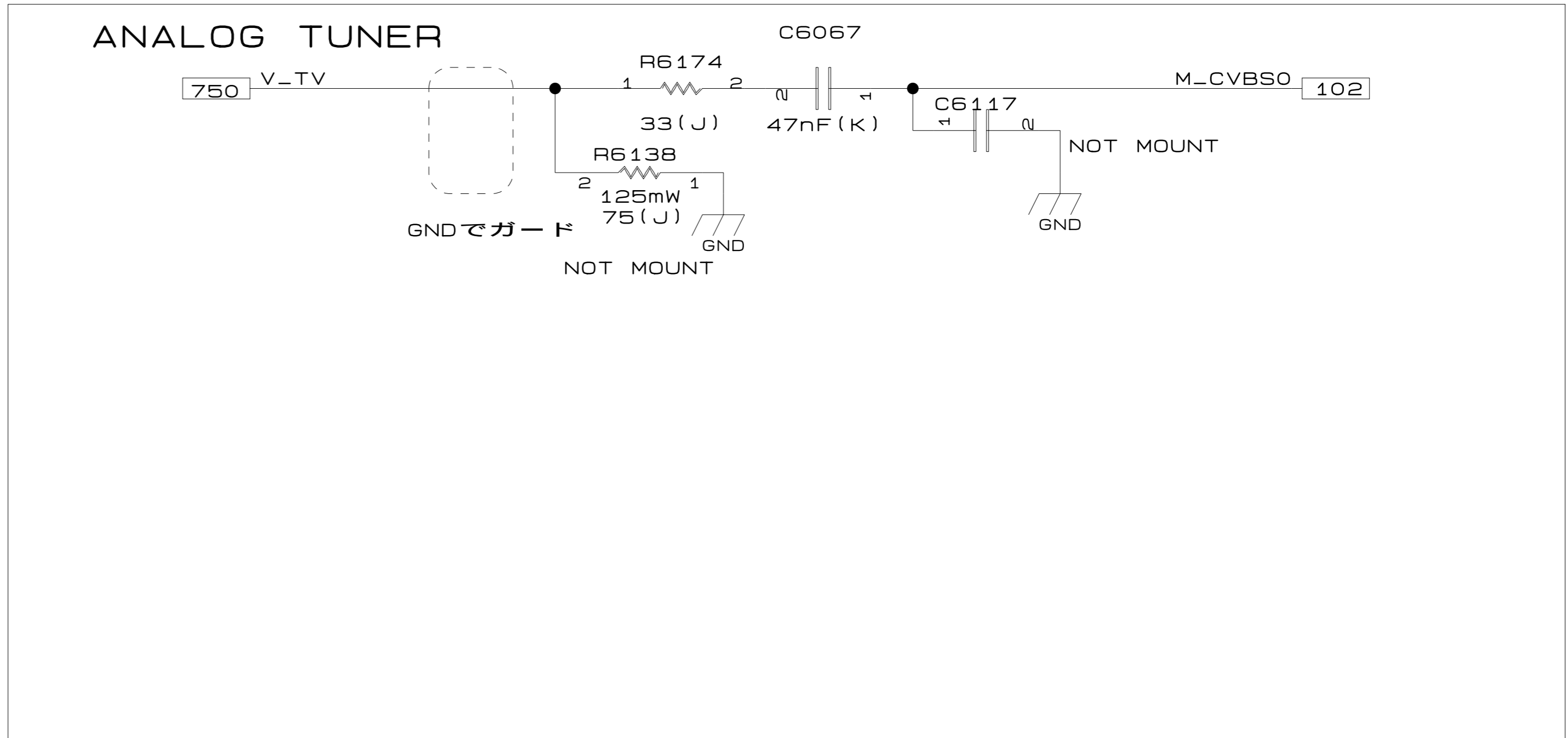
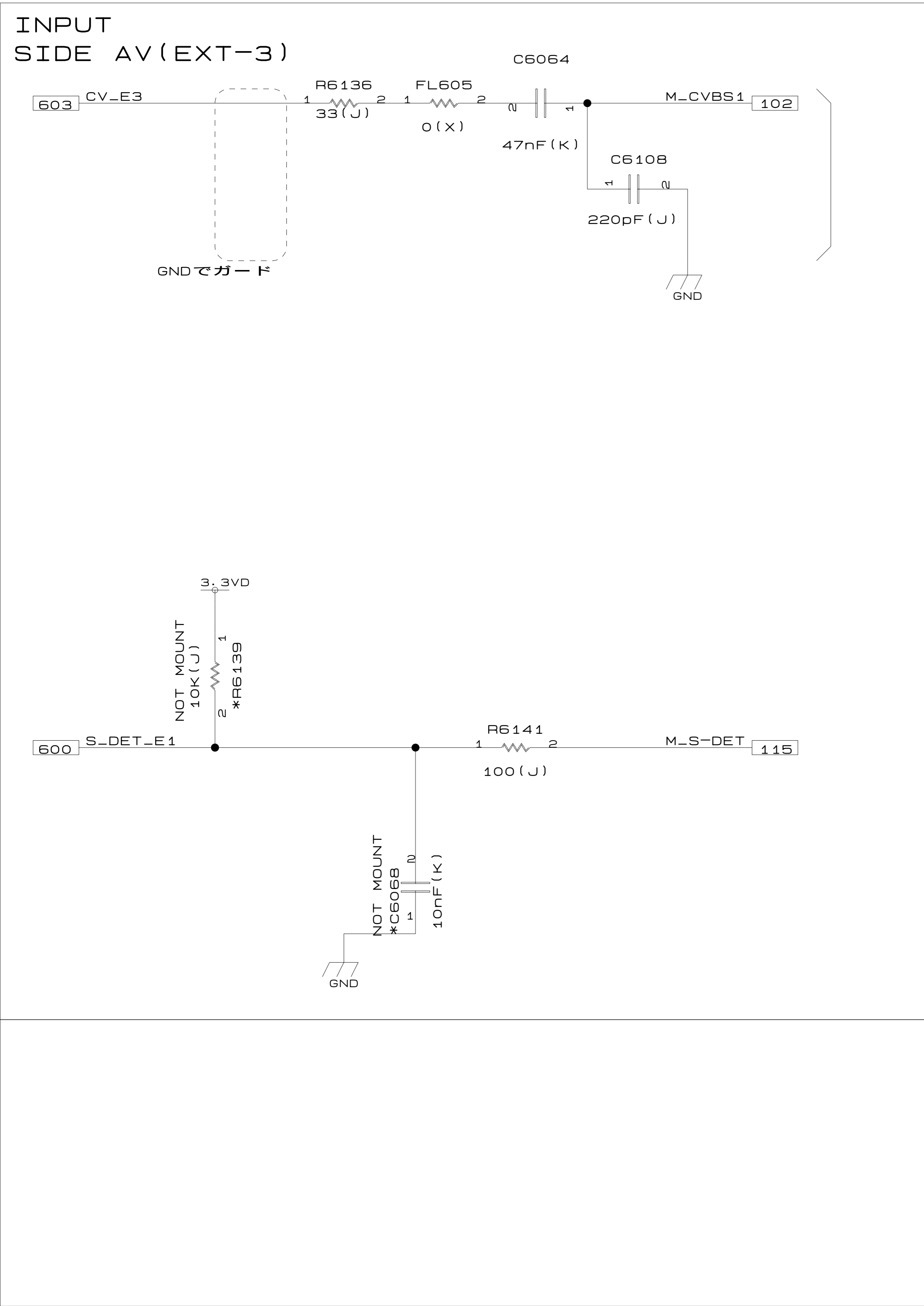
REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY SHIBATA/Audio	TITLE MAIN UNIT	FUNCTION AV INPUT 1	SH. NO. 606	PAGE NO. 45	REV. MARK MP	DRAWING. NO. PE1063ZA
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2012.01.19 13:53



DESIGNED BY
SHIBATA/Audio

TITLE
MAIN UNIT

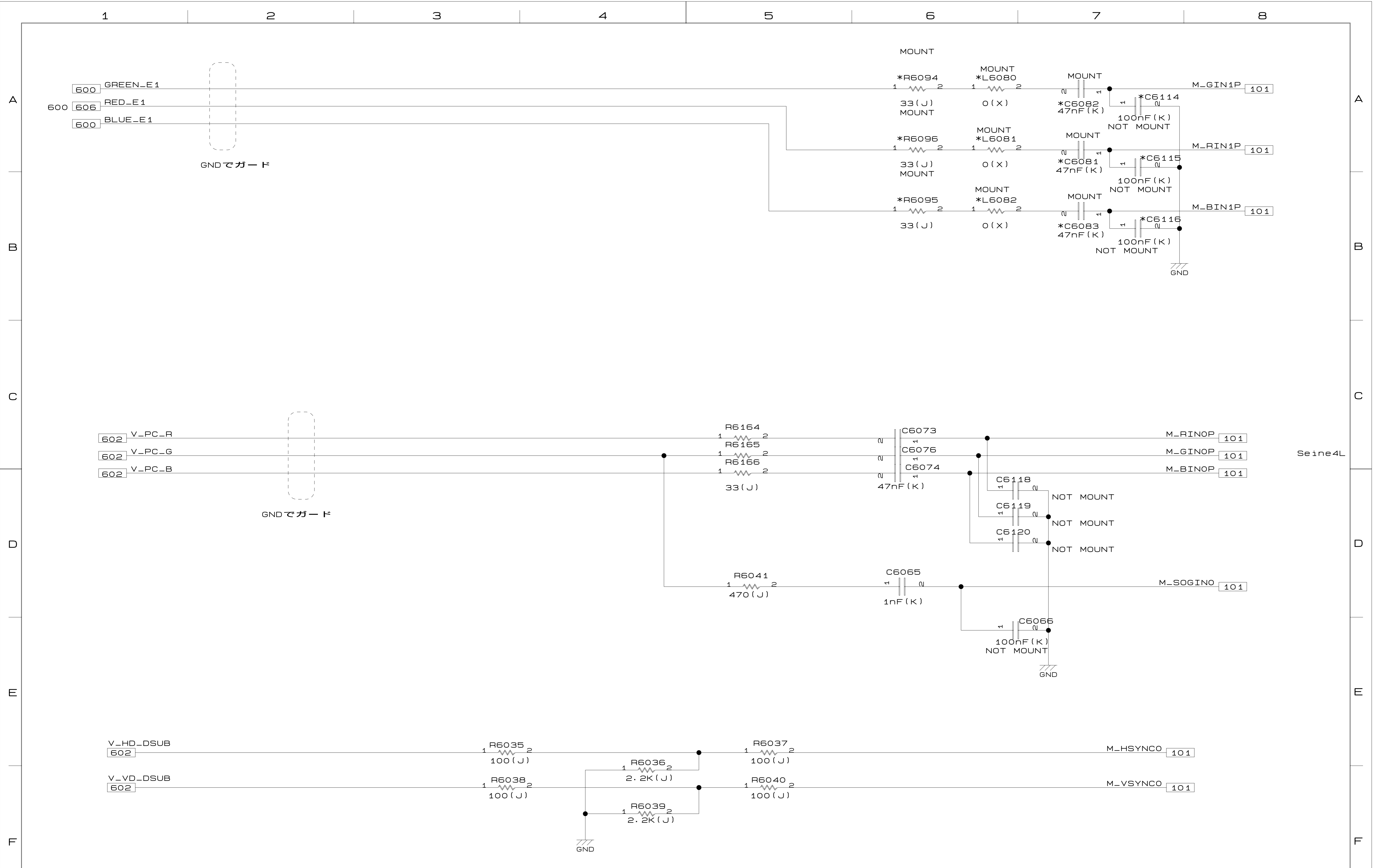
FUNCTION
AV INPUT2

SH. NO.
607

PAGE NO.
46

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
SHIBATA/Endo

TITLE
MAIN UNIT

FUNCTION
AV INPUT3

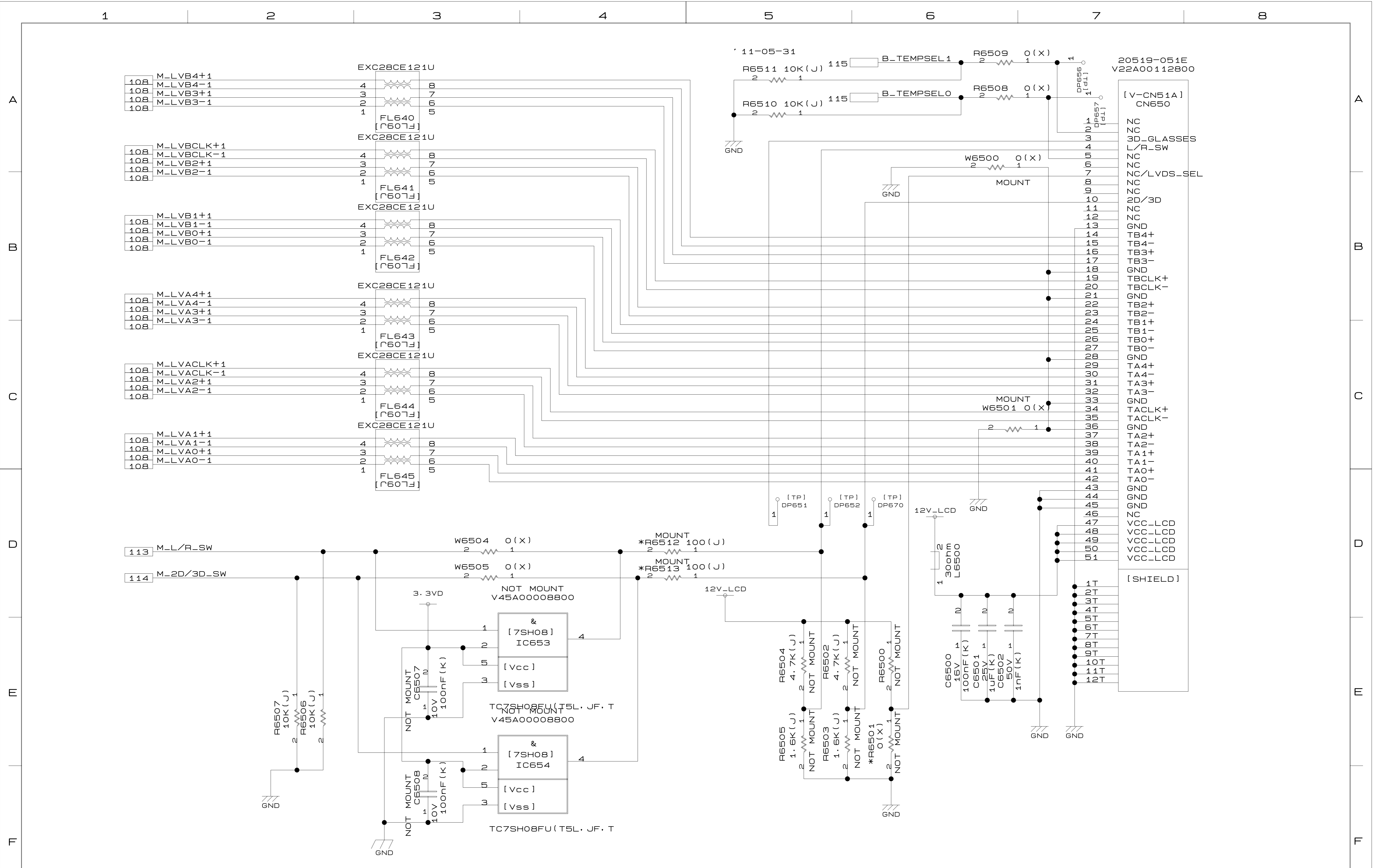
SH. NO.
608

PAGE NO.
47

REV. MARK
MP

DRAWING. NO.
PE1063ZA

2012.01.19 13:53



DESIGNED BY
S. KOMIYA

TITLE
MAIN UNIT

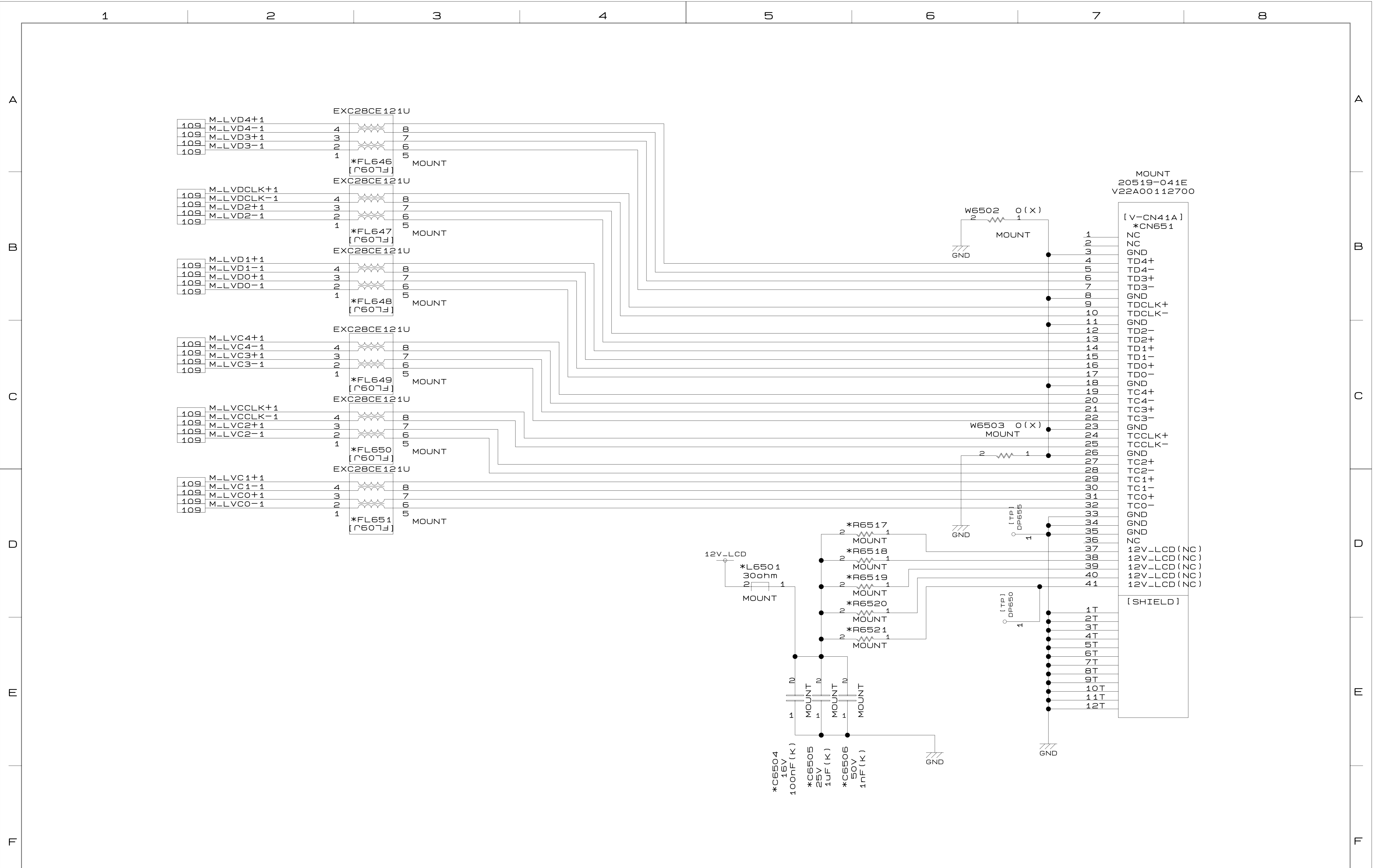
FUNCTION
LVDS CONNECTOR

SH. NO.
650

PAGE NO.
48

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
S. KOMIYA

TITLE
MAIN UNIT

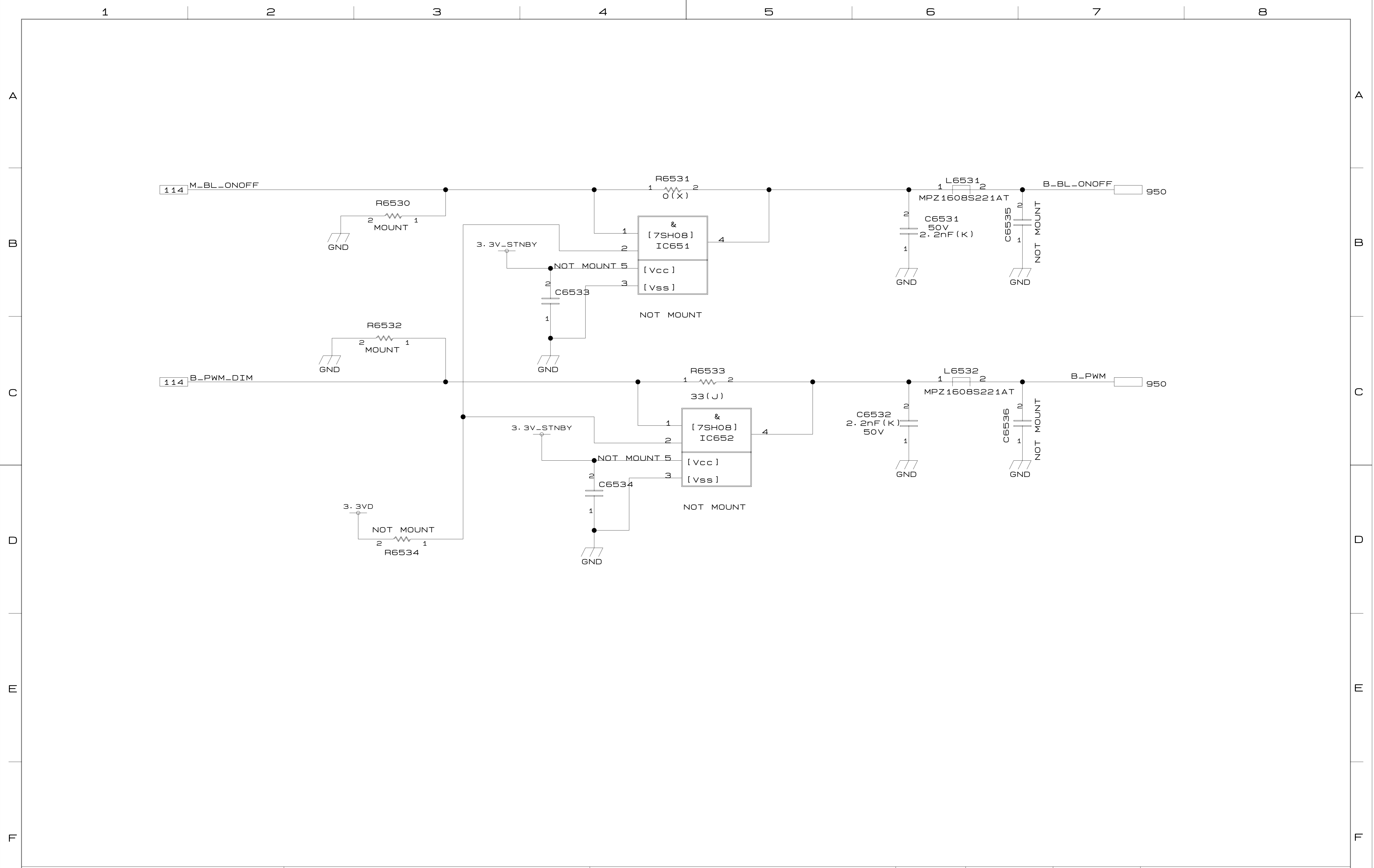
FUNCTION
LVDS CONNECTOR

SH. NO.
651

PAGE NO.
49

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
S. KOMIYA

TITLE
MAIN UNIT

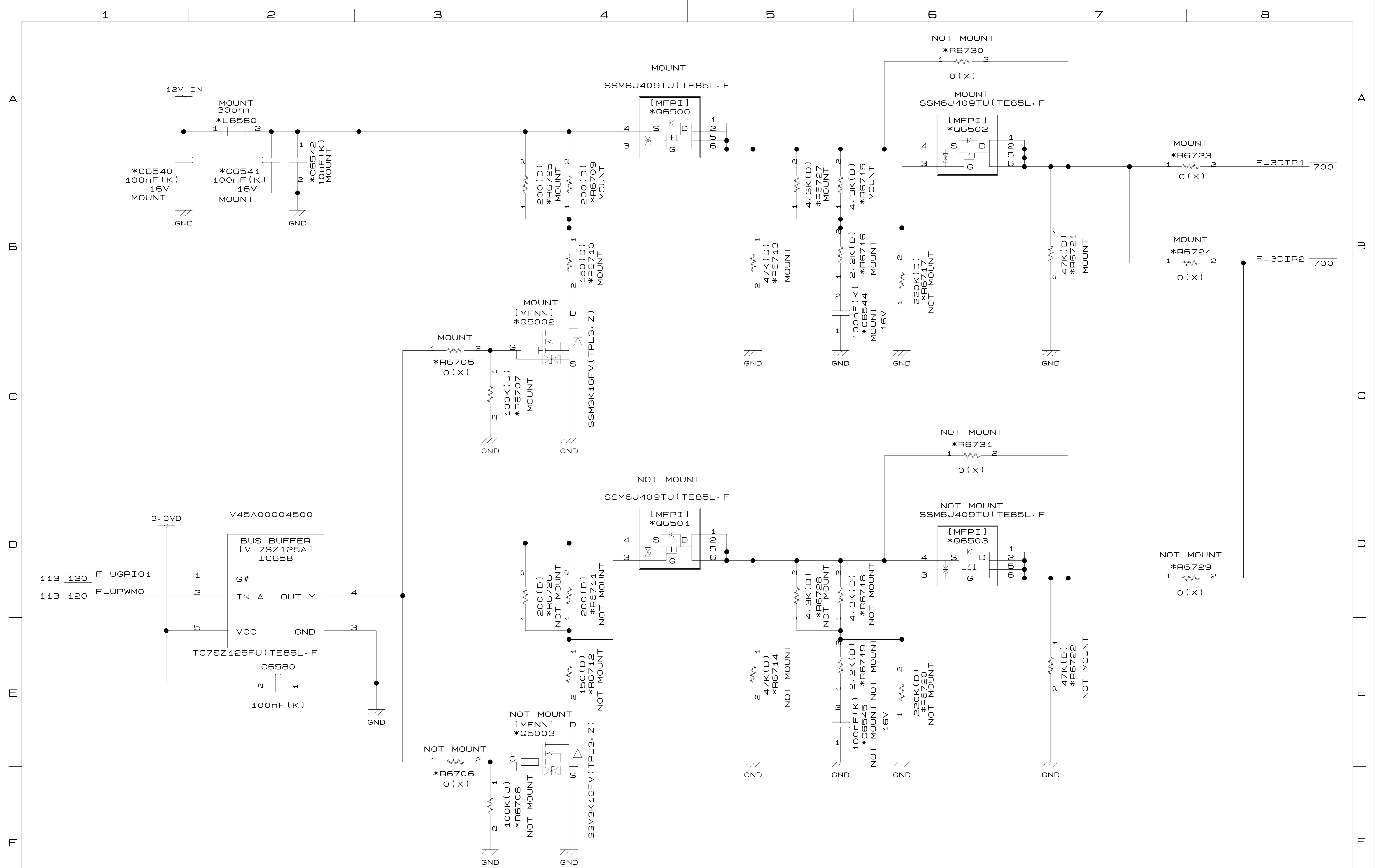
FUNCTION
BL_CONTROL & DIMMING

SH. NO.
652

PAGE NO.
50

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
M. SATO

TITLE
MAIN UNIT

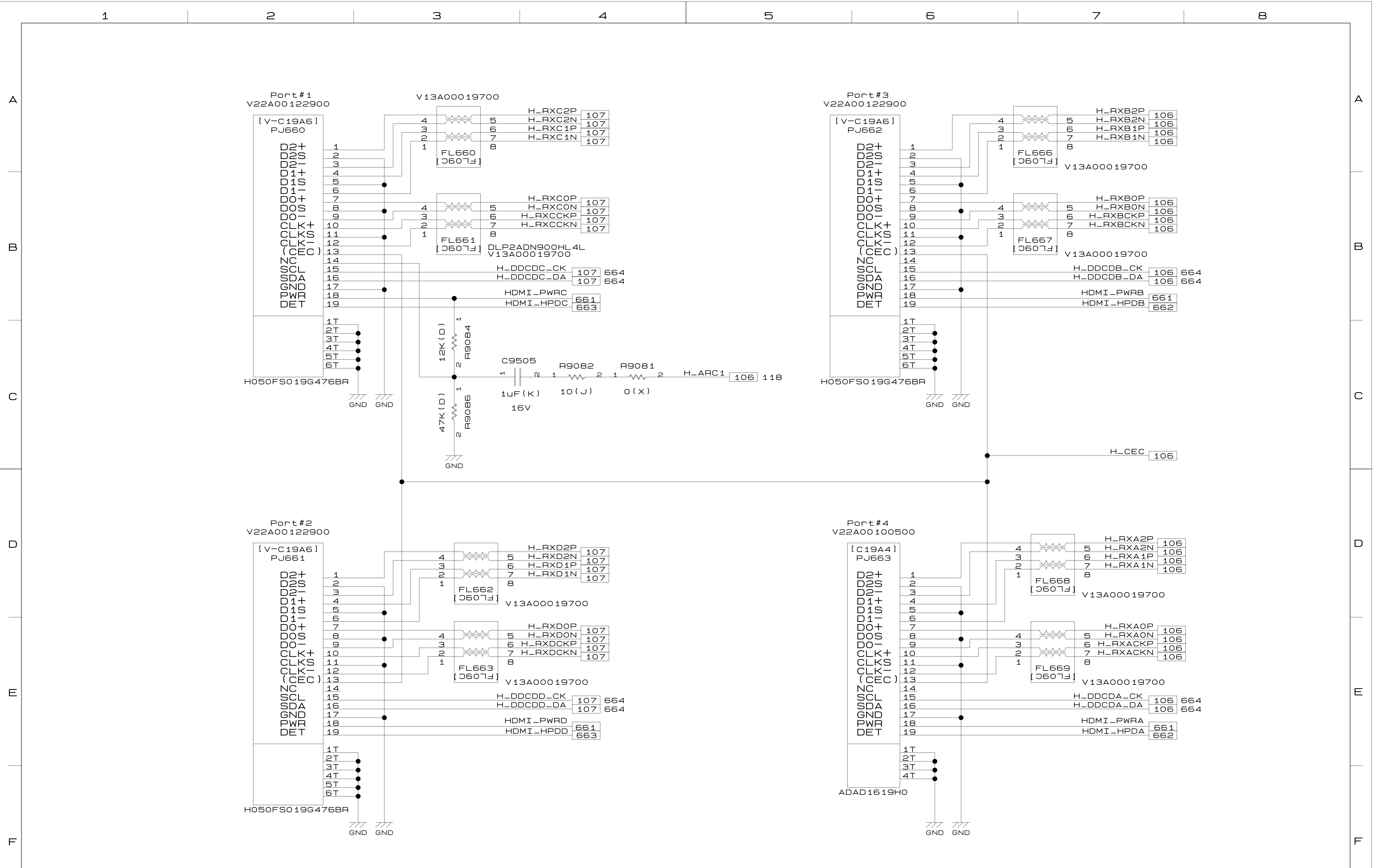
FUNCTION
@Func*i*

SH. NO.
658

PAGE NO.
51

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
Noguchi/Kuwata	MAIN UNIT	HDMI CONNECTOR	660	52	MP	PE1063ZA

2012.01.19 13:53

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A

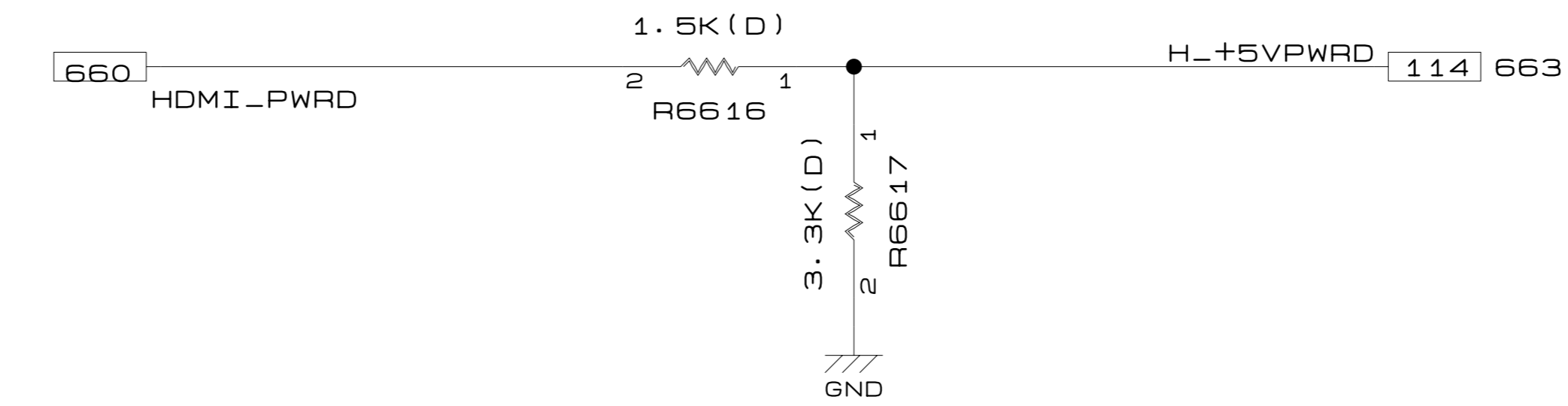
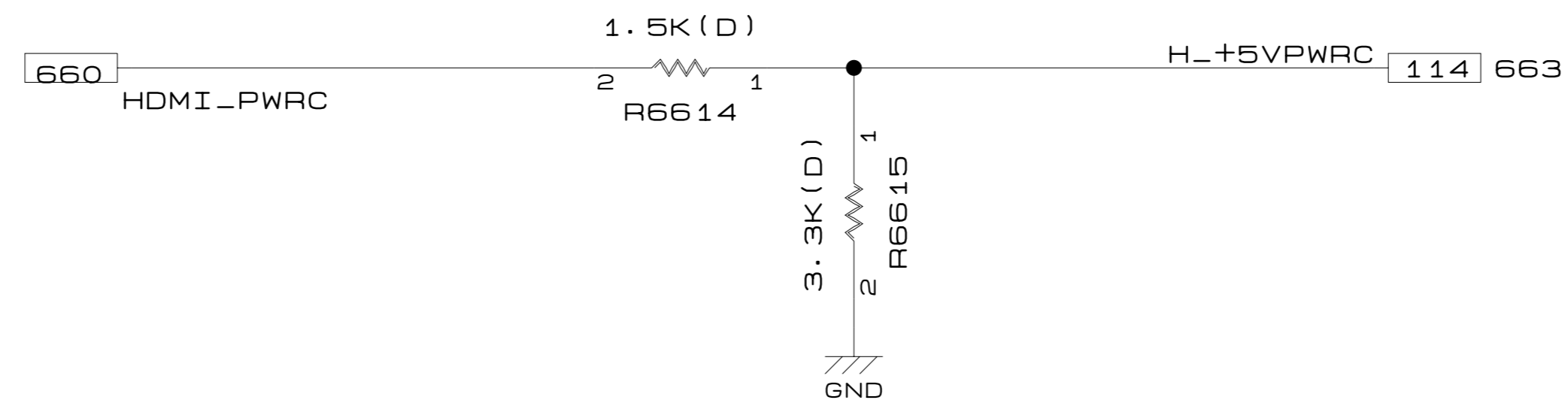
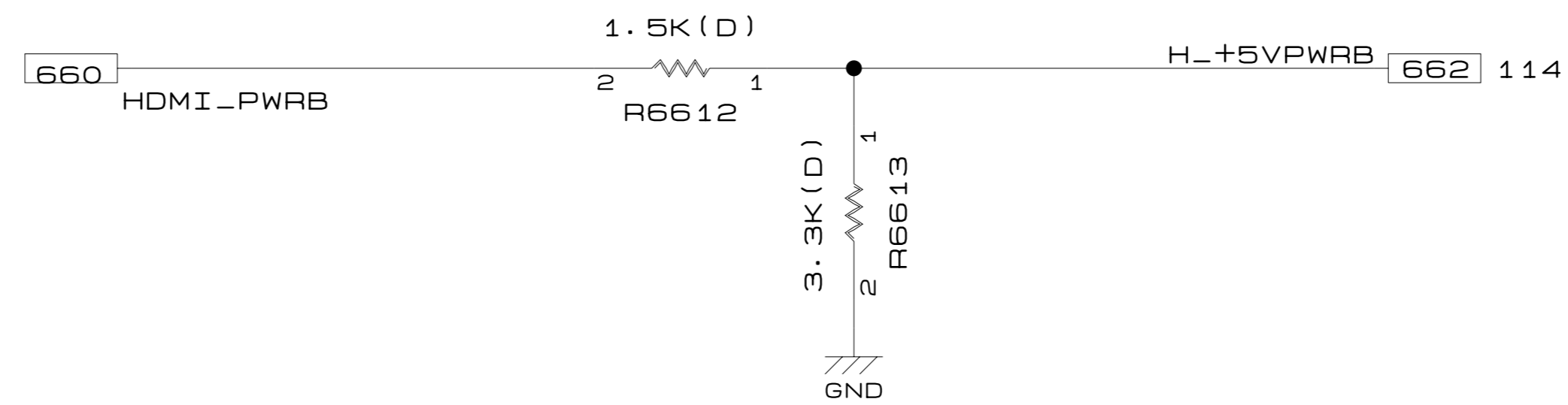
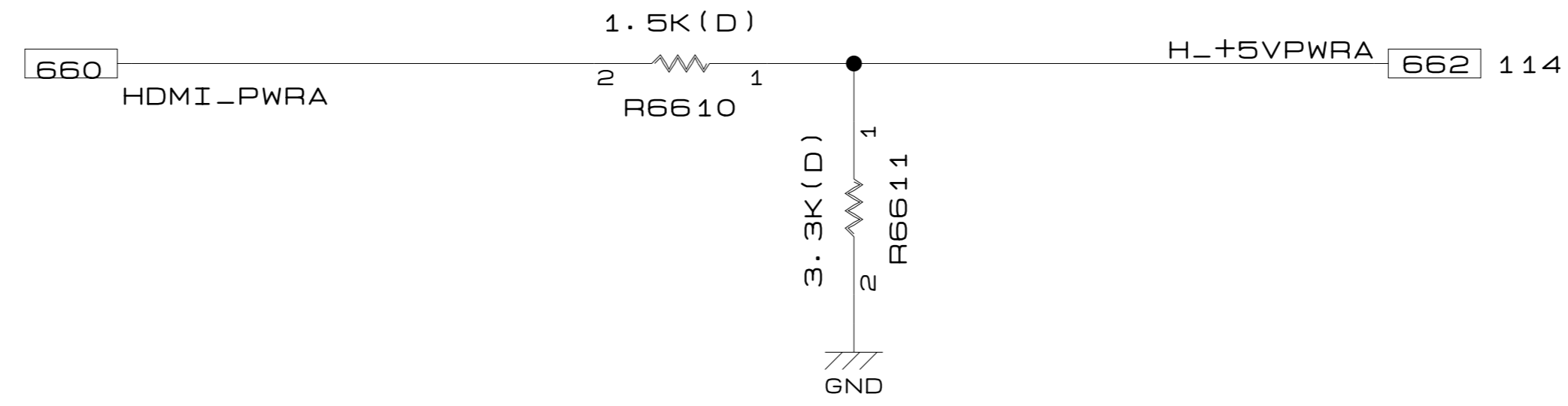
B

C

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F



DESIGNED BY
Kuwata

TITLE
MAIN UNIT

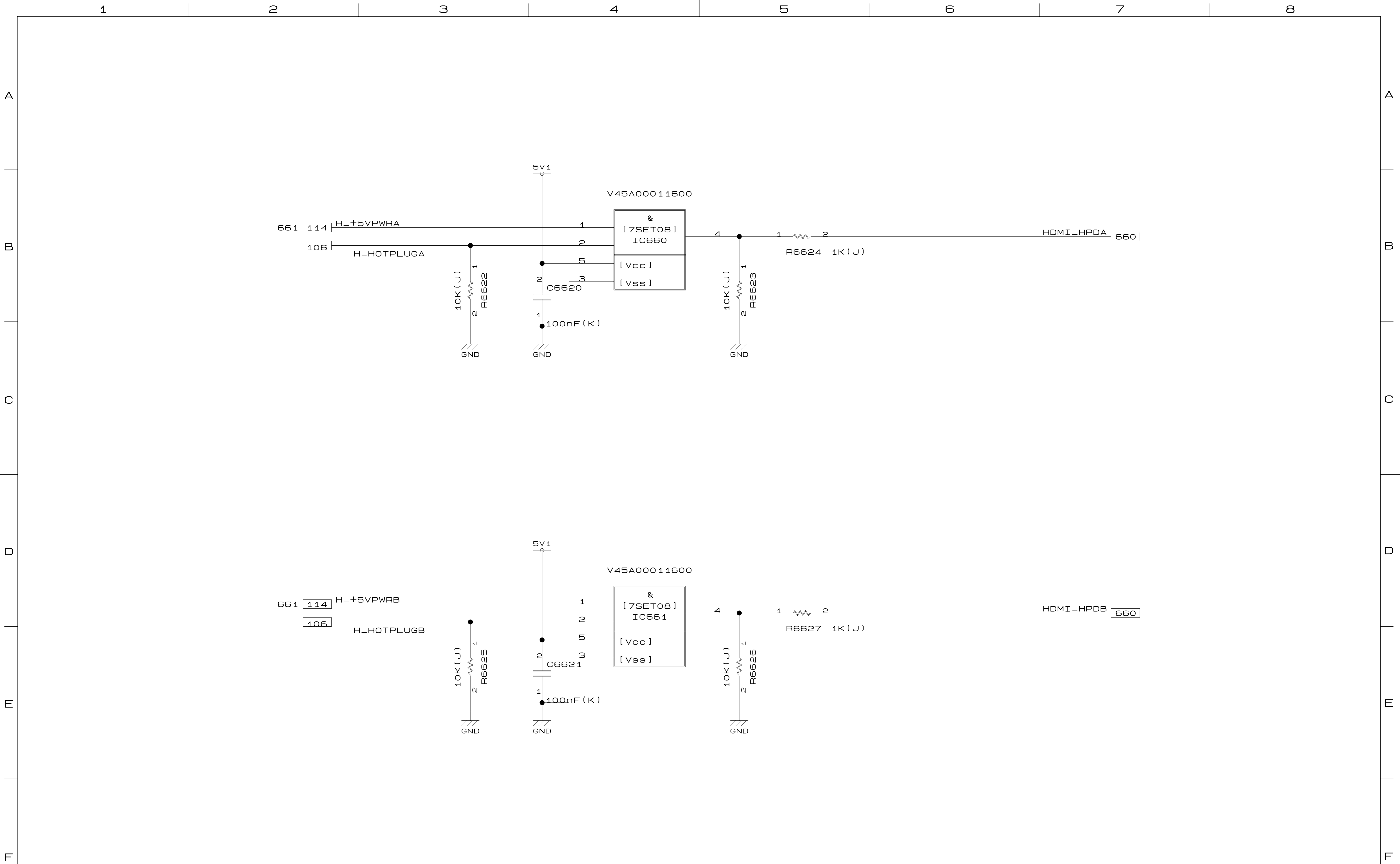
FUNCTION
HDMI 5VPWR

SH. NO.
661

PAGE NO.
53

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY

Kuwata

TITLE

MAIN UNIT

FUNCTION

HDMI HPD (1)

SH. NO.

662

PAGE NO.

54

REV. MARK

MP

DRAWING. NO.

PE1063ZA

1 2 3 4 5 6 7 8

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B

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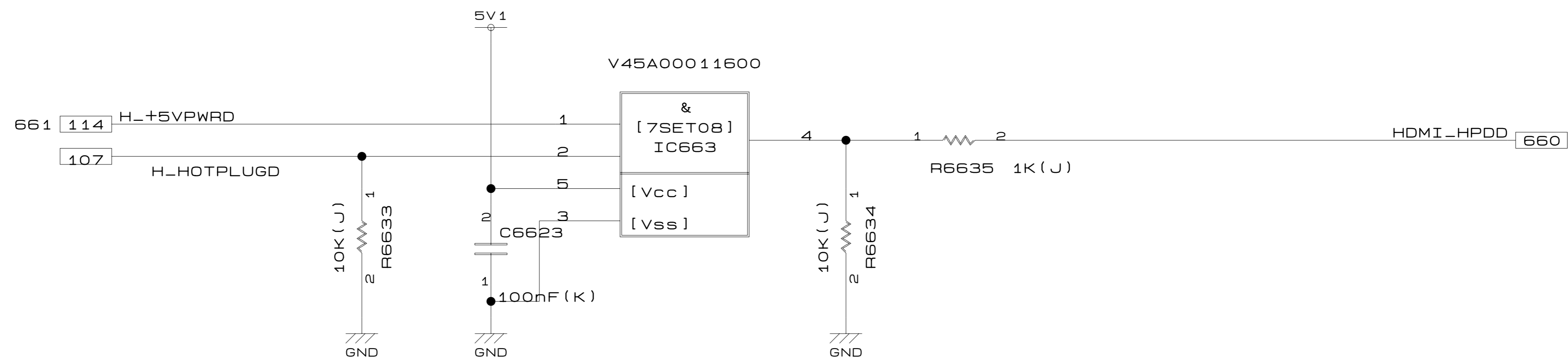
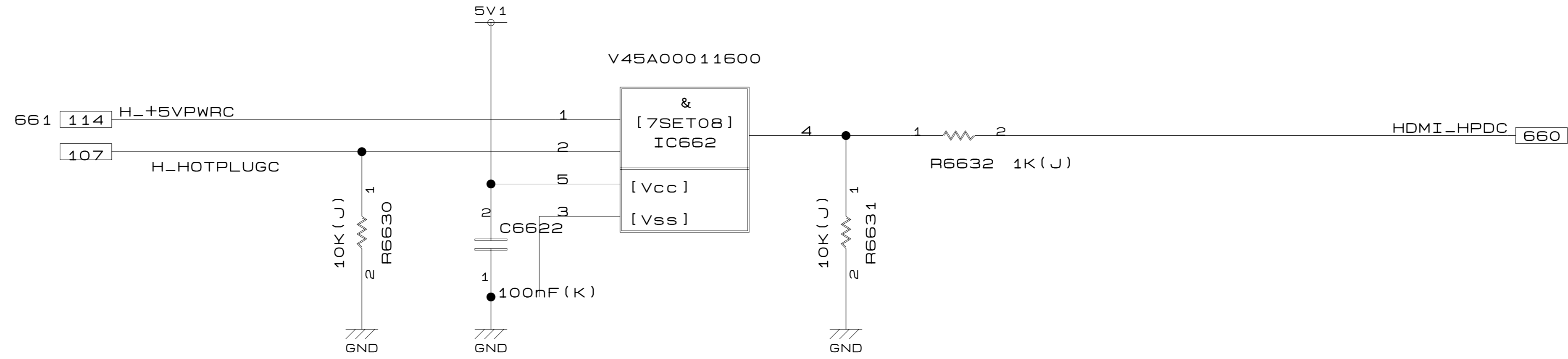
B

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DESIGNED BY
Kuwata

TITLE
MAIN UNIT

FUNCTION
HDMI HPD(2)

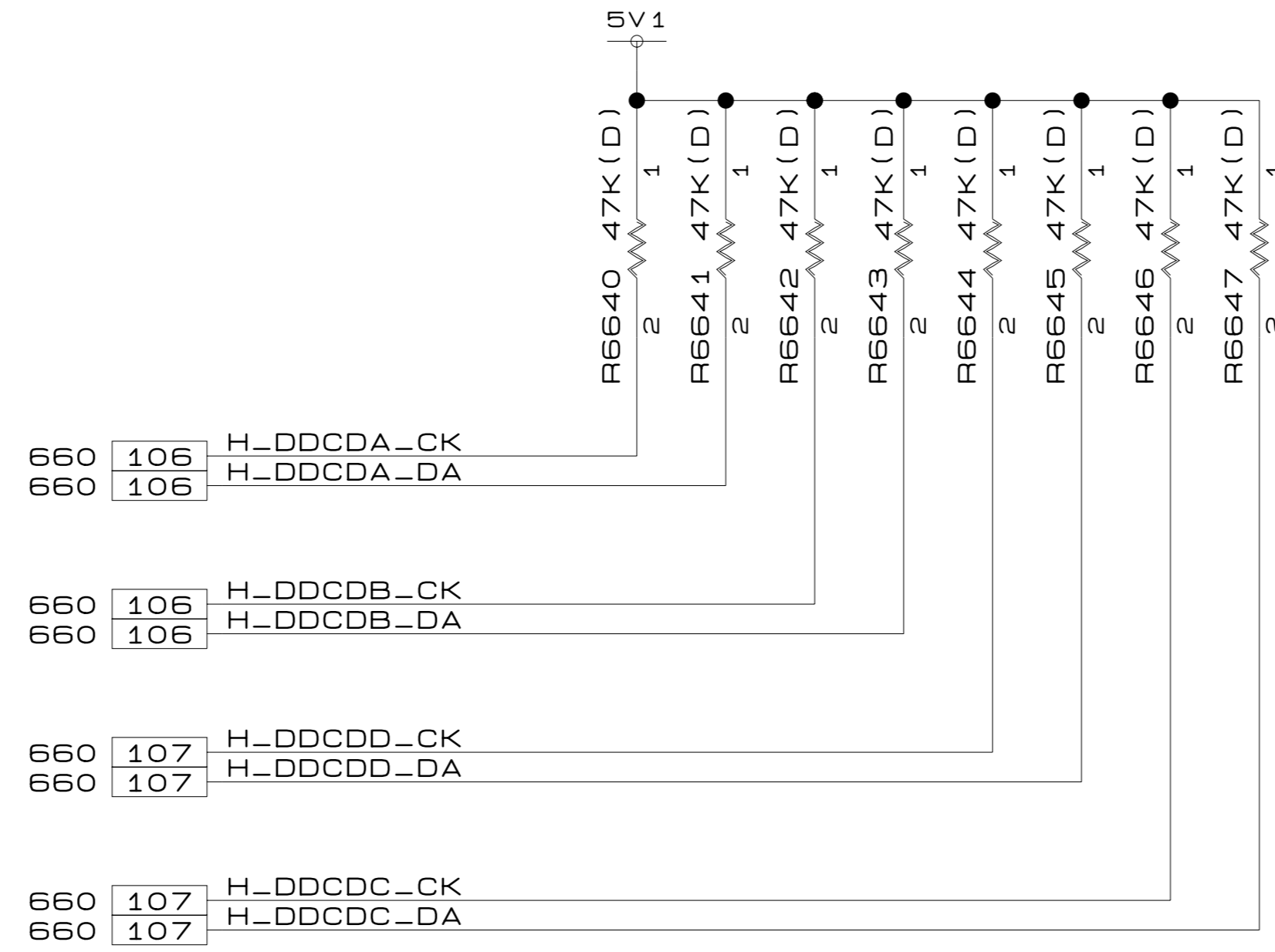
SH. NO.
663

PAGE NO.
55

REV. MARK
MP

DRAWING. NO.
PE1063ZA

2012.01.19 13:53



DESIGNED BY
Kuwata

TITLE
MAIN UNIT

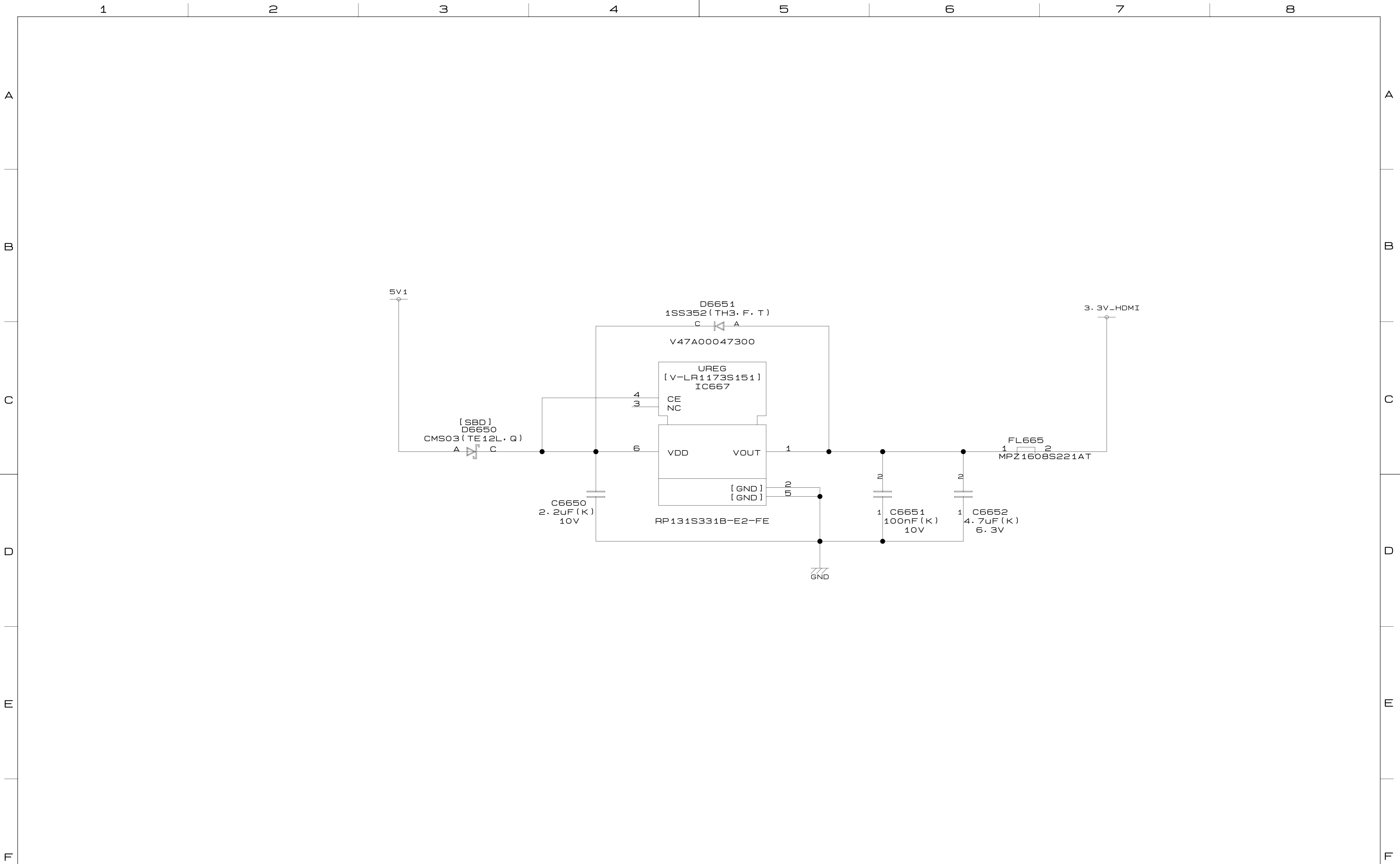
FUNCTION
HDMI DDC

SH. NO.
664

PAGE NO.
56

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
Kuwata

TITLE
MAIN UNIT

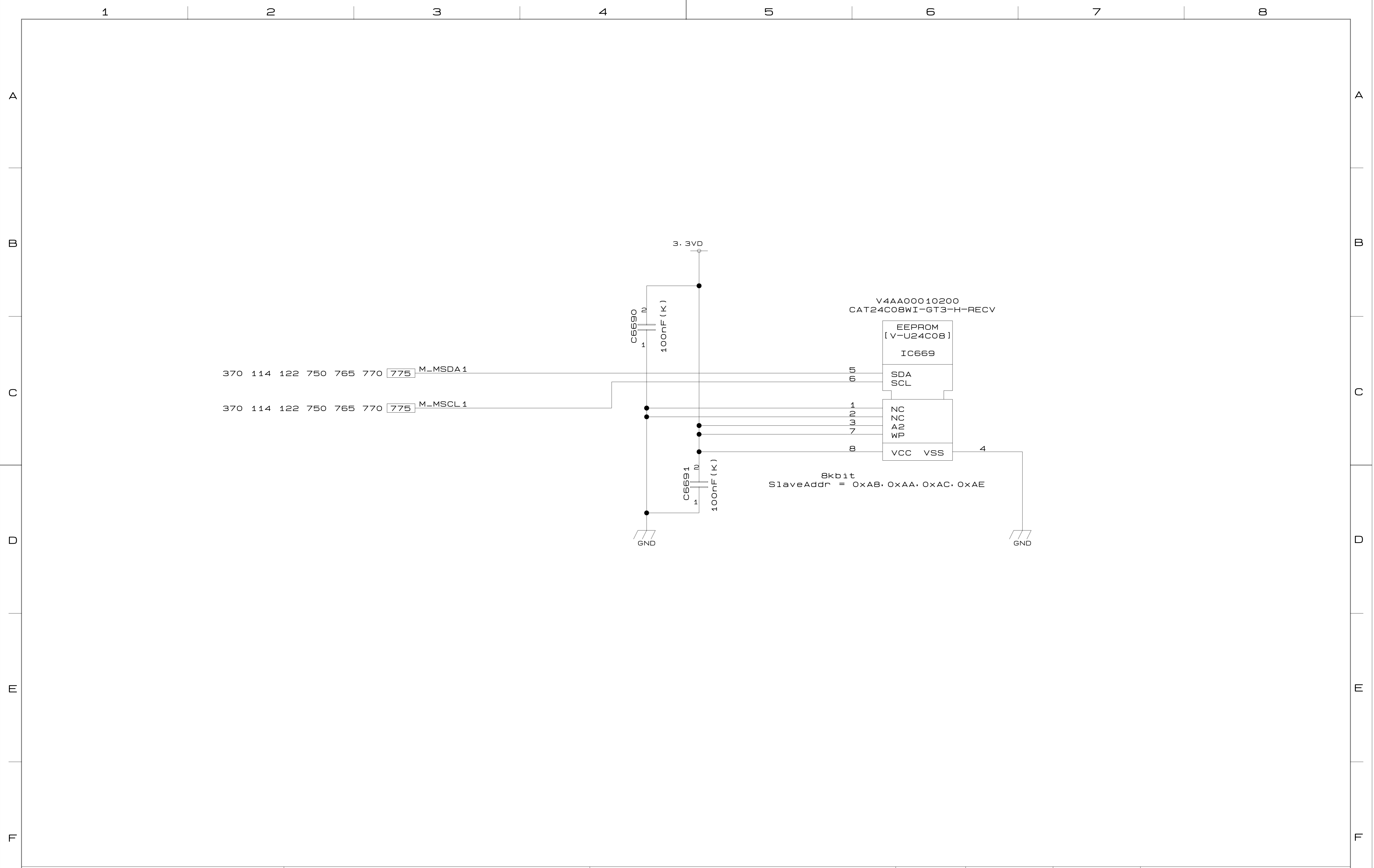
FUNCTION
3.3V_HDMI

SH. NO.
665

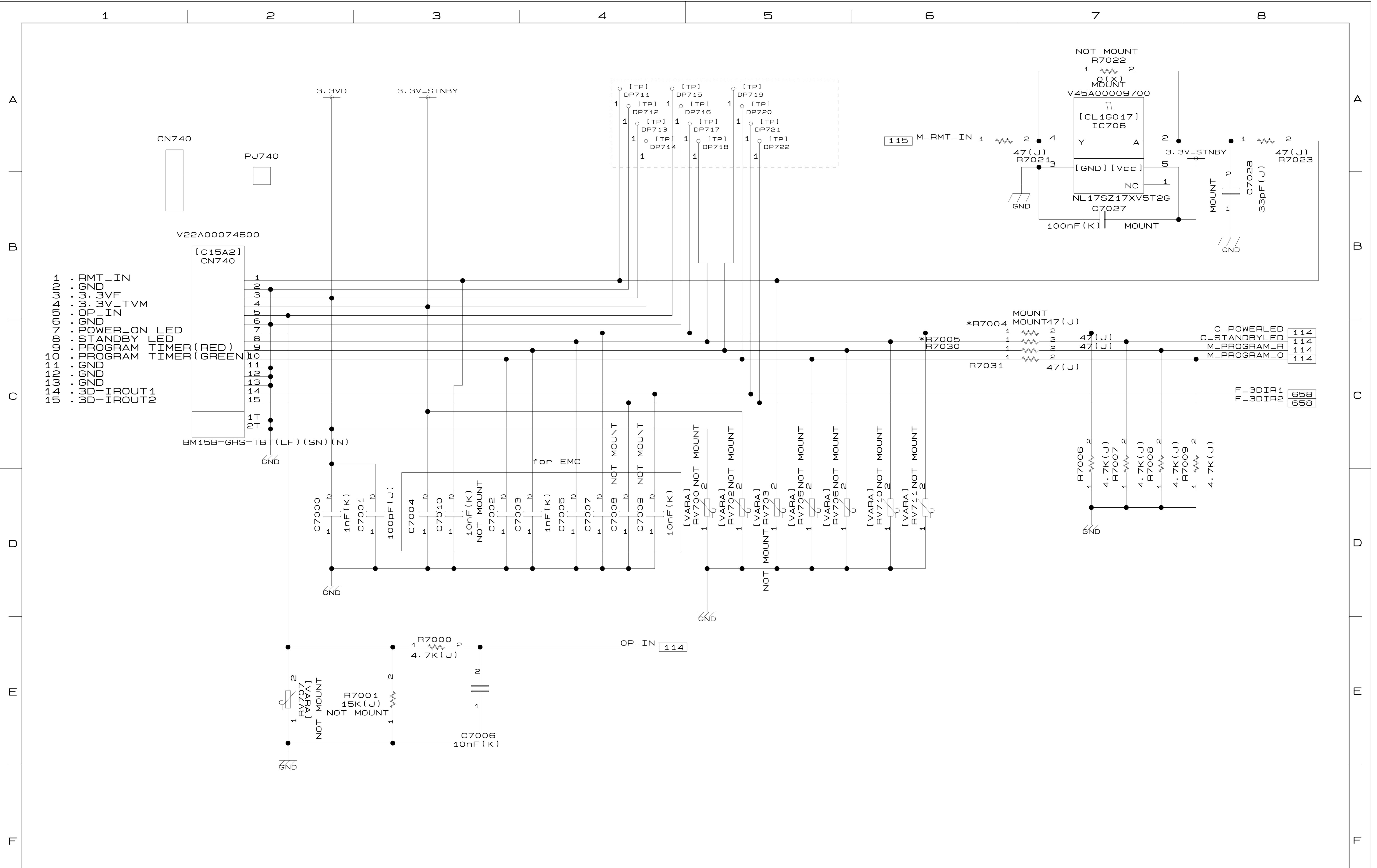
PAGE NO.
57

REV. MARK
MP

DRAWING. NO.



DESIGNED BY Noguchi/Kuwata	TITLE MAIN UNIT	FUNCTION HDCP KEY ROM	SH. NO. 669	PAGE NO. 58	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY
IMURA

TITLE
MAIN UNIT

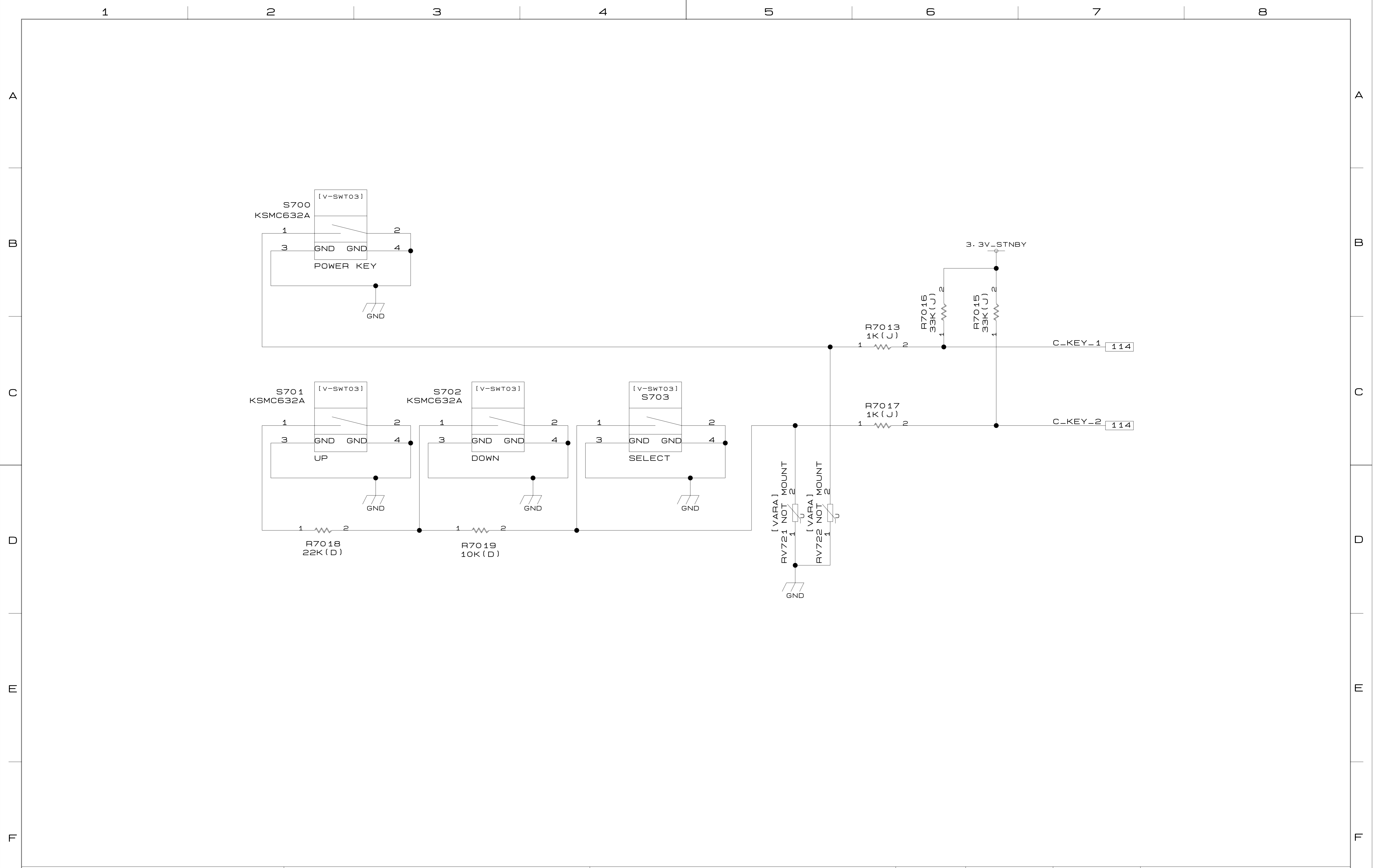
FUNCTION
LED/IR IF

SH. NO.
700

PAGE NO.
59

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
IMURA

TITLE
MAIN UNIT

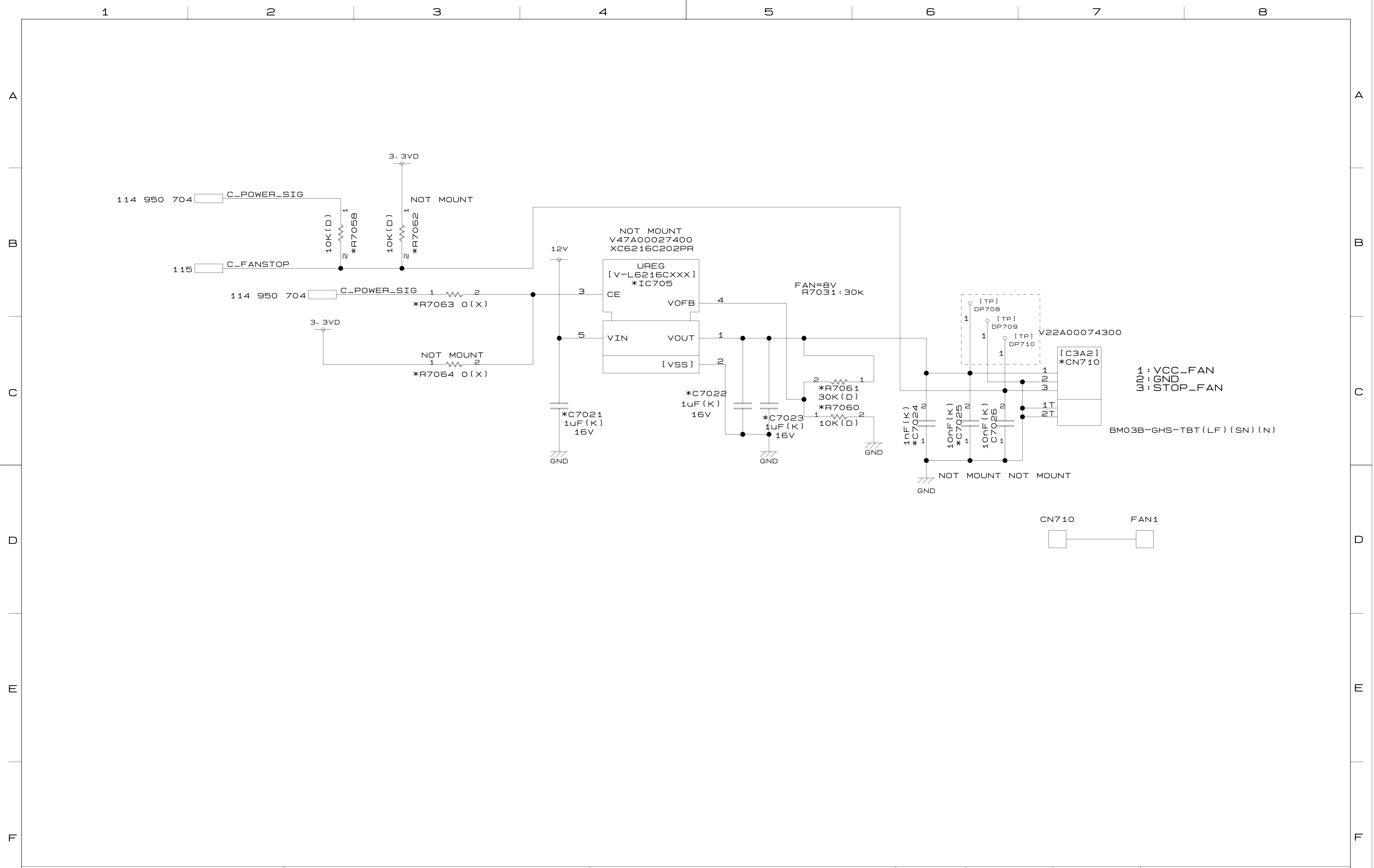
FUNCTION
5V LED/KEY

SH. NO.
701

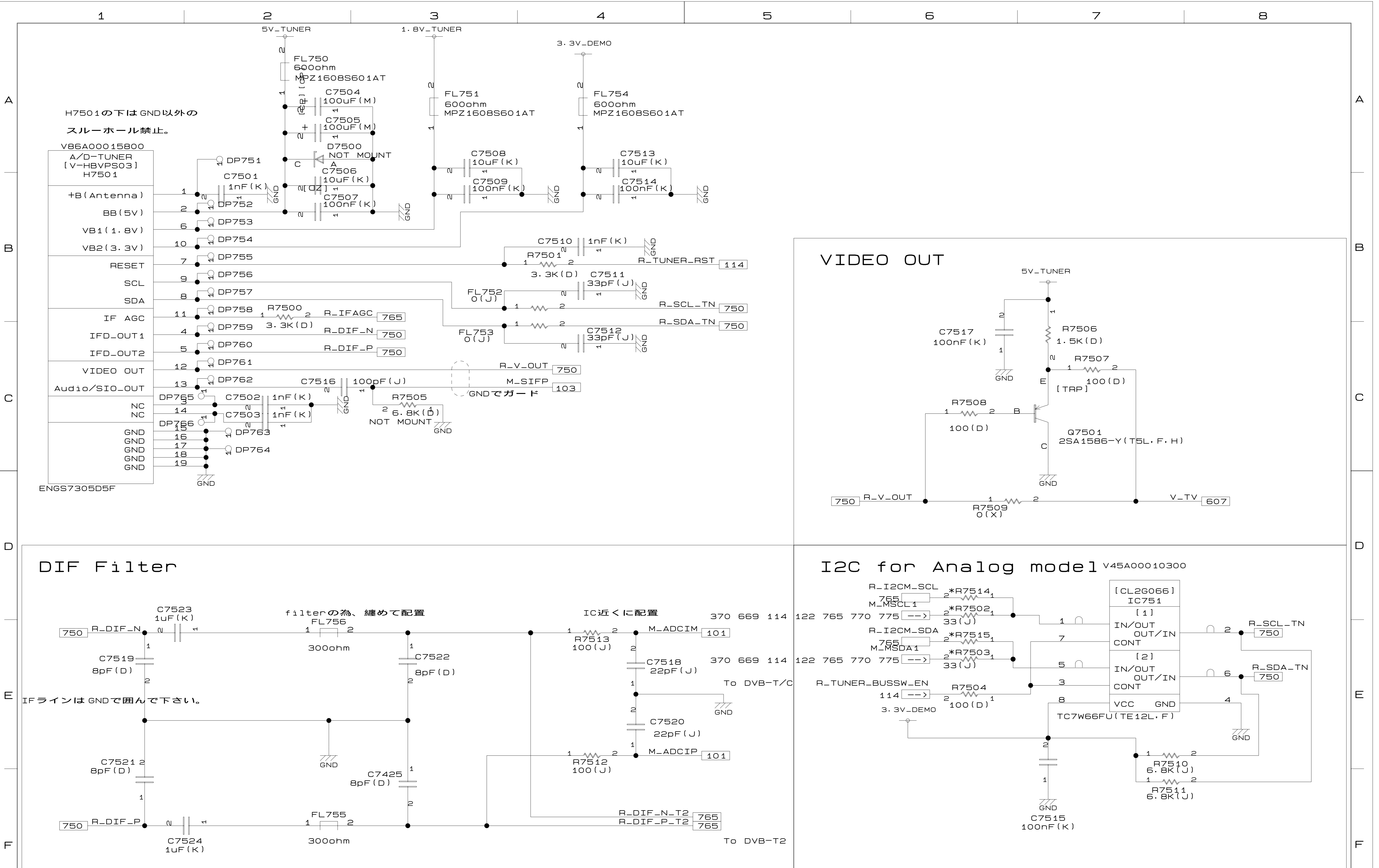
PAGE NO.
60

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY IMURA	TITLE MAIN UNIT	FUNCTION TVM FAN CONTROL	SH. NO. 704	PAGE NO. 61	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY
R. IZUMI

TITLE
MAIN UNIT

FUNCTION
TUNER

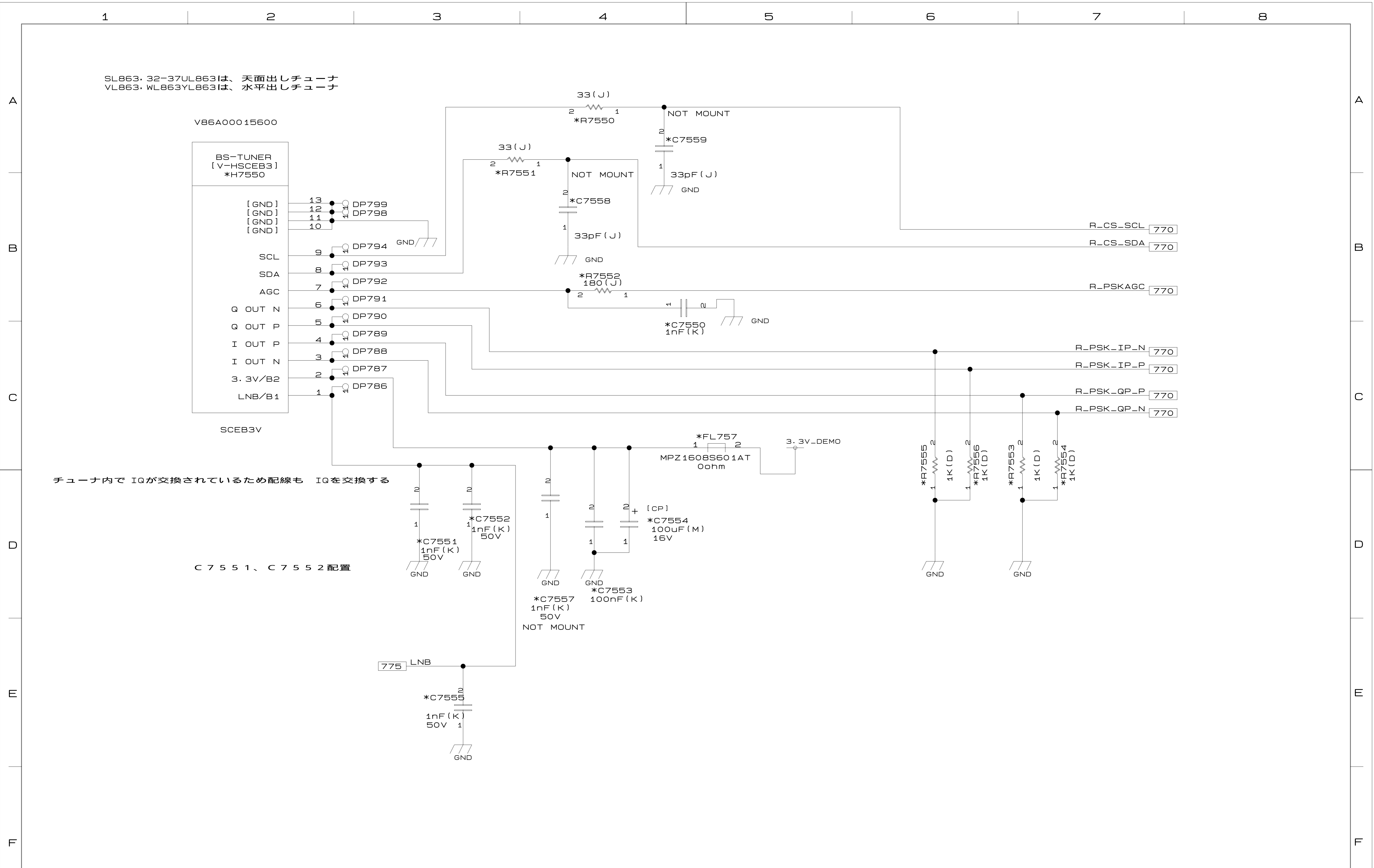
SH. NO.
750

PAGE NO.
62

REV. MARK
MP

DRAWING NO.
PE1063ZA

SL863・32-37UL863は、天面出しチューナ
VL863・WL863YL863は、水平出しチューナ



チューナ内で IQが交換されているため配線も IQを交換する

C 7 5 5 1、C 7 5 5 2 配置

DESIGNED BY
R. IZUMI

TITLE
MAIN UNIT

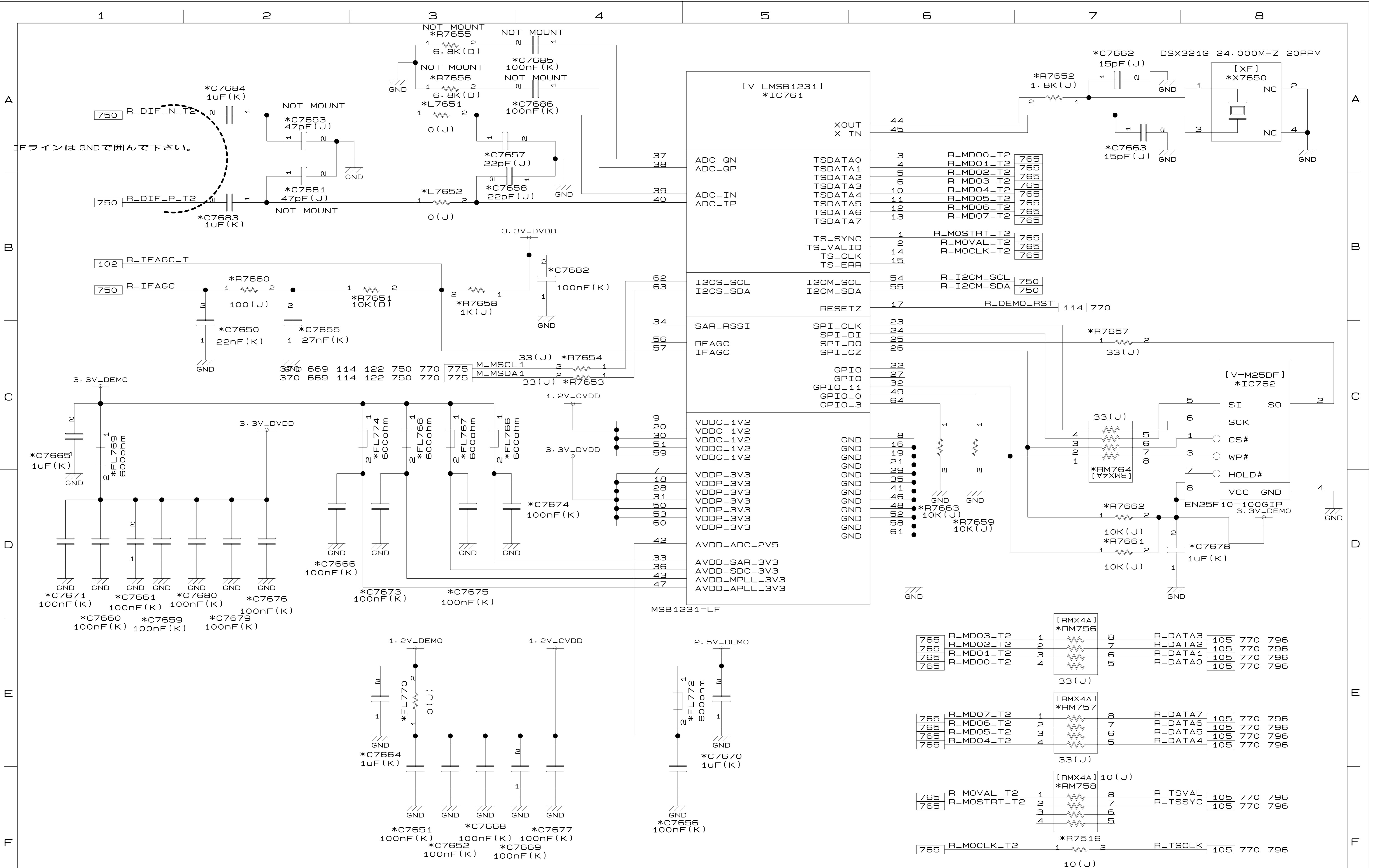
FUNCTION
TUNER DVB-S/S2

SH. NO.
755

PAGE NO.
63

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
R. IZUMI

TITLE
MAIN UNIT

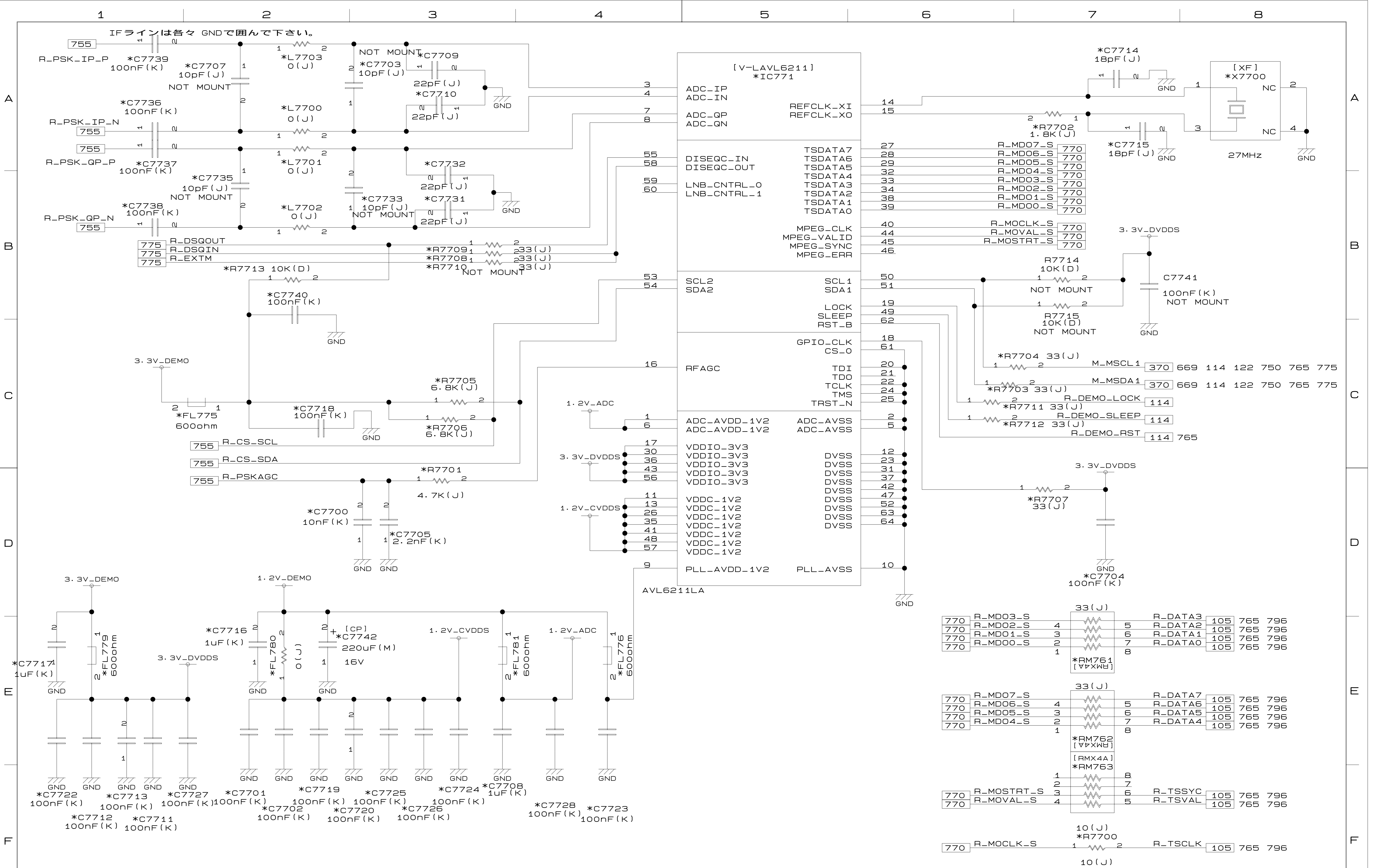
FUNCTION
DEMOM DVB-T2/T/C

SH. NO.
765

PAGE NO.
64

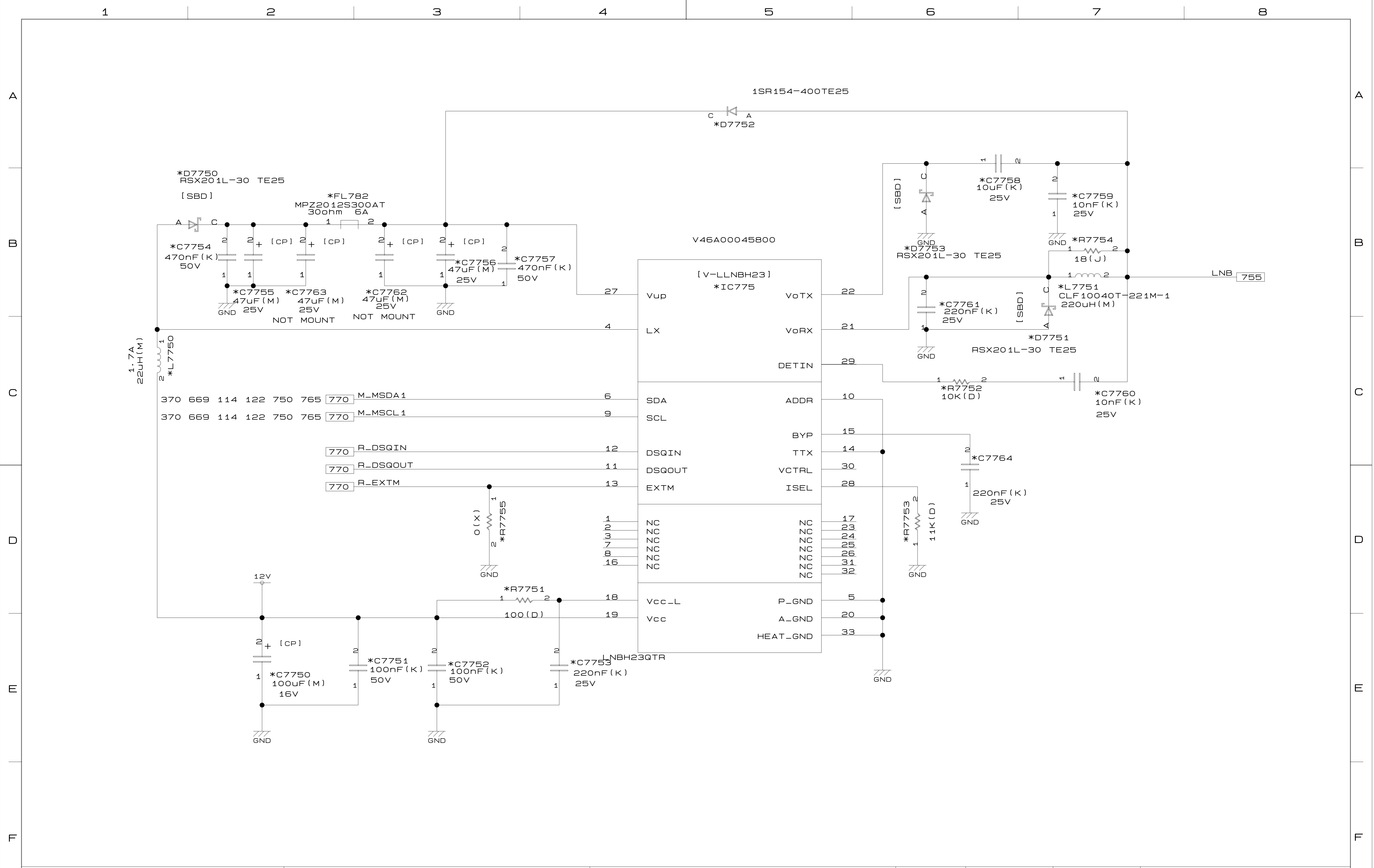
REV. MARK
MP

DRAWING. NO.
PE1063ZA

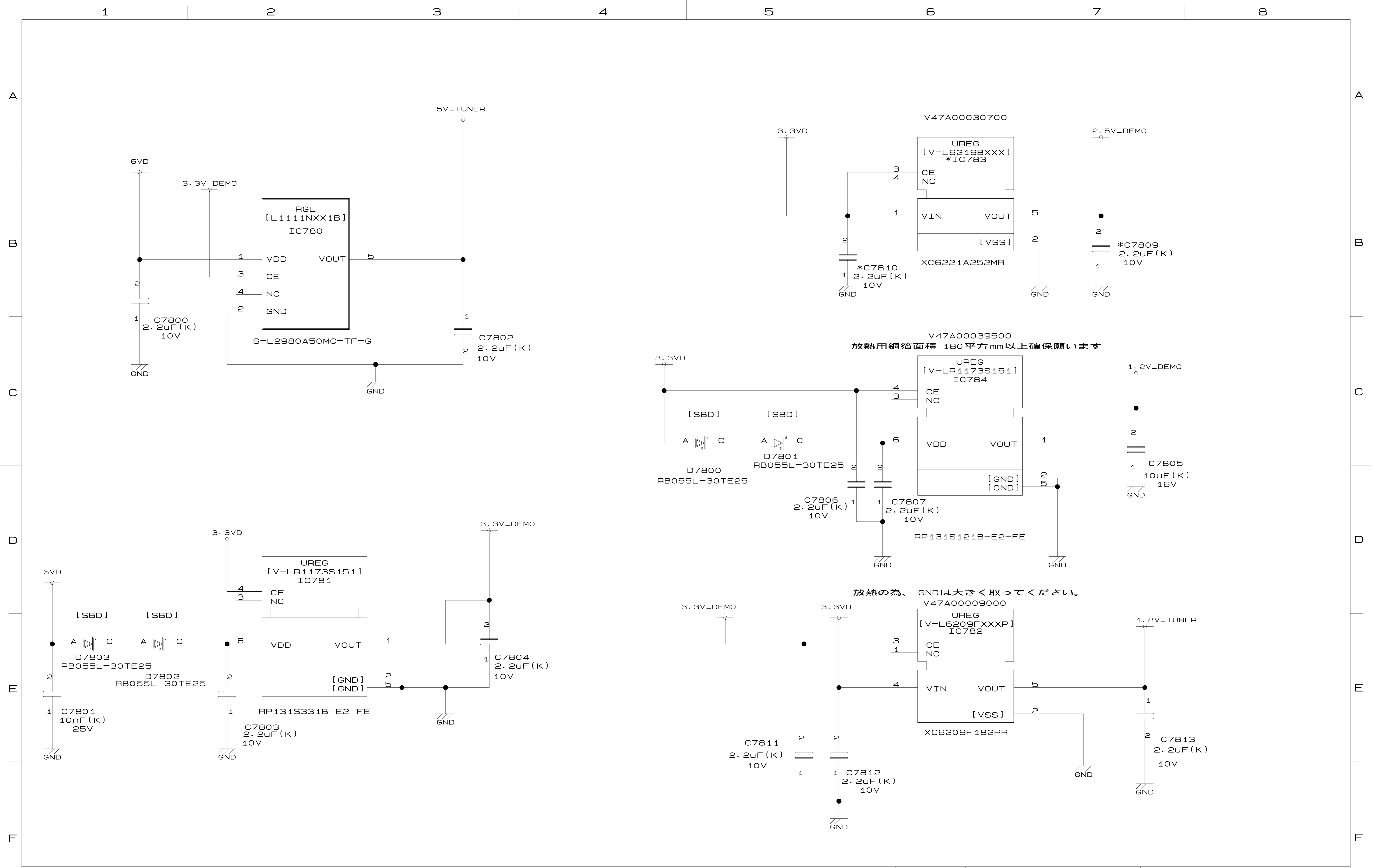


DESIGNED BY R. IZUMI	TITLE MAIN UNIT	FUNCTION DEMOD DVB-S2/S	SH. NO. 770	PAGE NO. 65	REV. MARK MP	DRAWING. NO. PE1063ZA
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2012.01.19 13:53



DESIGNED BY	TITLE	FUNCTION	SH. NO.	PAGE NO.	REV. MARK	DRAWING. NO.
R. IZUMI	MAIN UNIT	DiSeqC, LNB POWER	775	66	MP	PE1063ZA



DESIGNED BY
R. IZUMI

TITLE
MAIN UNIT

FUNCTION
TUNER POWER

SH. NO.
780

PAGE NO.
67

REV. MARK
MP

DRAWING. NO.
PE1063ZA

2012.01.19 13:53

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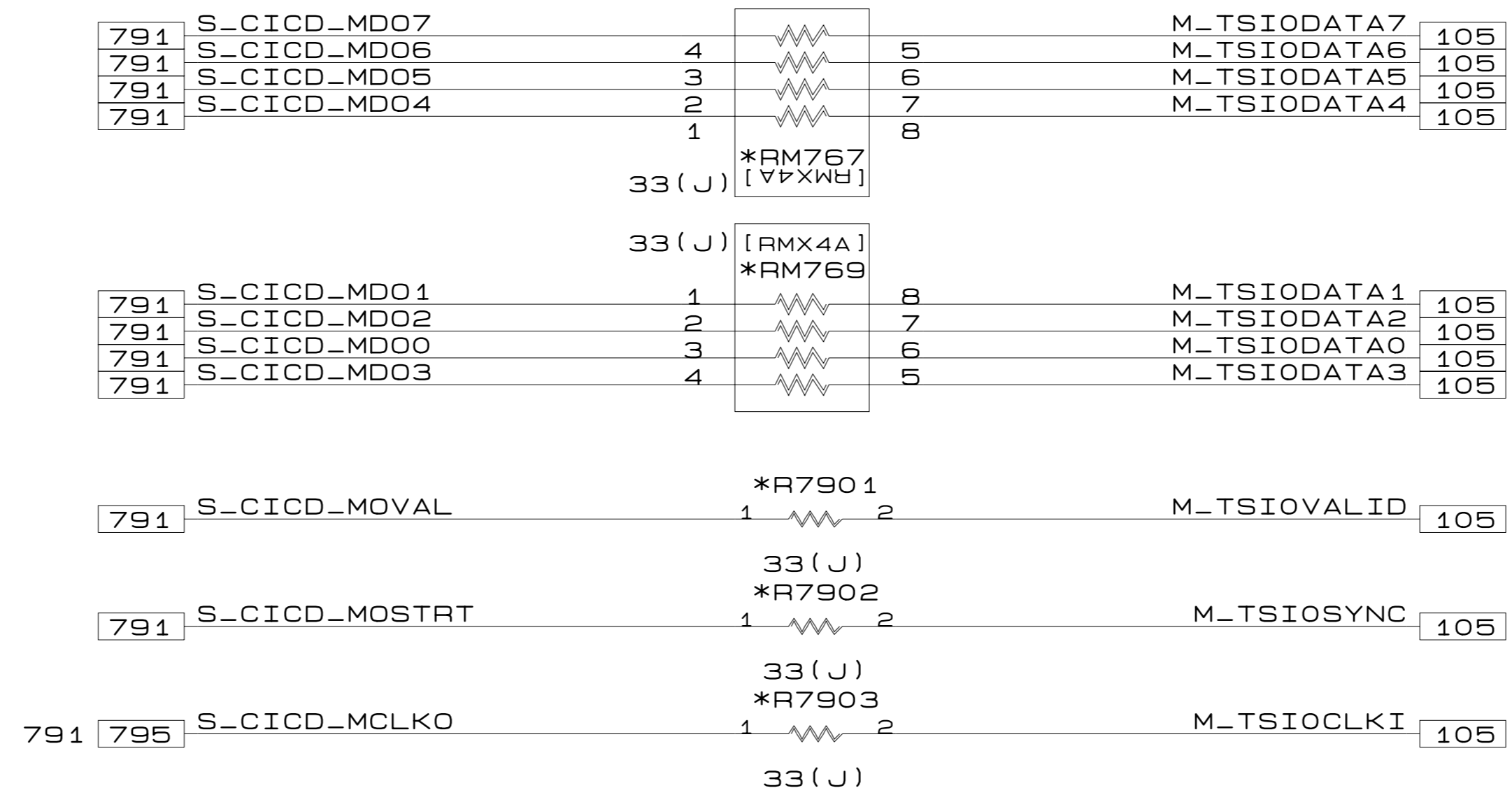
D

E

E

F

F



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

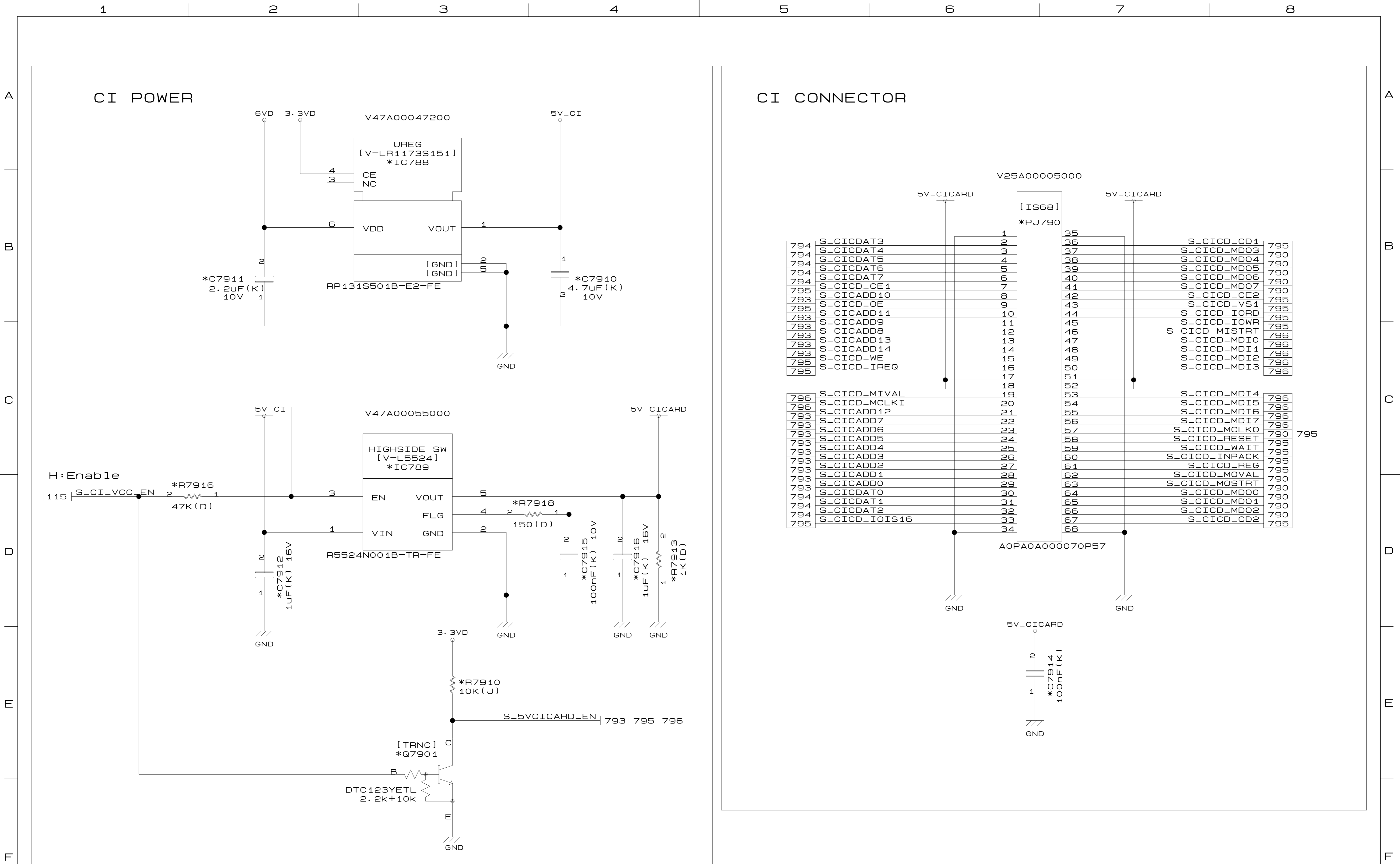
FUNCTION
CI TS RESISTOR

SH. NO.
790

PAGE NO.
68

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

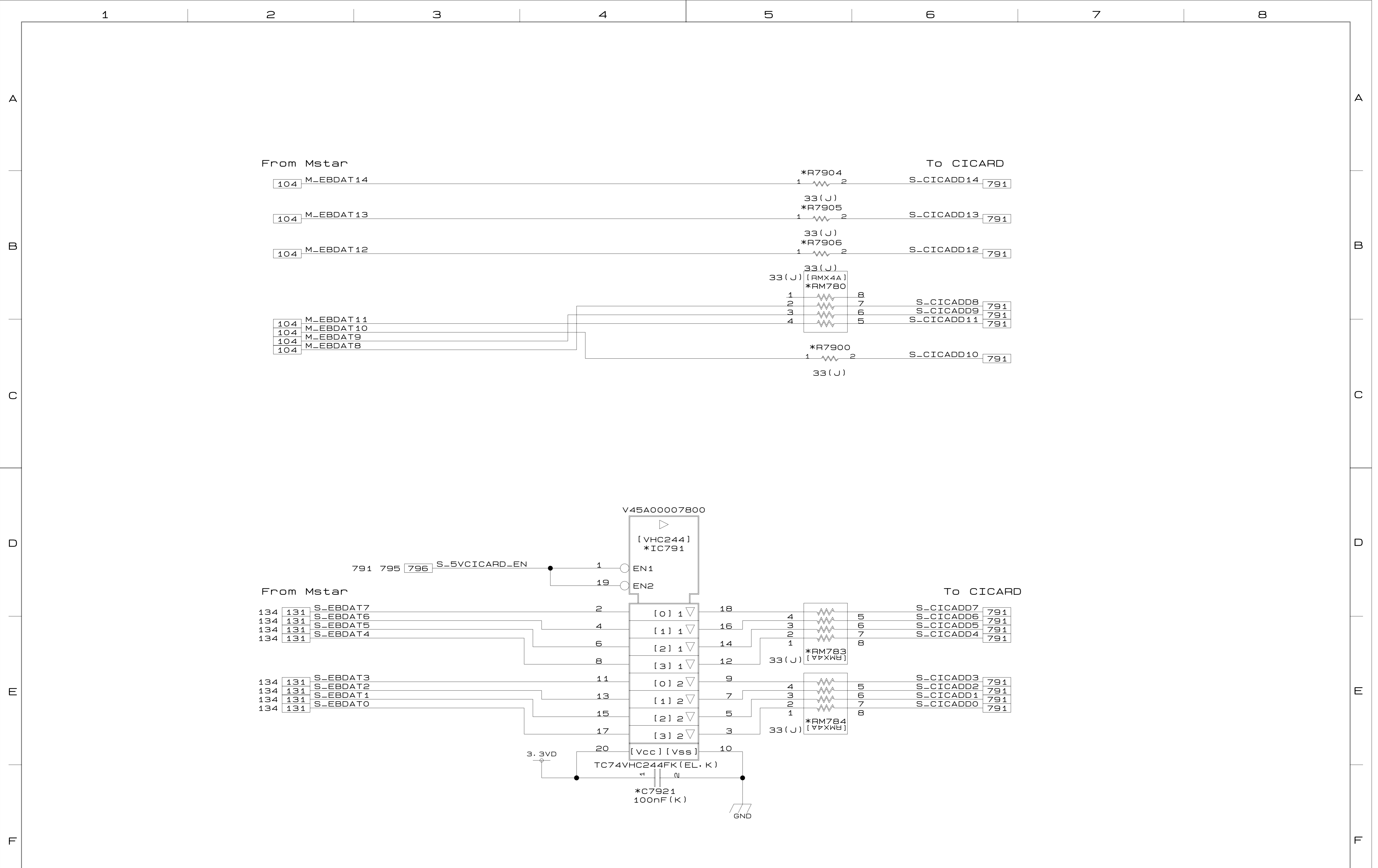
FUNCTION
CI POWER/CONNECTOR

SH. NO.
791

PAGE NO.
69

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

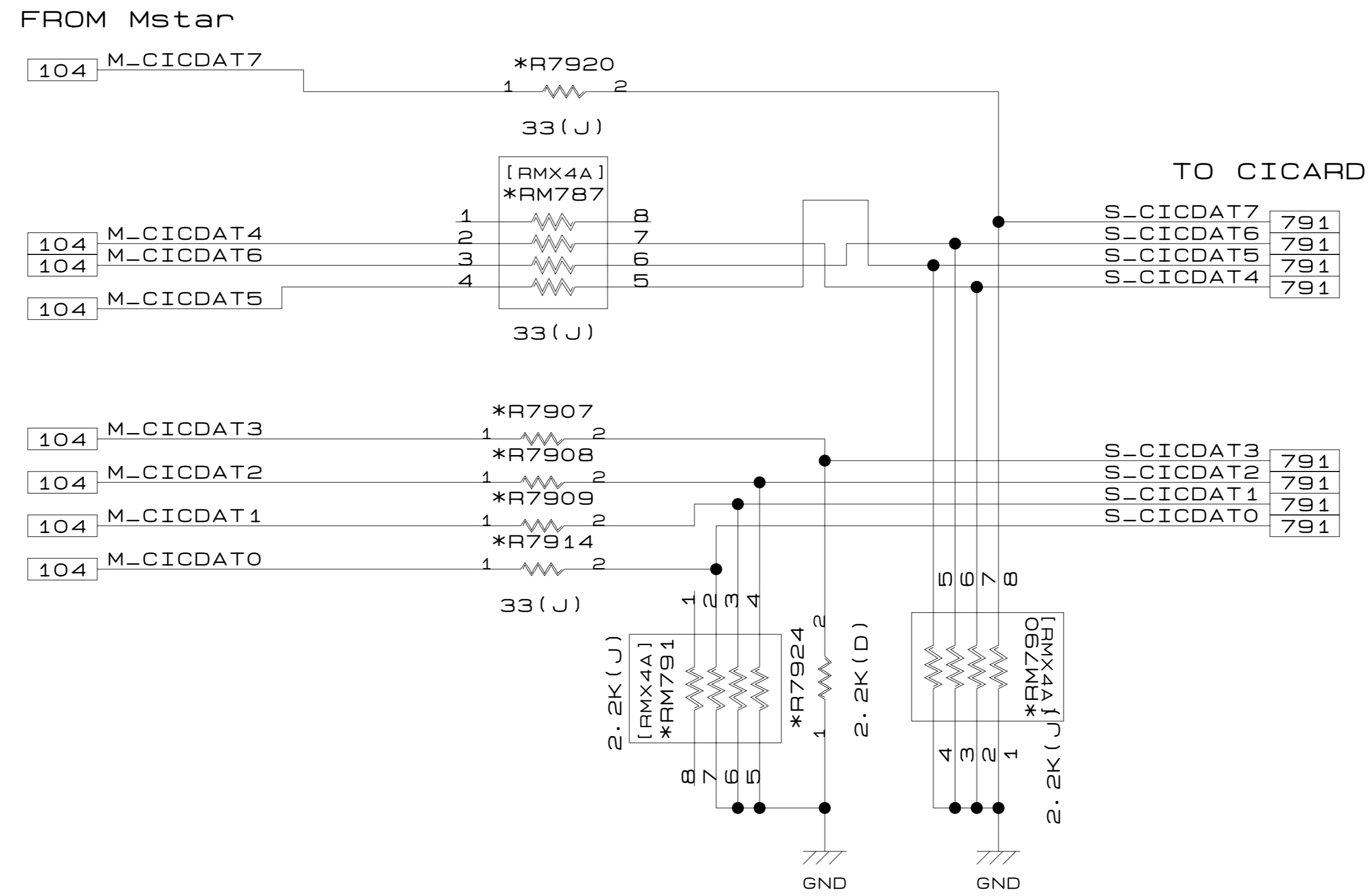
FUNCTION
CI ADDRESS BUFFER

SH. NO.
793

PAGE NO.
70

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

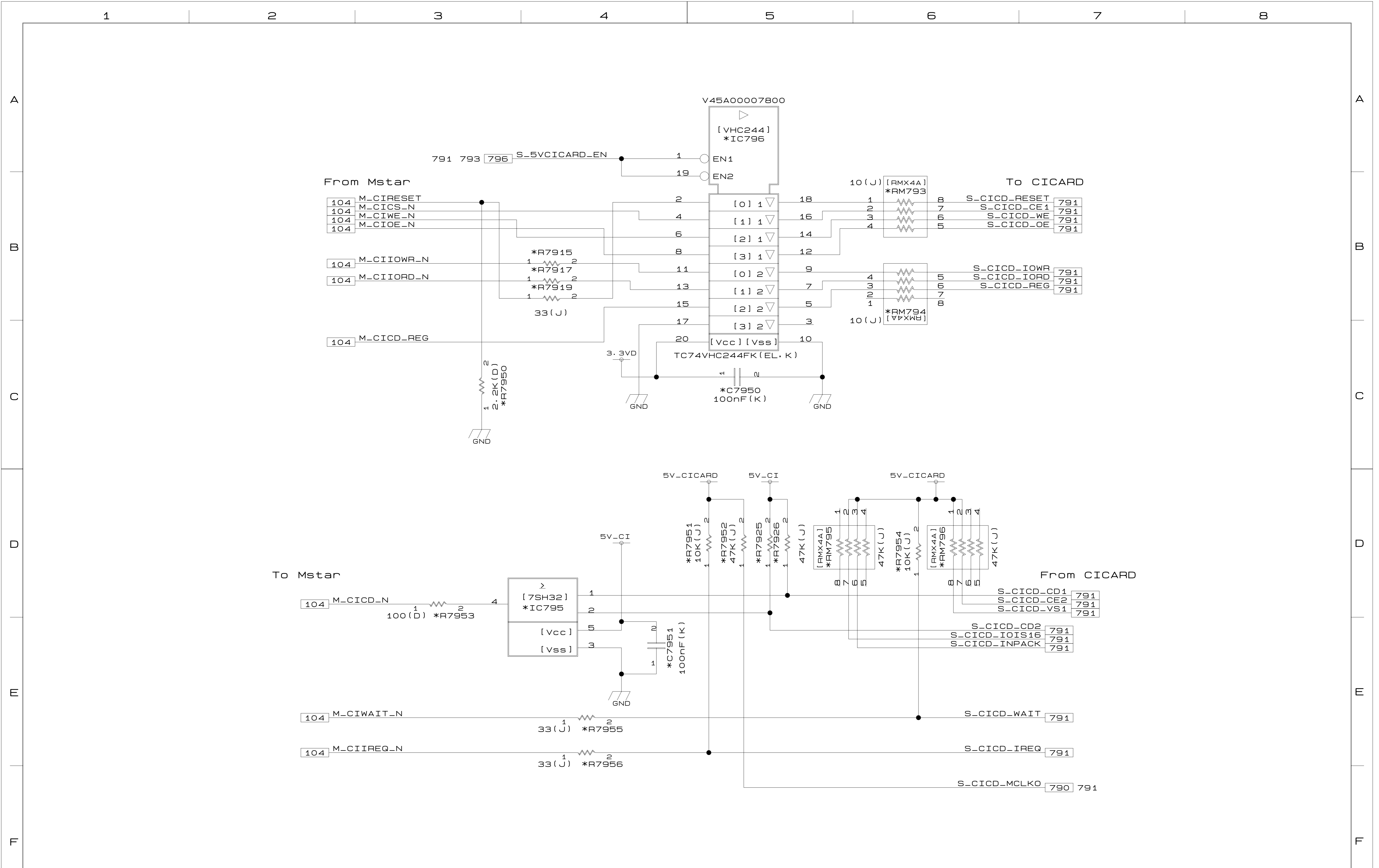
FUNCTION
CI DATA RESISTOR

SH. NO.
794

PAGE NO.
71

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

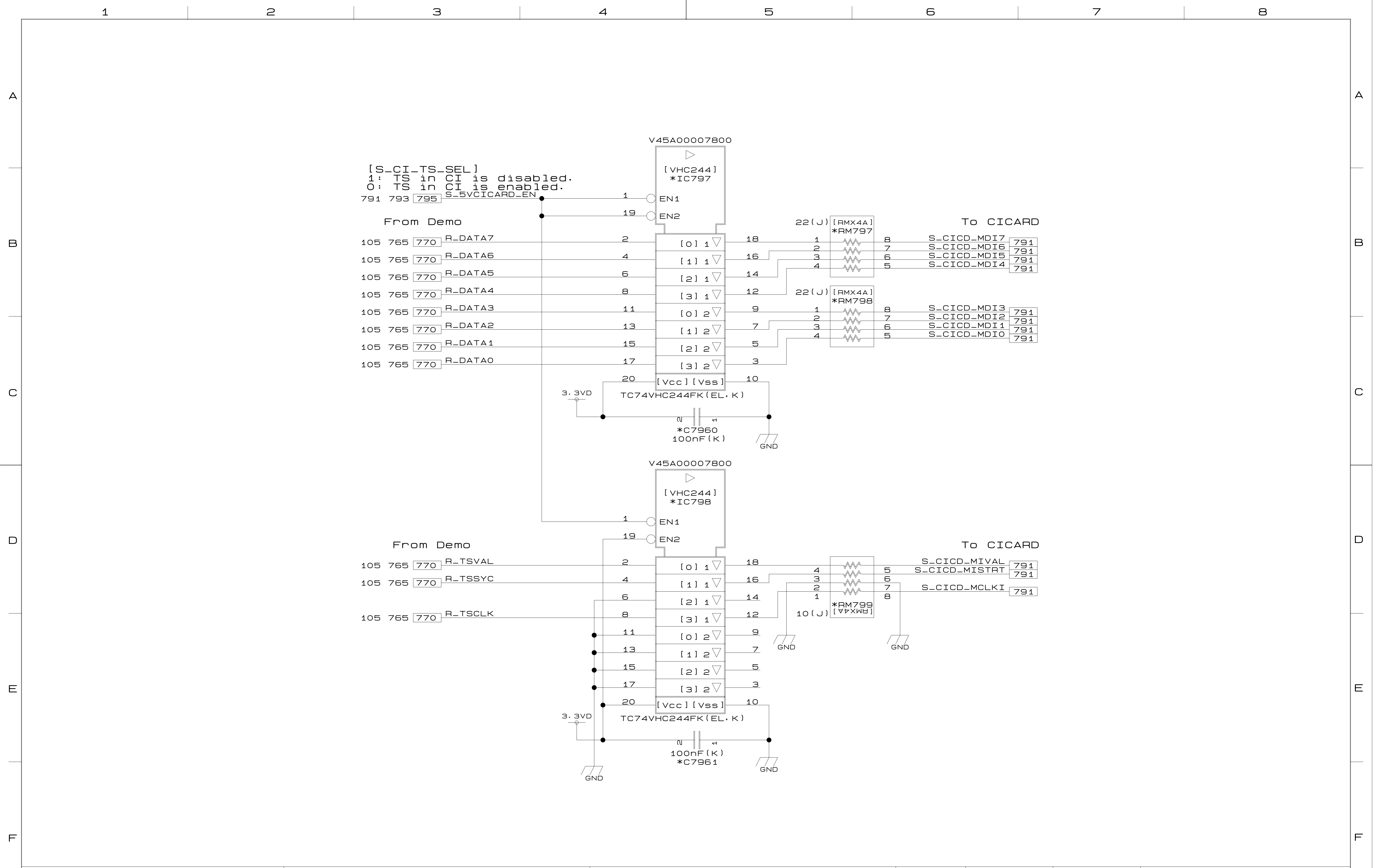
FUNCTION
CI CONTROL BUFFER

SH. NO.
795

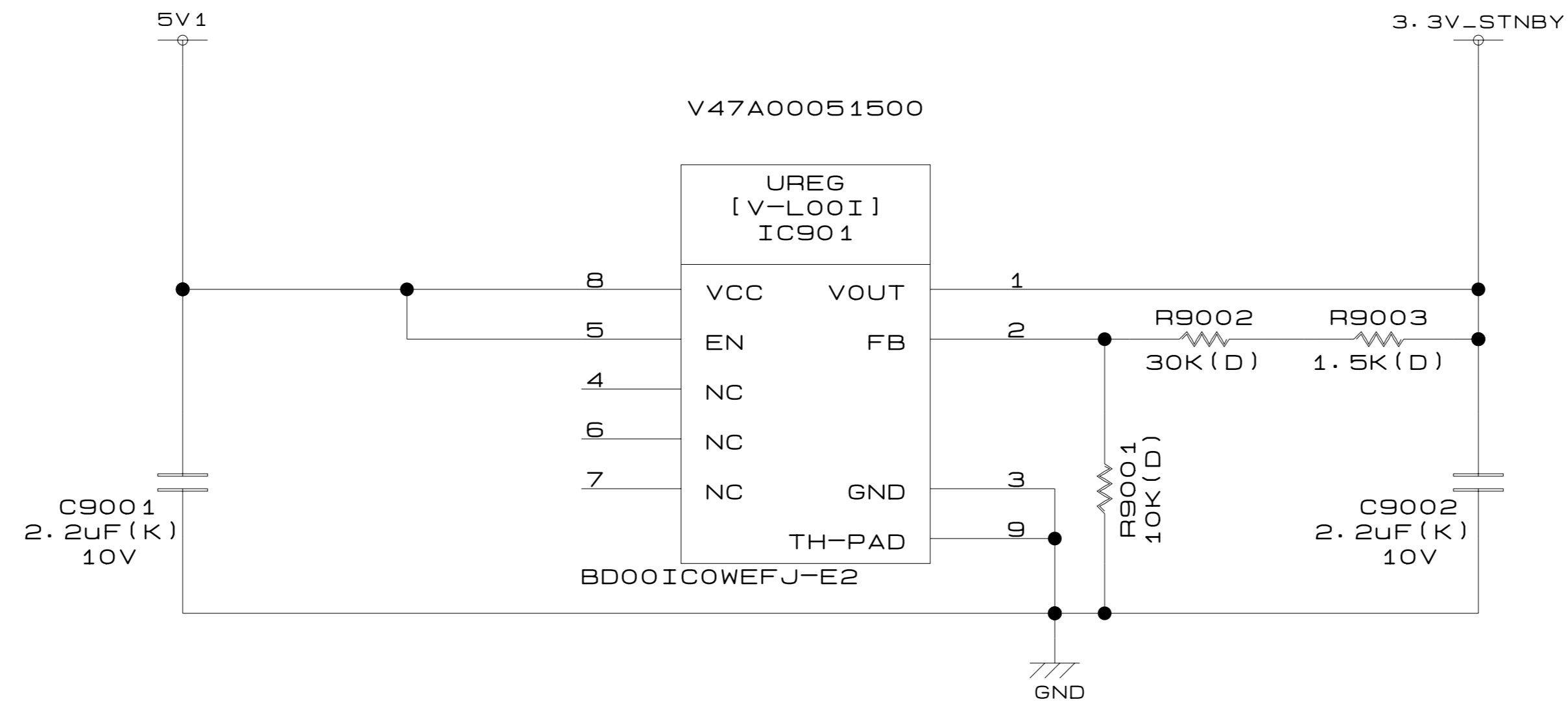
PAGE NO.
72

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY OSANAI	TITLE MAIN UNIT	FUNCTION CI TS OUTPUT BUFFER	SH. NO. 796	PAGE NO. 73	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY
HASUMI

TITLE
MAIN UNIT

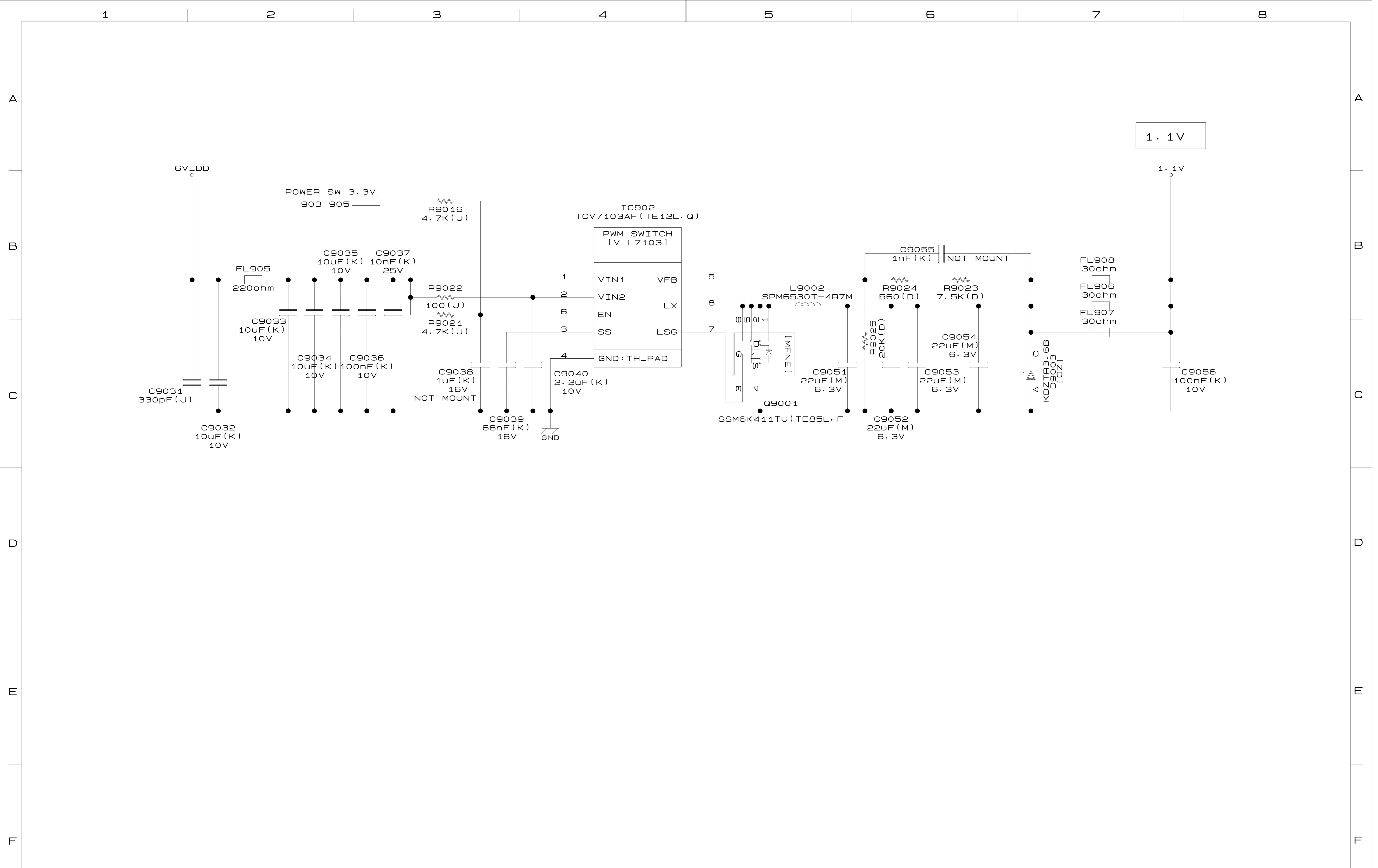
FUNCTION
3.3V STANDBY

SH. NO.
901

PAGE NO.
74

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY 2011/11/21
HASUMI

TITLE
MAIN UNIT

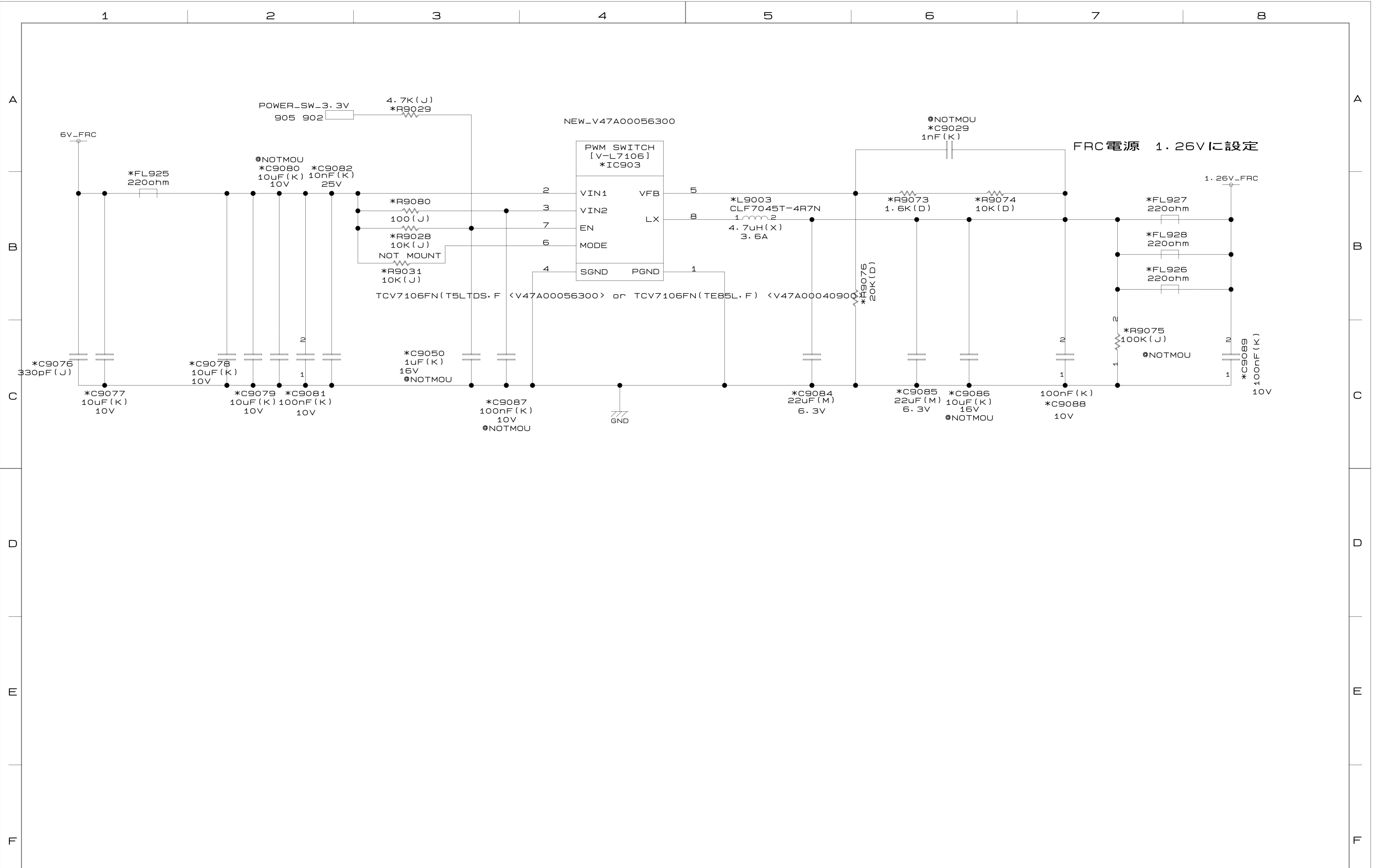
FUNCTION
POWER 1.1V

SH. NO.
902

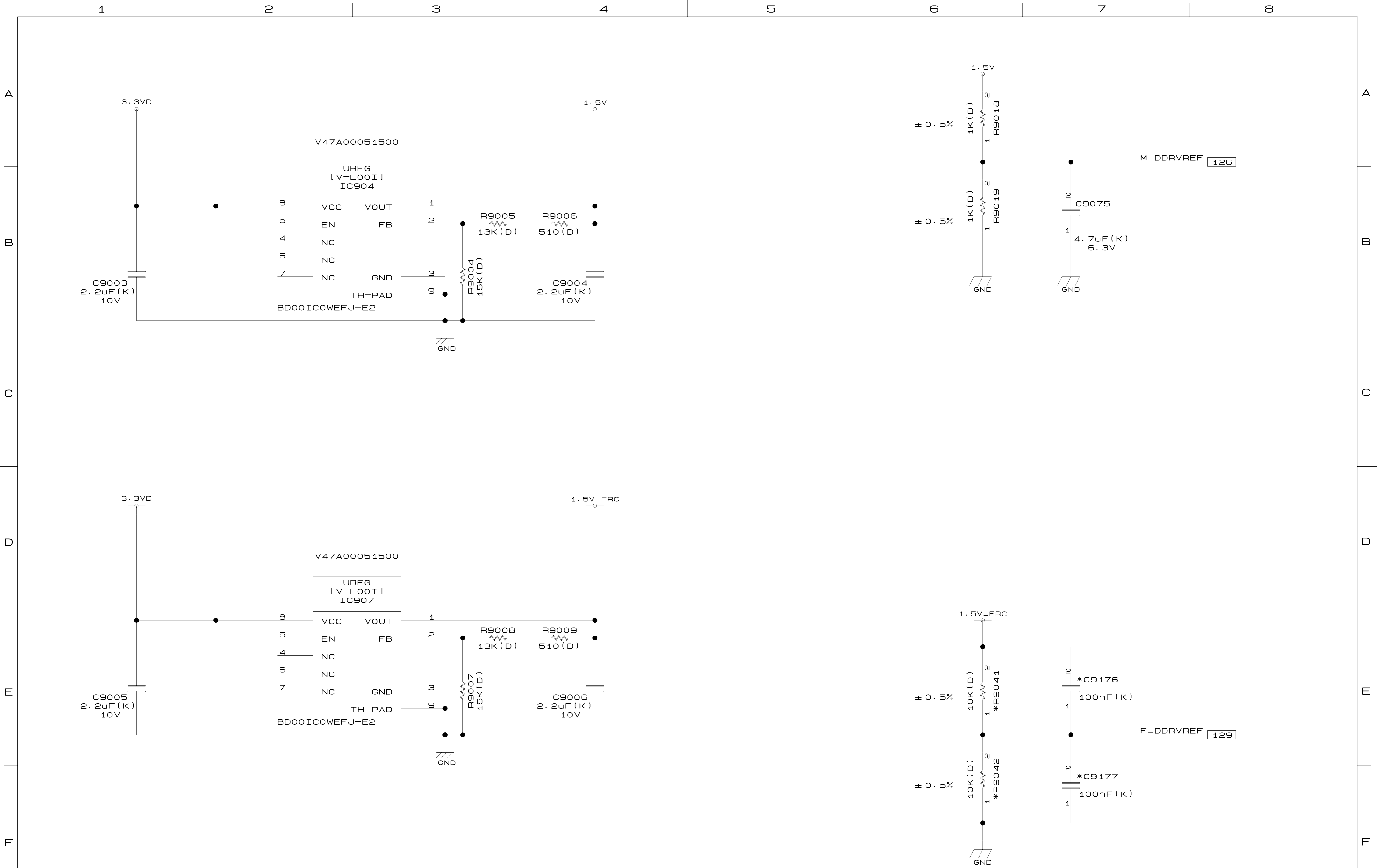
PAGE NO.
75

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY HASUMI	2011/11/21	TITLE MAIN UNIT	FUNCTION FRC POWER 1.26V	SH. NO. 903	PAGE NO. 76	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY
HASUMI

TITLE
MAIN UNIT

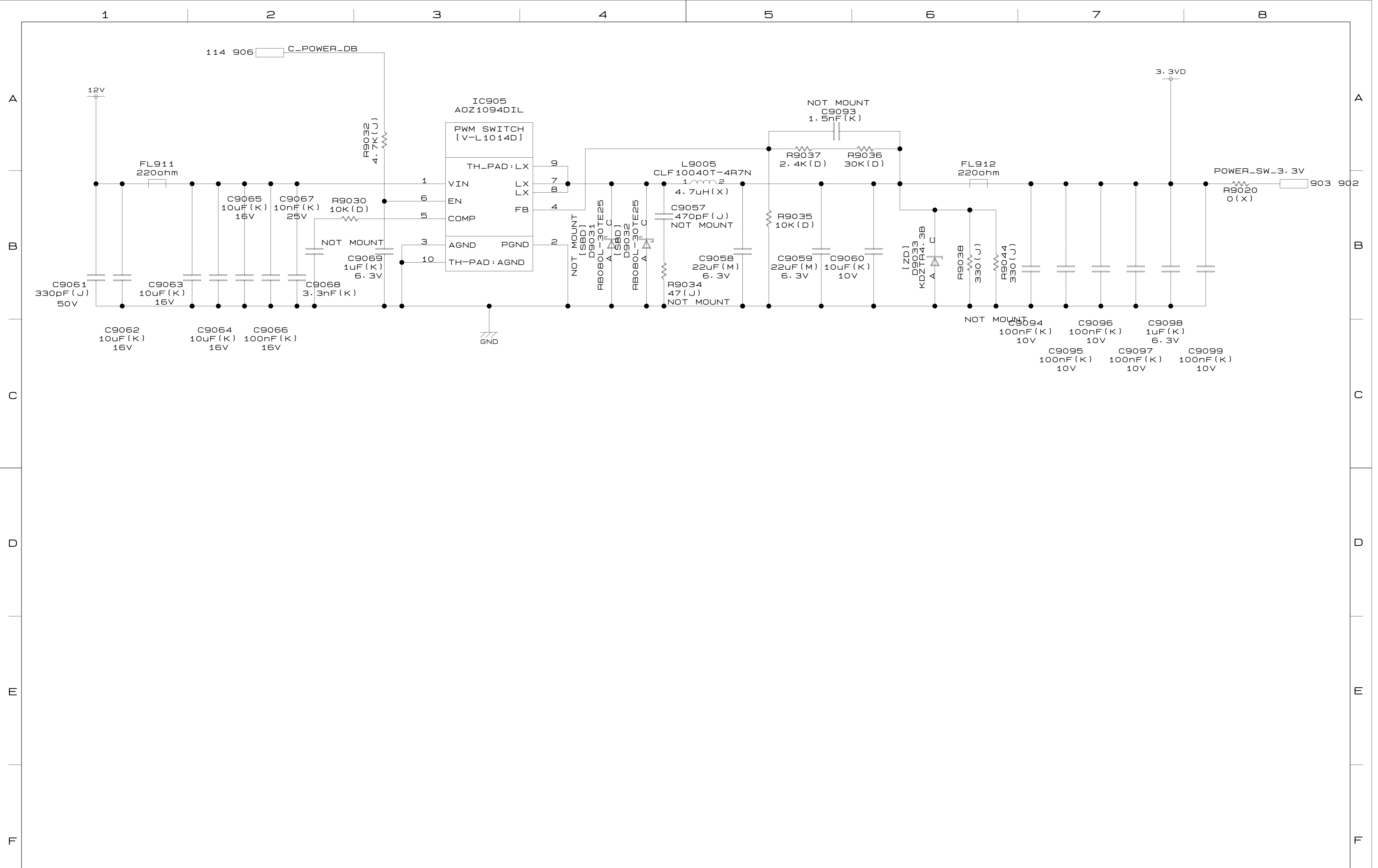
FUNCTION
FRC POWER 1.5V

SH. NO.
904

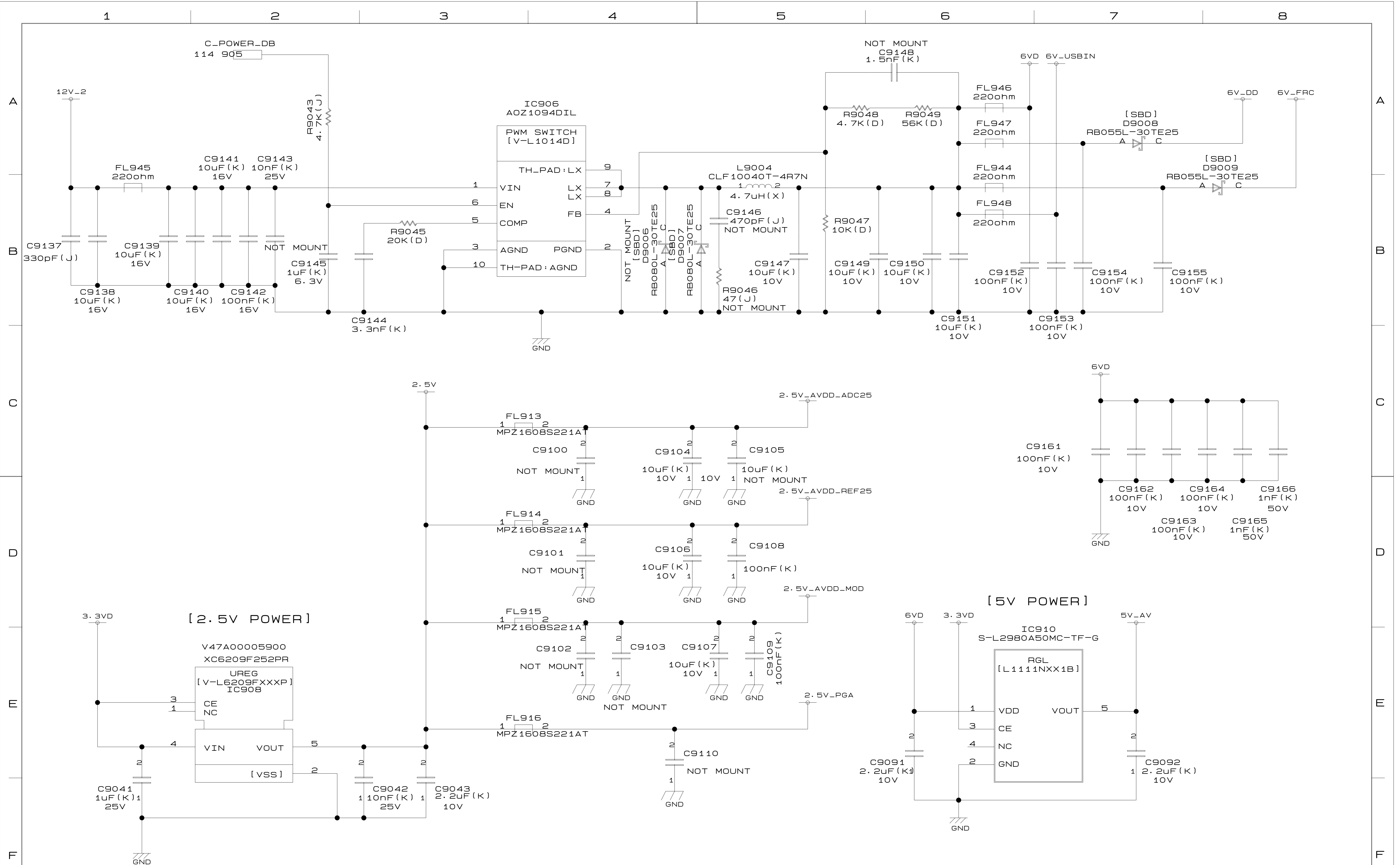
PAGE NO.
77

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY HASUMI	2011/11/21	TITLE MAIN UNIT	FUNCTION POWER 3.3VD	SH. NO. 905	PAGE NO. 78	REV. MARK MP	DRAWING. NO. PE1063ZA
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DESIGNED BY
HASUMI

2011/11/21

TITLE
MAIN UNIT

FUNCTION
POWER 6V

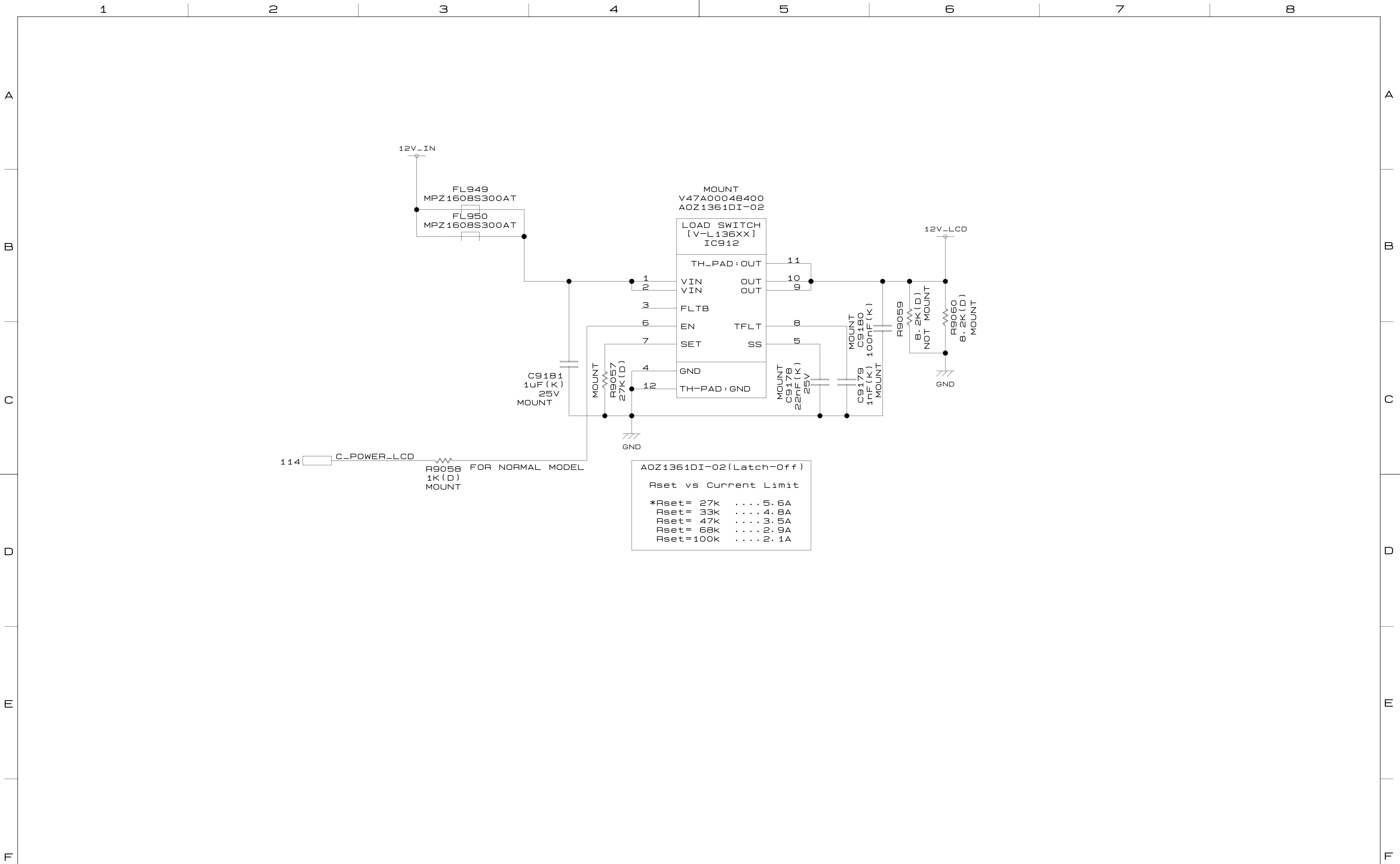
SH. NO.
906

PAGE NO.
79

REV. MARK
MP

DRAWING. NO.
PE1063ZA

2012.01.19 13:54



DESIGNED BY 2011/11/21
HASUMI

TITLE
MAIN UNIT

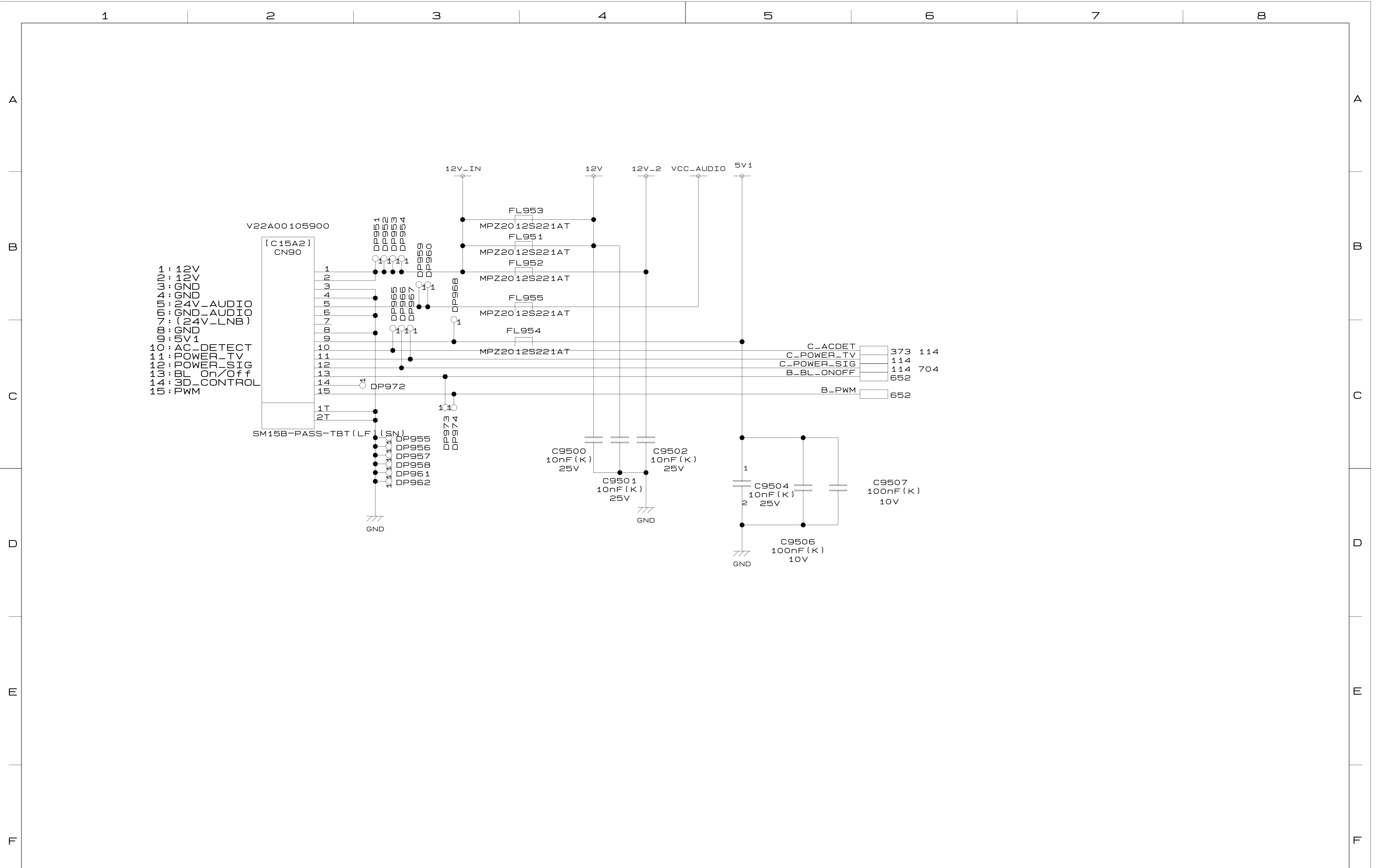
FUNCTION
PANEL I/F (POWER)

SH. NO.
914

PAGE NO.
80

REV. MARK
MP

DRAWING. NO.
PE1063ZA



DESIGNED BY
OSANAI

TITLE
MAIN UNIT

FUNCTION
POWER CONNECTOR

SH. NO.
950

PAGE NO.
81

REV. MARK
MP

DRAWING. NO.
PE1063ZA

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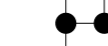
F

F

NOT MOUNT
VE1A00004500
OG-503040

[V-FC3]
E960

1 2 3



GND

NOT MOUNT
VE1A00004500
OG-503040

[V-FC3]
E961

1 2 3



GND

DESIGNED BY
OSANAI

TITLE
MAIN UNIT

FUNCTION
EMC CONTROL

SH. NO.
960

PAGE NO.
82

REV. MARK
MP

DRAWING. NO.
PE1063ZA

