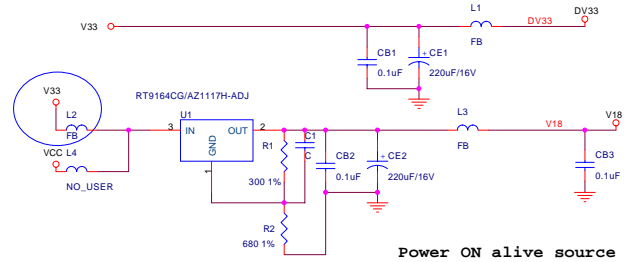
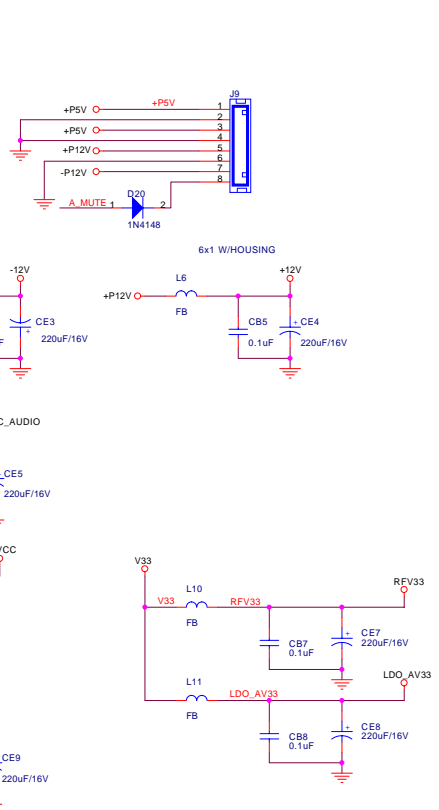


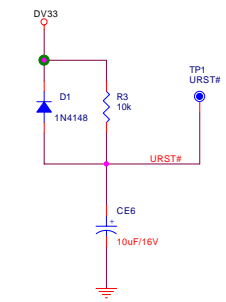
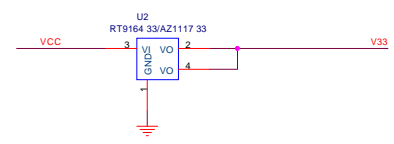
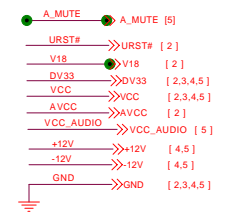
MT1389D (LQFP216) DVD Demo Board for KHM310

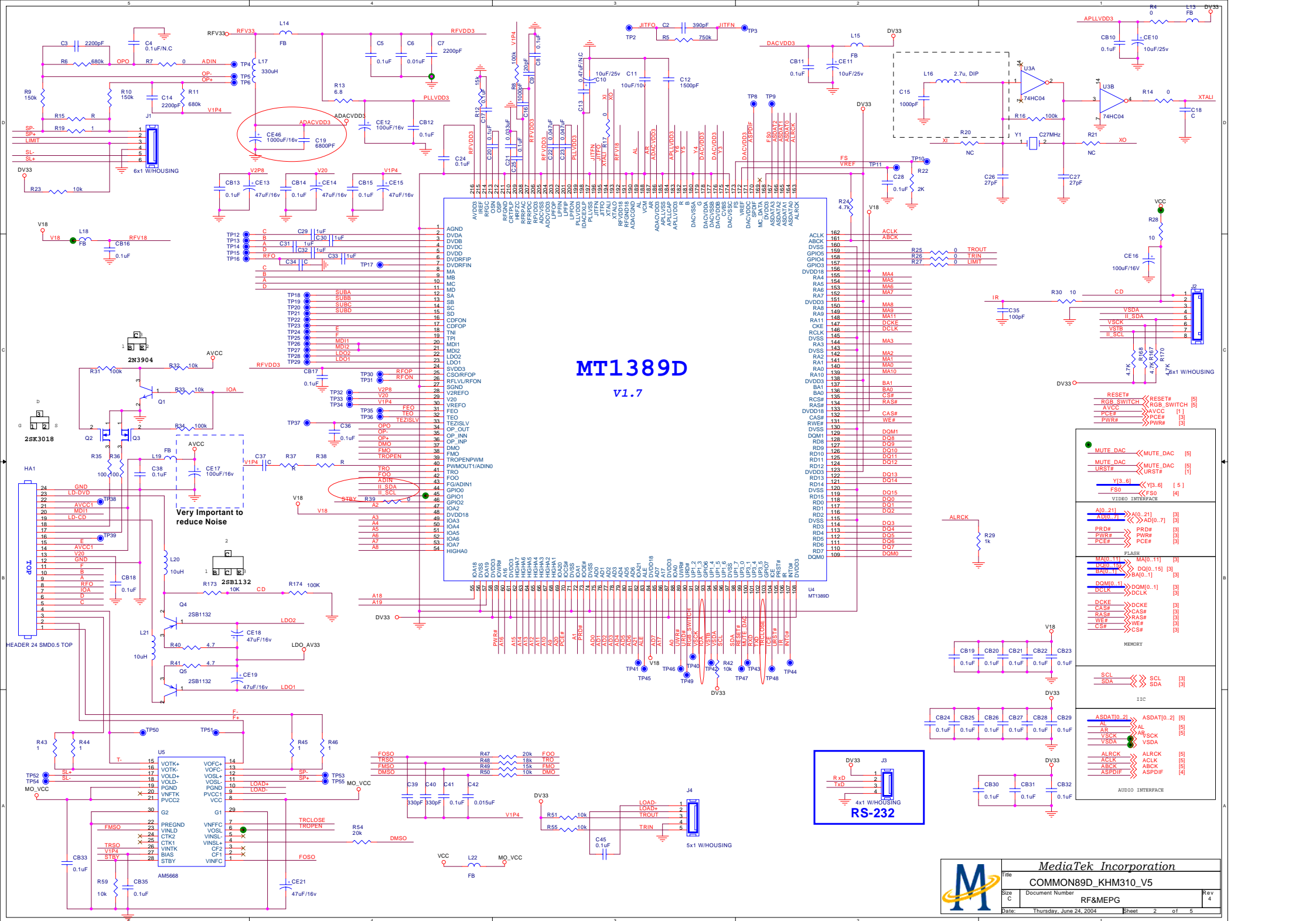
- 1 INDEX & POWER, RESET
- 2 RF, SERVO & MPEG - MT1389E
- 3 MEMORY - SDRAM, FLASH/EEPROM
- 4 VIDEO OUT
- 5 AUDIO DAC WMA8766

NAME	TYPE	DEVICE
VCC	Digital 5V	SUPPLY
DV33	Digital 3.3V	MT1389E
RFV33	Servo 3.3V	MT1389E
LDO_AV33	Laser Diode 3.3V	
AVCC	RF 5V	PICKUP HEADER
V18	Digital 1.8V	MT1389E
SD33	Digital 3.3V	SDRAM
+12V	Audio +12V	OP AMP.
-12V	Audio -12V	OP AMP.
AVDD	Audio 5V	Audio DAC
DVDD	Audio 3V3	Audio DAC



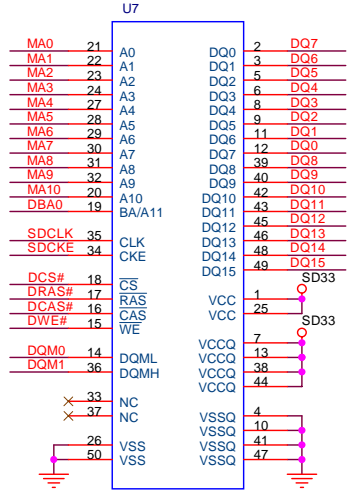
Regulator	R1	R2
Fix regulator	0 ohm	OFF
Adj regulator	300 1%	680 1%



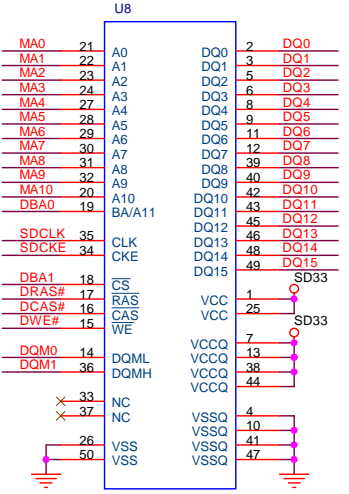


MT1389D V1.7

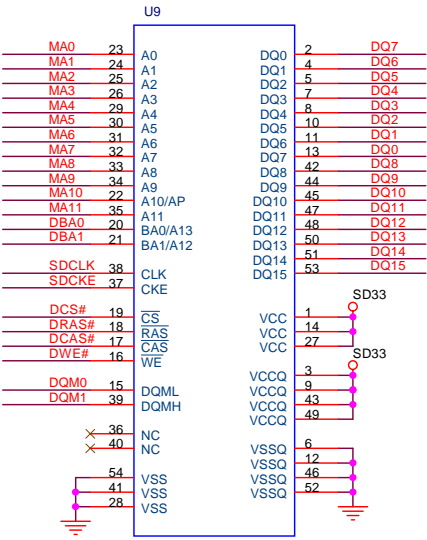
RESET#	RESET#	[5]
RGB_SWITCH	RGB_SWITCH	[5]
AVCC	AVCC	[1]
PCE#	PCE#	[3]
PWR#	PWR#	[3]
MUTE_DAC	MUTE_DAC	[5]
MUTE_DAC	MUTE_DAC	[5]
URST#	URST#	[1]
Y[3..6]	Y[3..6]	[15]
FS0	FS0	[4]
AIO[2:1]	AIO[2:1]	[3]
AUD[7]	AUD[7]	[3]
PRD#	PRD#	[3]
PWR#	PWR#	[3]
PCE#	PCE#	[3]
FLASH		
MA[0..11]	MA[0..11]	[3]
DO[0..15]	DO[0..15]	[3]
BA[0..1]	BA[0..1]	[3]
DOM[0..1]	DOM[0..1]	[3]
DCLK	DCLK	[3]
DCKE	DCKE	[3]
CAS#	CAS#	[3]
RAS#	RAS#	[3]
WE#	WE#	[3]
CS#	CS#	[3]
SCL	SCL	[3]
SDA	SDA	[3]
ASDAT[0..2]	ASDAT[0..2]	[5]
AR	AR	[5]
ASBCK	ASBCK	[5]
VSDA	VSDA	[5]
ALCK	ALCK	[5]
ACLK	ACLK	[5]
ABCK	ABCK	[5]
ASPDIF	ASPDIF	[4]



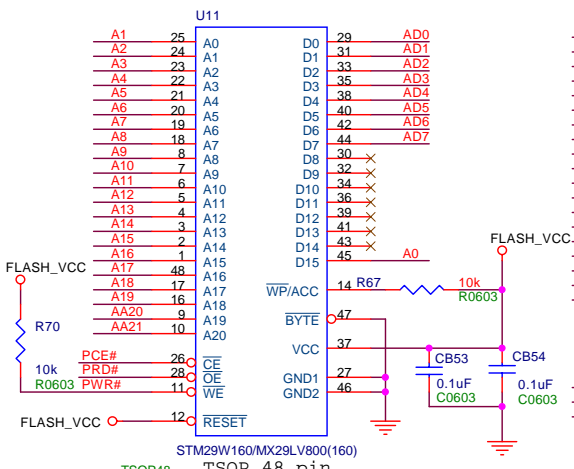
ESMT M12L16161A-7
TSOP50



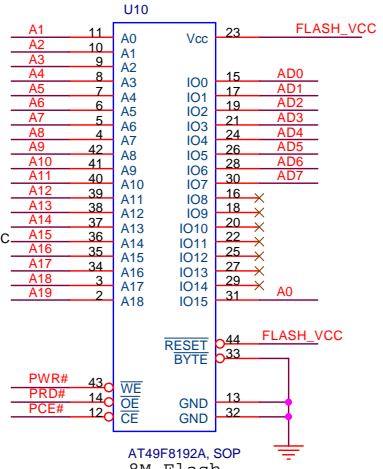
ESMT M12L16161A-7
TSOP50



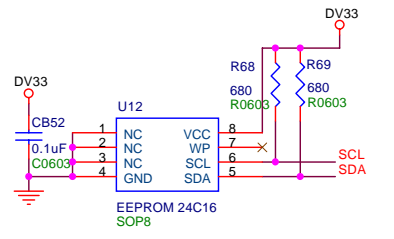
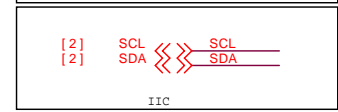
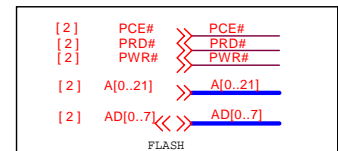
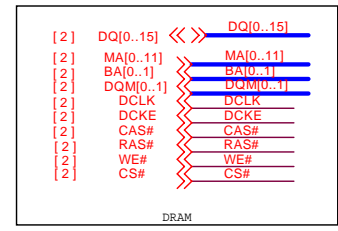
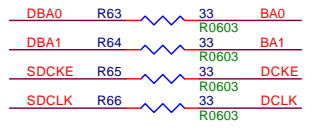
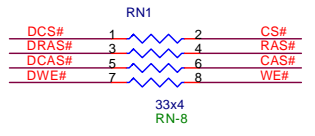
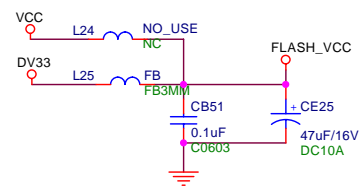
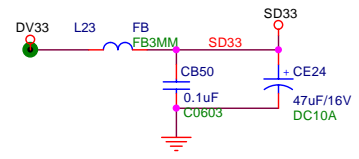
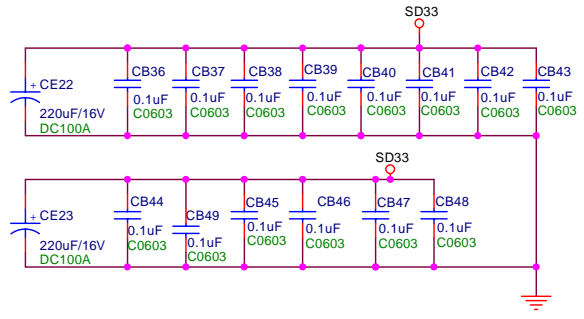
ESMT M12L64164A/N.C.
TSOP54



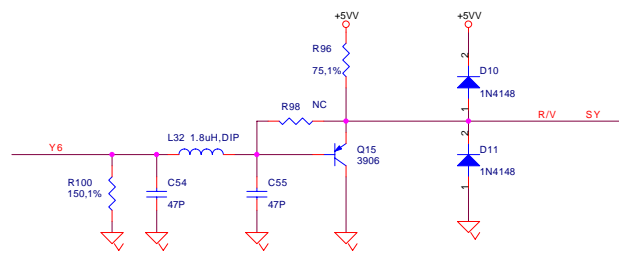
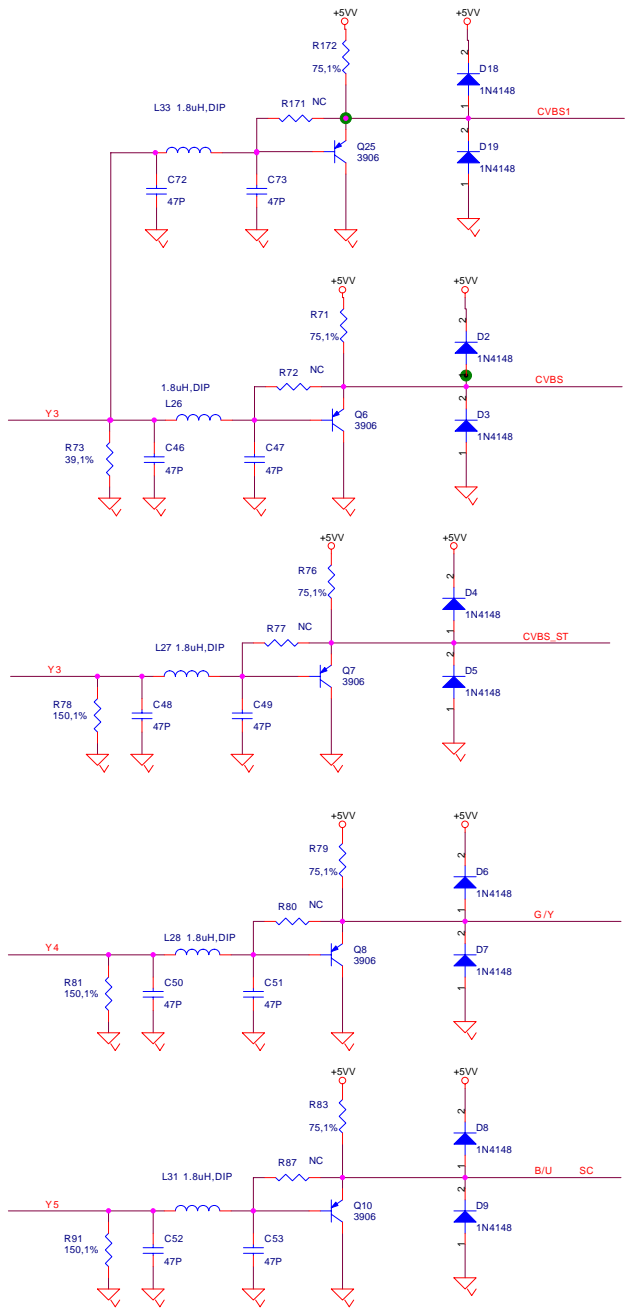
STM29W160/MX29LV800 (160)
TSOP48
8M 16M 32M FLASH



AT49F8192A, SOP
8M Flash
TS48



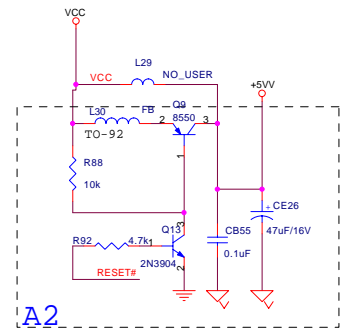
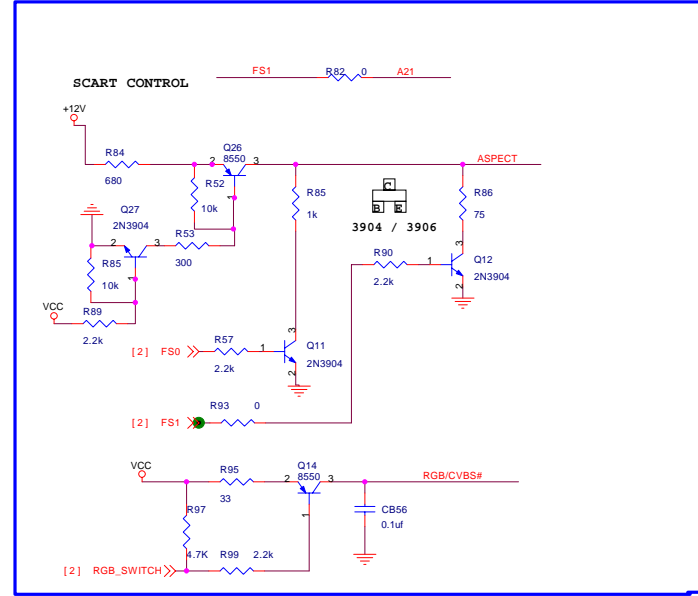
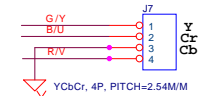
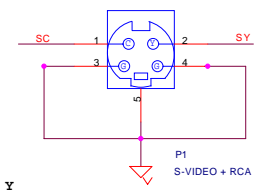
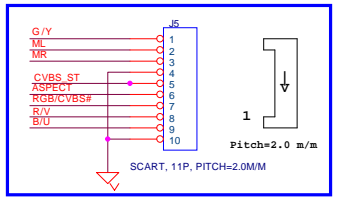
	MediaTek Incorporation	
	Title COMMON89D_KHM310_V5	
Size B	Document Number SDRAM&FLASH	Rev 4
Date: Thursday, June 24, 2004	Sheet 3	of 5



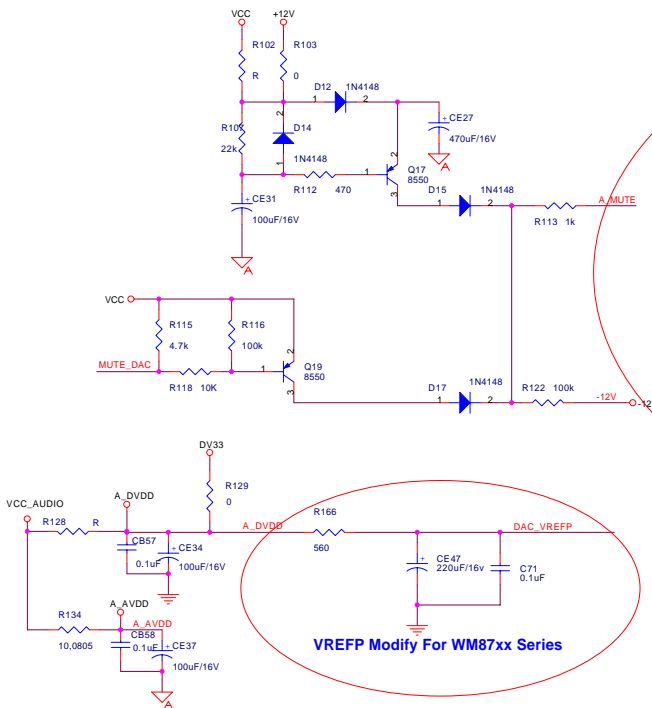
CVBS	<< CVBS [5]
CVBS1	<< CVBS1 [5]
RESET#	<< RESET# [2]
MUTE_DAC	<< MUTE_DAC [5]
Y[3..6]	<< Y[3..6] [2]
AL	<< AL [2]
AR	<< AR [2]
ML	<< ML [5]
MR	<< MR [5]
G/Y	<< G/Y [5]
B/U	<< B/U [5]
R/V	<< R/V [5]
FS0	<< FS0 [2]
A2T	<< A2T [2]
ASDAT0	<< ASDAT0 [2]
RGB_SWITCH	<< RGB_SWITCH [2]
GND	<< GND [1]
VCC	<< VCC [1]
+12V	<< +12V [1]
-12V	<< -12V [1]



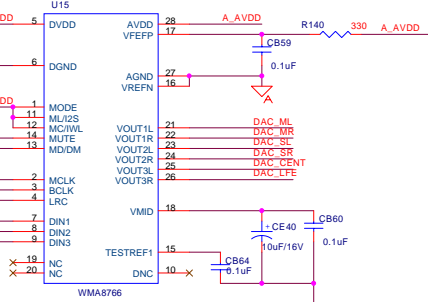
SCART CONNECTOR



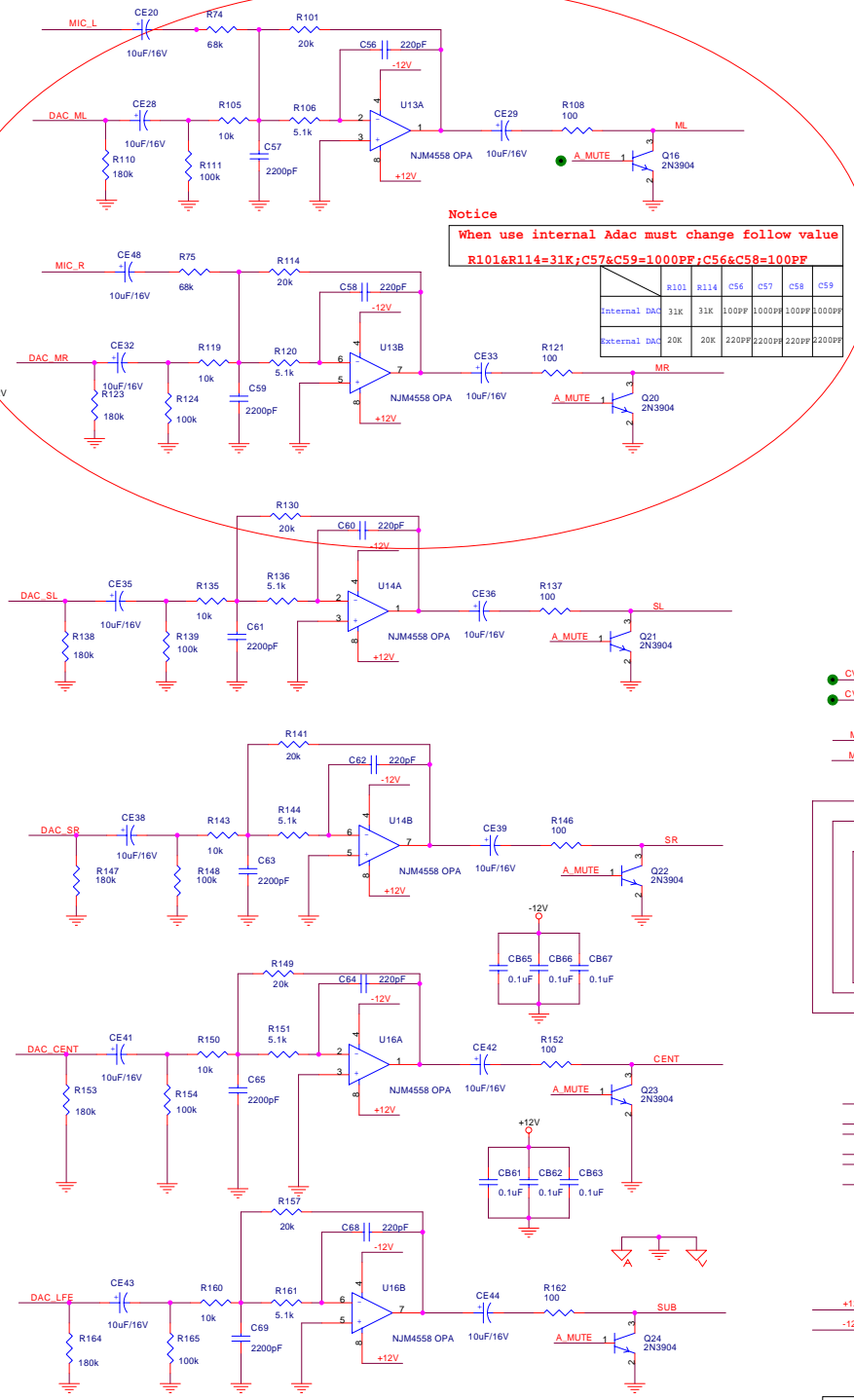
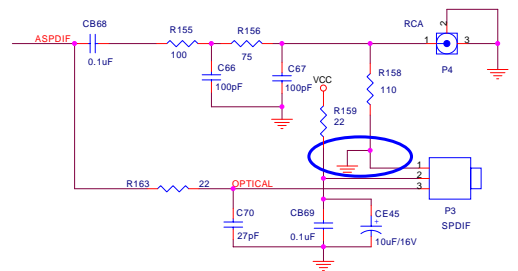
MediaTek Incorporation			
File	COMMON89D_KHM310_V5		
Size	Document Number	VIDEO OUT PORT	Rev
Custom			4
Date:	Thursday, June 24, 2004	Sheet	4 of 5



VREFP Modify For WM87xx Series



HARDWARE: IIS 24BIT



Notice
When use internal Adac must change follow value
R101&R114=31K;C57&C59=1000PF;C56&C58=100PF

	R101	R114	C56	C57	C58	C59
Internal DAC	31K	31K	1000PF	1000PF	100PF	1000PF
External DAC	20K	20K	220PF	220PF	220PF	2200PF

- G/V <-> G/Y [4]
- B/D <-> B/RV [4]
- R/V <-> R/V [4]
- [1] A_MUTE <-> A_MUTE
- [4] CVBS <-> CVBS
- [4] CVBS1 <-> CVBS1
- [1] -12V <-> -12V
- [1] +12V <-> +12V
- [1] DV33 <-> DV33
- [1] VCC <-> VCC
- [1] VCC_AUDIO <-> VCC_AUDIO
- [1,2,3,5] GND <-> GND
- [2] AL <-> AL
- [2] AR <-> AR
- [2] ASPDIF <-> ASPDIF
- [2] ASDAT0.2I <-> ASDAT0_2I
- [2] ACLK <-> ACLK
- [2] ABCK <-> ABCK
- [2] ALRCK <-> ALRCK
- [2] MUTE_DAC <-> MUTE_DAC
- [4] ML <-> ML
- [4] MR <-> MR
- ACLK R125 <-> 33 SACLK
- ALRCK R126 <-> 33 SLRCK
- ABCK R127 <-> 33 SBCLK
- ASDAT0 R131 <-> 33 SDAT0
- ASDAT1 R132 <-> 33 SDAT1
- ASDAT2 R133 <-> 33 SDAT2

