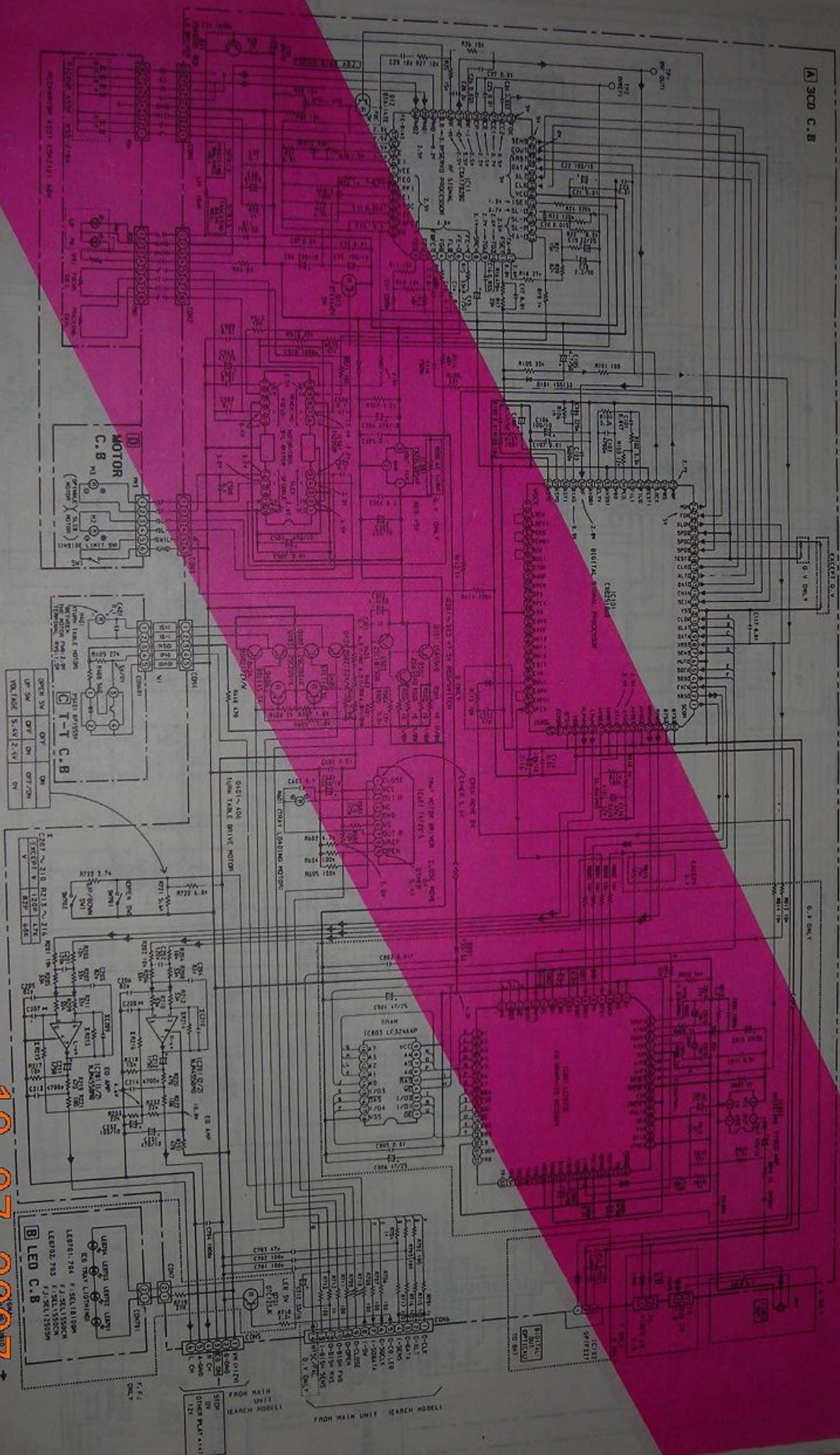
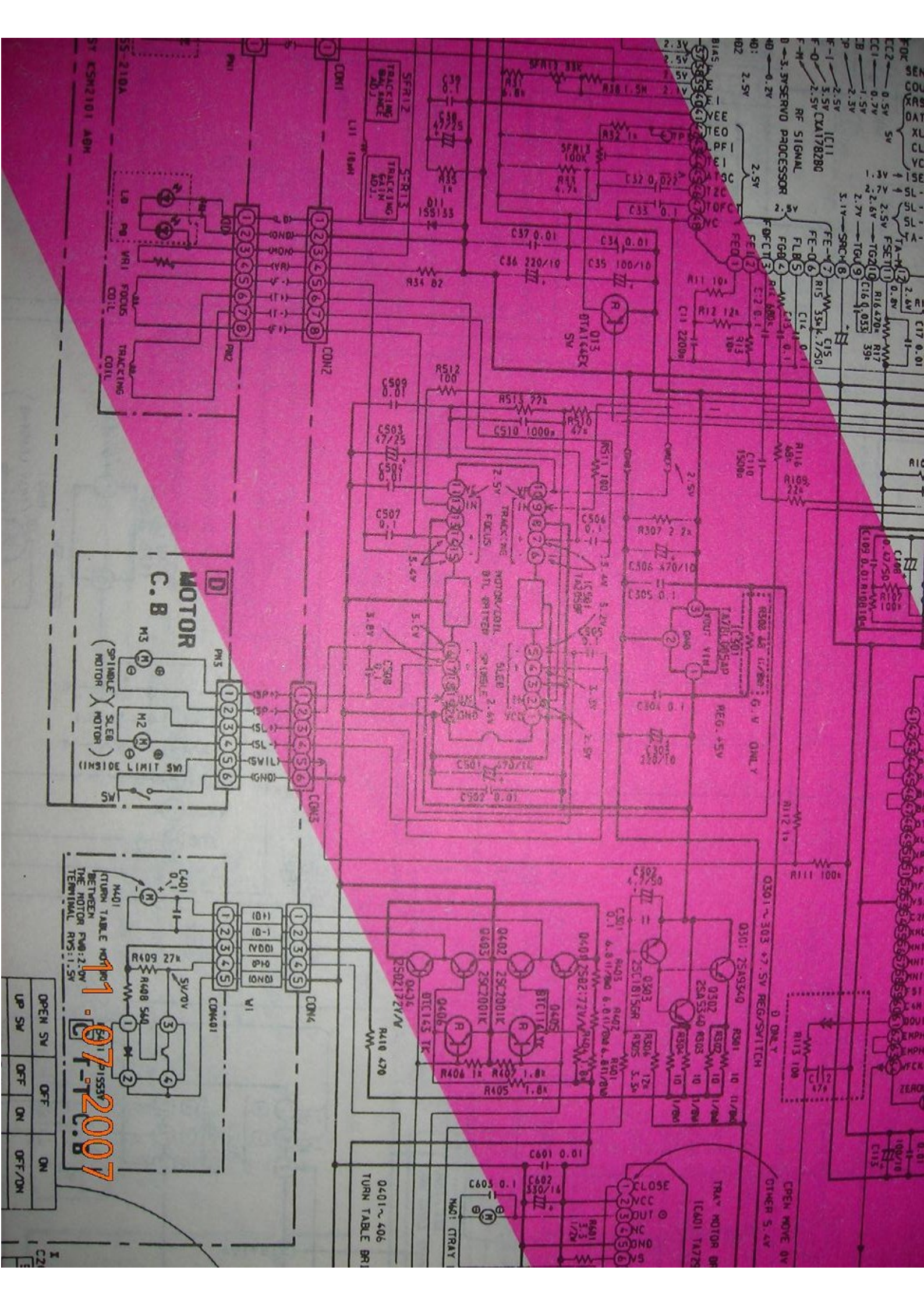


SCHEMATIC DIAGRAM-2 (EXCEPT VI, CR)



10.07.2007



11-07-2007

UP SW	DOWN SW	OFF	ON
UP SW	DOWN SW	OFF	ON

2.5 25 MDP
 0.0 24 MON
 0.0 23 FOX
 5.0 32 (LD ON) XLON
 5.0 21 XTSL
 5.0 20 (CLOSE SW) SPOC
 5.0 19 (OPEN SW) SPOB
 0.0 18 IPTC SW SPOA
 5.0 17 CLKD

5.0 13 XLTO
 0.0 15 DATA
 0.0 14 CNIN
 0.0 13 SEIN
 0.0 12 YSS
 5.0 11 CLOK
 5.0 10 XLAT
 0.0 9 DATA
 5.0 8 XRST
 0.0 7 SENS
 5.0 6 MUTE
 5.0 5 SQCK
 0.0 4 SPSO
 5.0 3 EXCK
 2.5 2 SPSO
 0.0 1 SCOR

IC04
 CXD2508AQ

DIGITAL SIGNAL PROCESSOR

5.0 40 XUSF
 2.3 50 XPCK
 0.0 51 SFS
 2.5 52 RFCK
 0.0 53 VBS
 5.0 54 C2PD
 0.0 55 XPOF
 4.3 56 HMT3
 3.8 57 HMT1
 0.0 58 HMT0
 2.8 59 FSTT
 2.3 60 C4M
 2.4 61 DOUT
 0.0 62 EMPH
 0.0 63 EMPHI
 2.5 64 MFCK

4.7 65 ZERDL
 4.7 66 ZERDR
 0.0 67 DT51
 5.0 68 VCO
 2.5 69 LPNM
 2.5 70 NLPM
 5.0 71 AYD02
 5.0 72 AYD03
 2.5 73 ITA1
 2.5 74 ITA0
 0.0 75 AYSS3
 0.0 76 AYSS2
 2.5 77 MDPM
 2.5 78 RPRN
 0.0 79 DT52

R37 10K
 R38 10K

R39 10K

R40 10K

R41 10K

R42 10K

R43 10K

R44 10K

R45 10K

R46 10K

R47 10K

R48 10K

R49 10K

R50 10K

R51 10K

R52 10K

R53 10K

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R98 10K

R99 10K

R100 10K