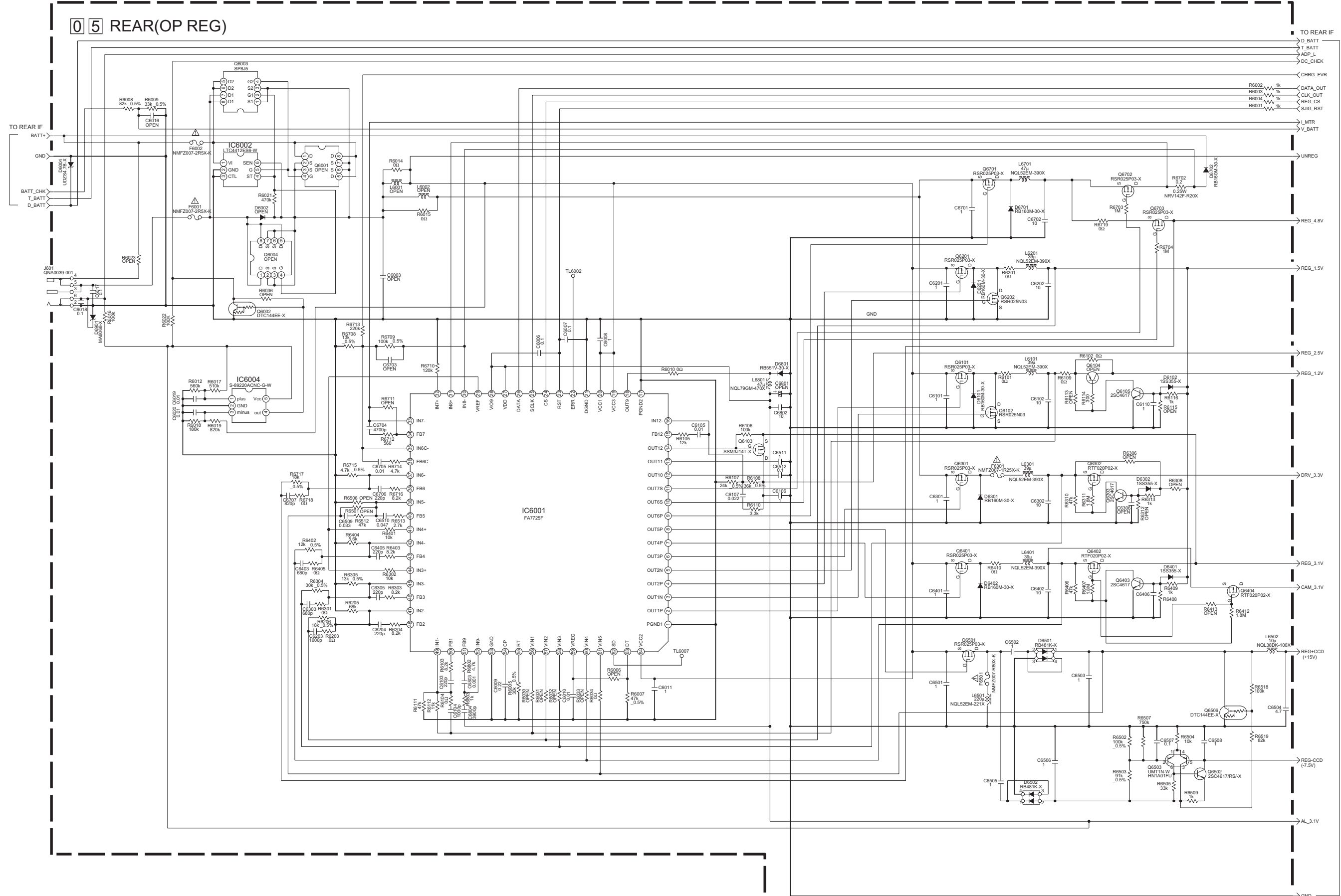


REAR(OP REG) SCHEMATIC DIAGRAM



NOTES: 1. For the destination of each signal and further line connections that are cut off from this diagram, refer to "BOARD INTERCONNECTIONS".
 2. The parts with marked (*) is not used.

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