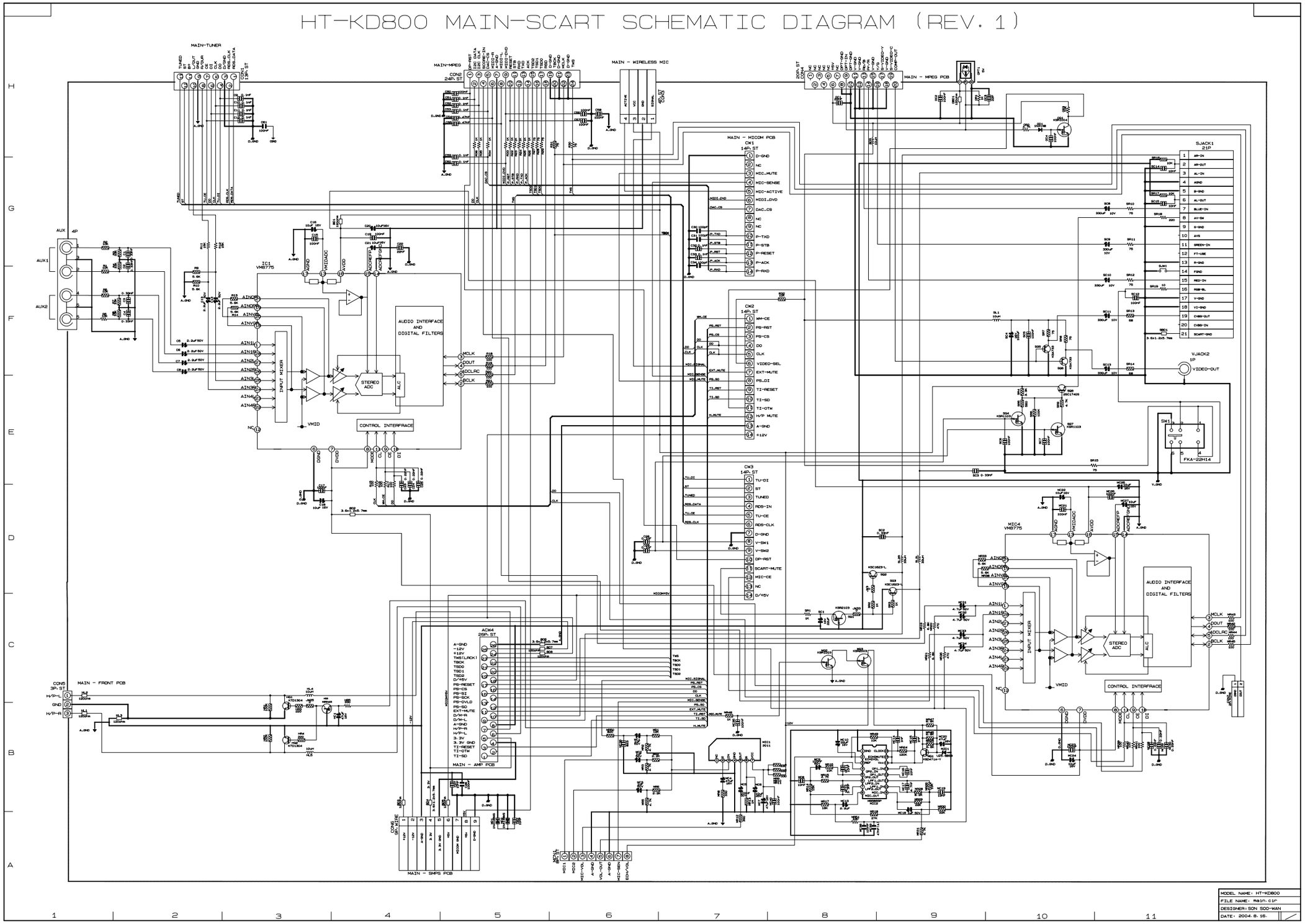
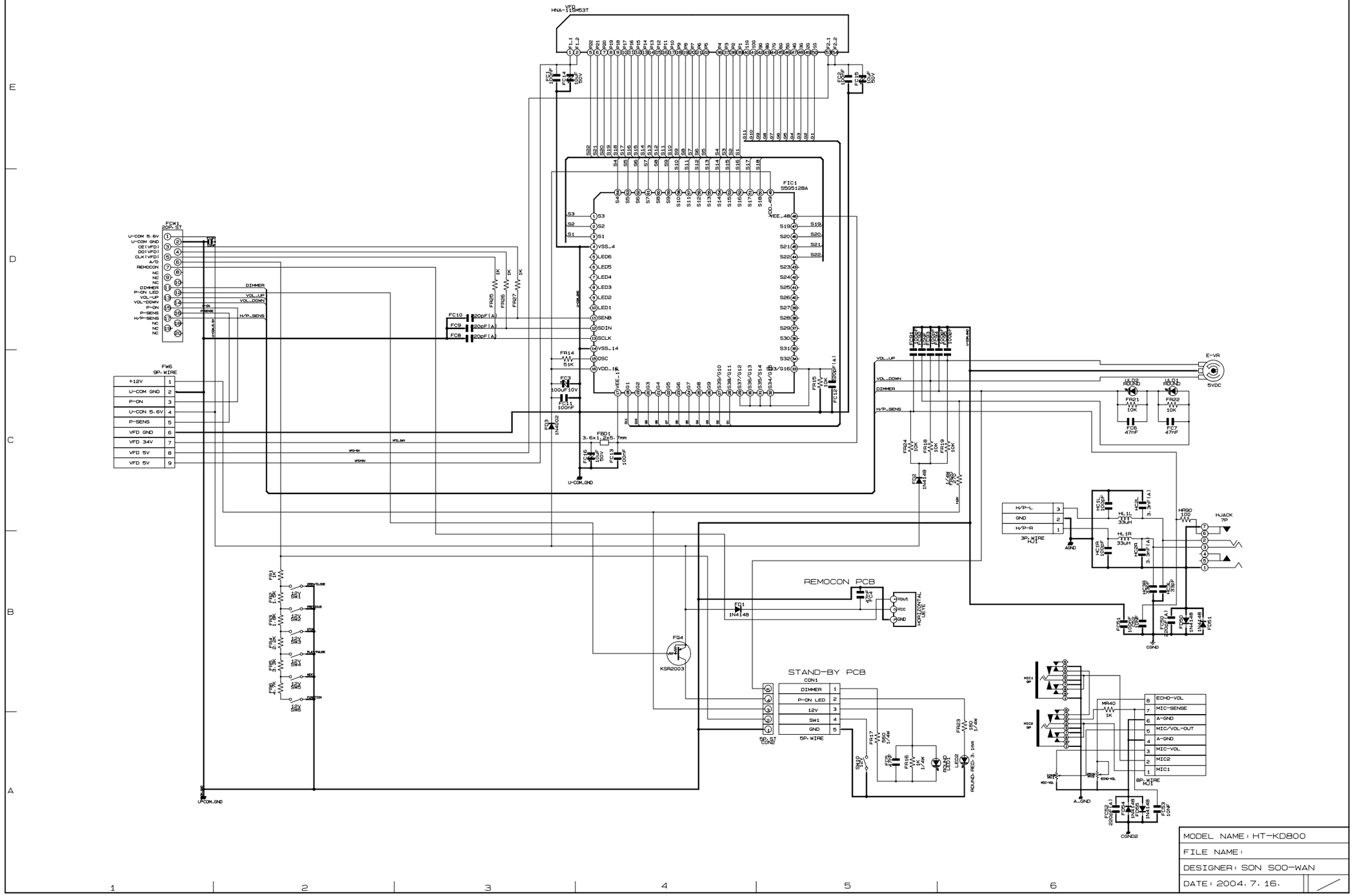
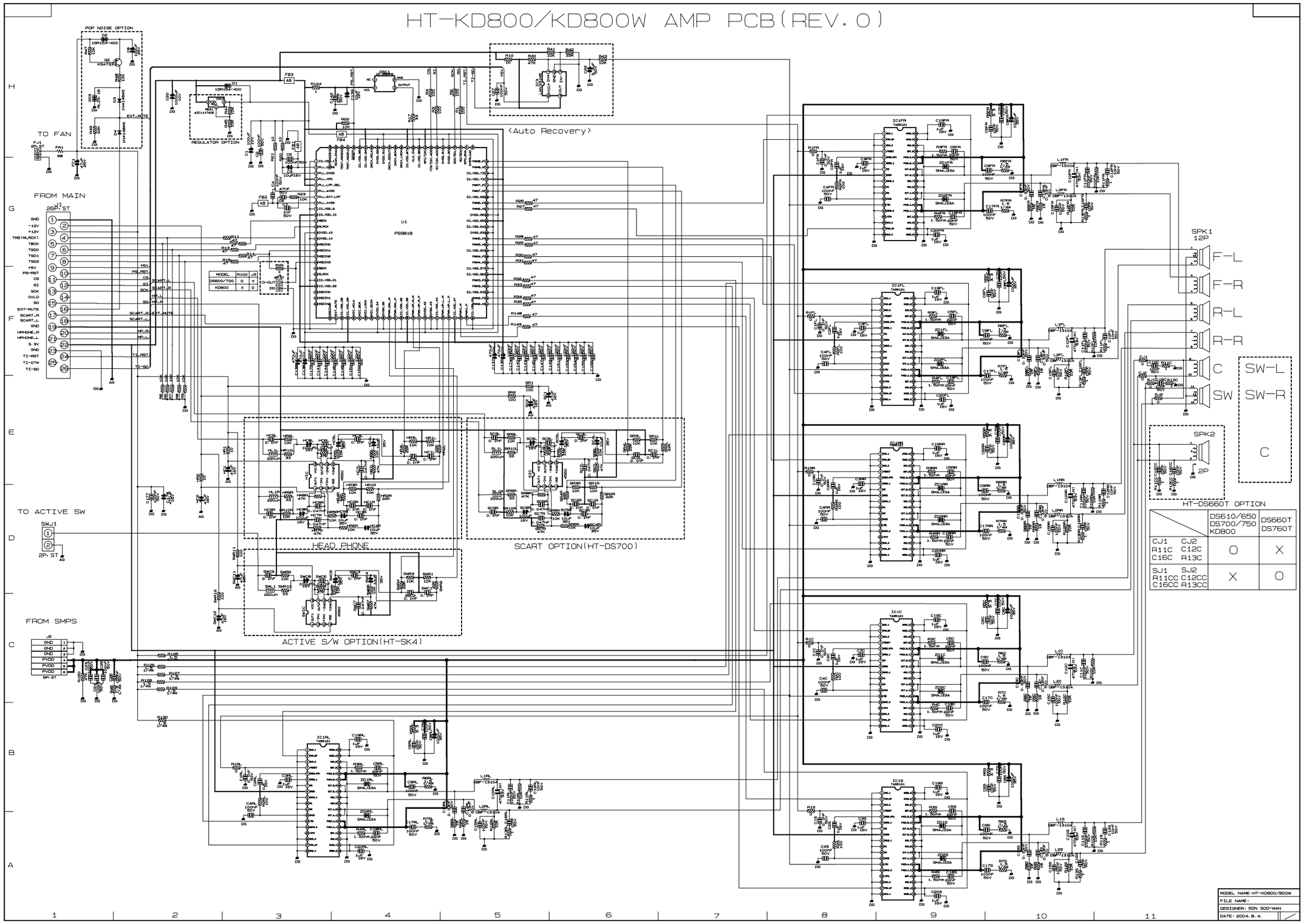
6-1 MAIN

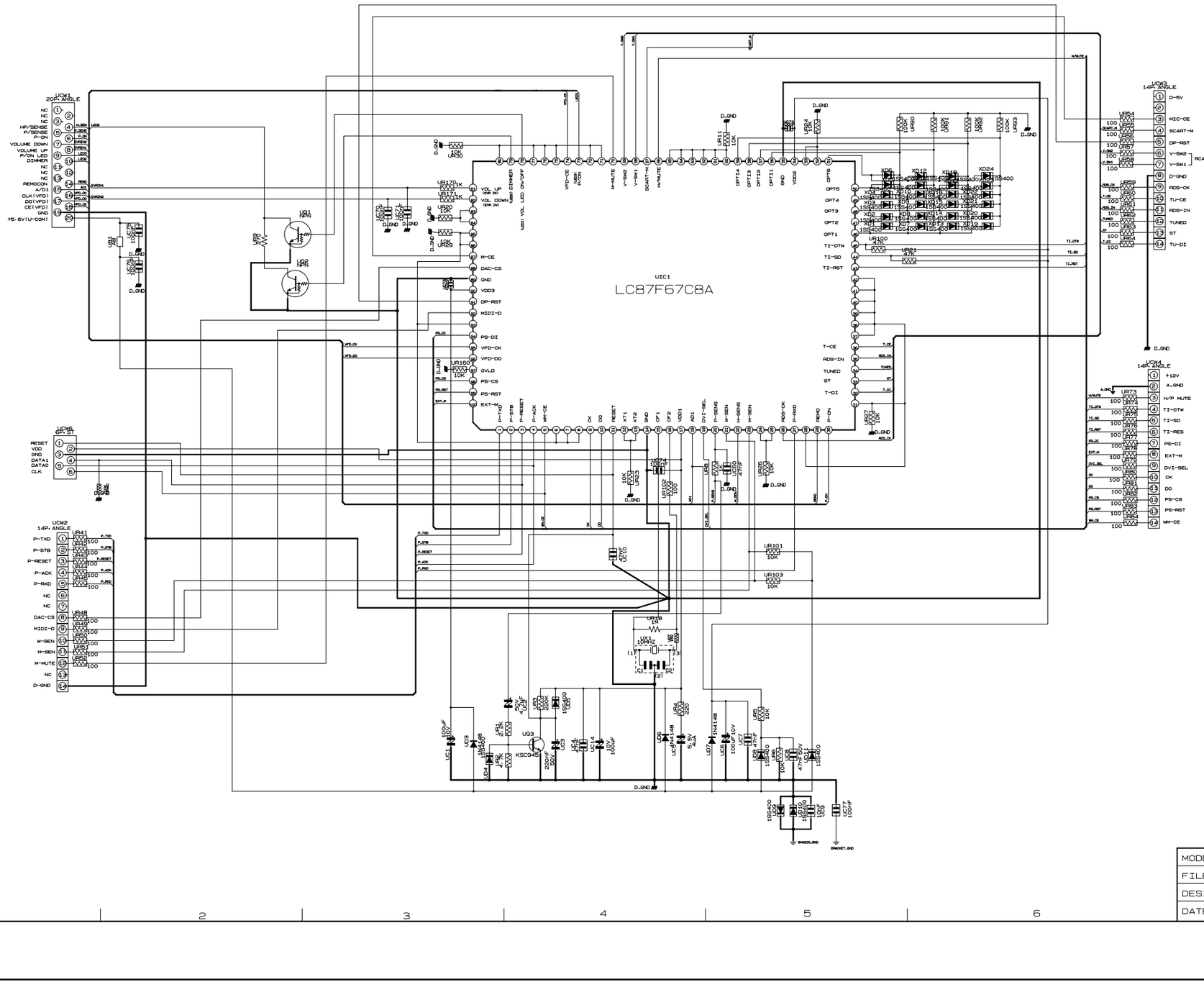

HT-KD800 MAIN-SCART SCHEMATIC DIAGRAM (REV. 1)
MAIN-TUNER
MAIN-MPEG
MAIN - WIRELESS MIC
MAIN - MPEG PCB
MAIN - MICOM PCB
MAIN - FRONT PCB
MAIN - AMP PCB
MAIN - SMPS PCB
AUX 4P
SJACK1 21P
VJACK2 1P
VIDEO-OUT
IC1 VM8775
MIC4 VM8775
AUDIO INTERFACE AND DIGITAL FILTERS
INPUT MIXER
STEREO ADC
ALC
CONTROL INTERFRACE
FKA-22H14
MODEL NAME: HT-KD800
FILE NAME: main.cir
DESIGNER: SON SOO-MAN
DATE: 2004.8.16.

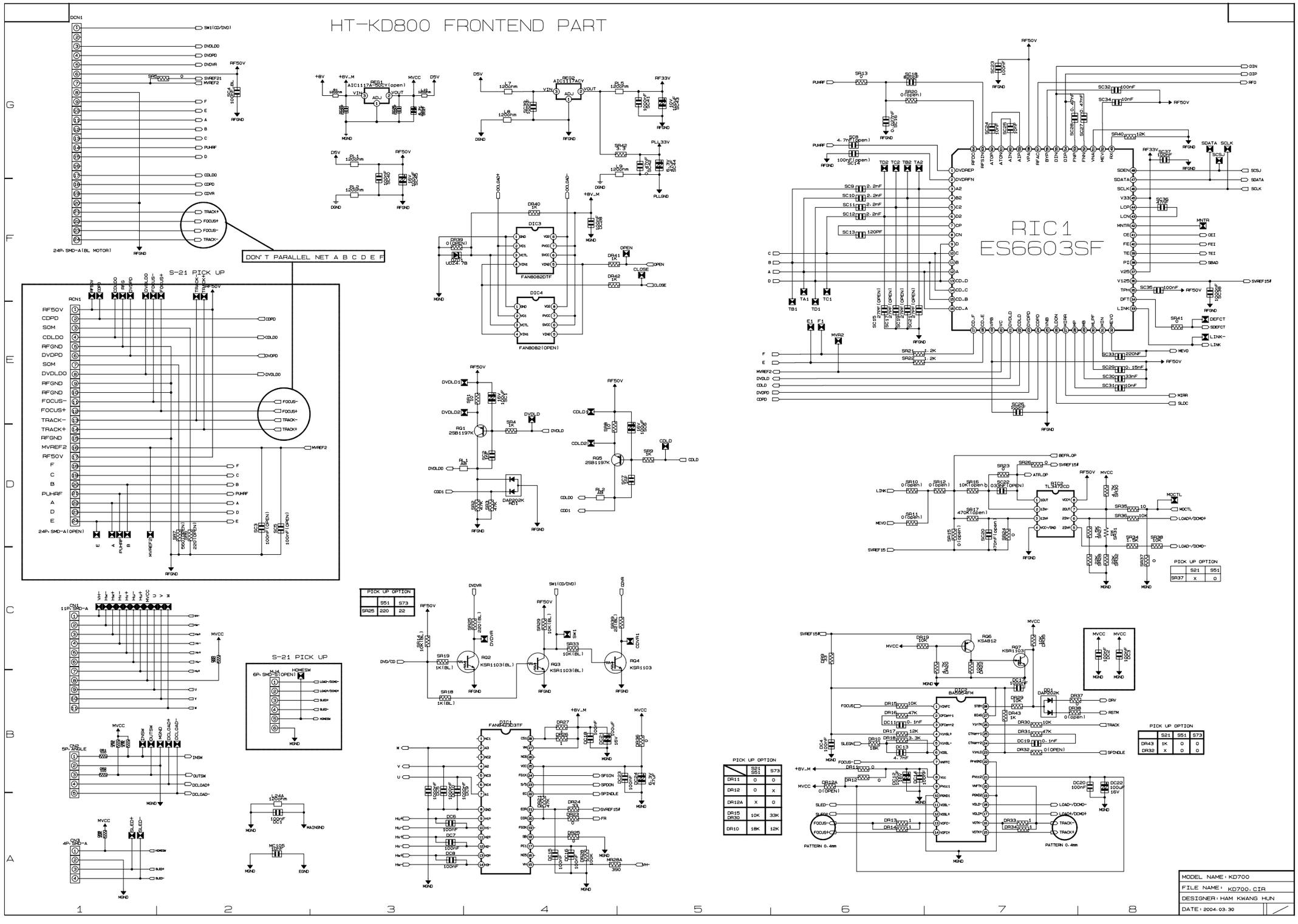
HT-KD800 FRONT SCHEMATIC DIAGRAM (REV.0)

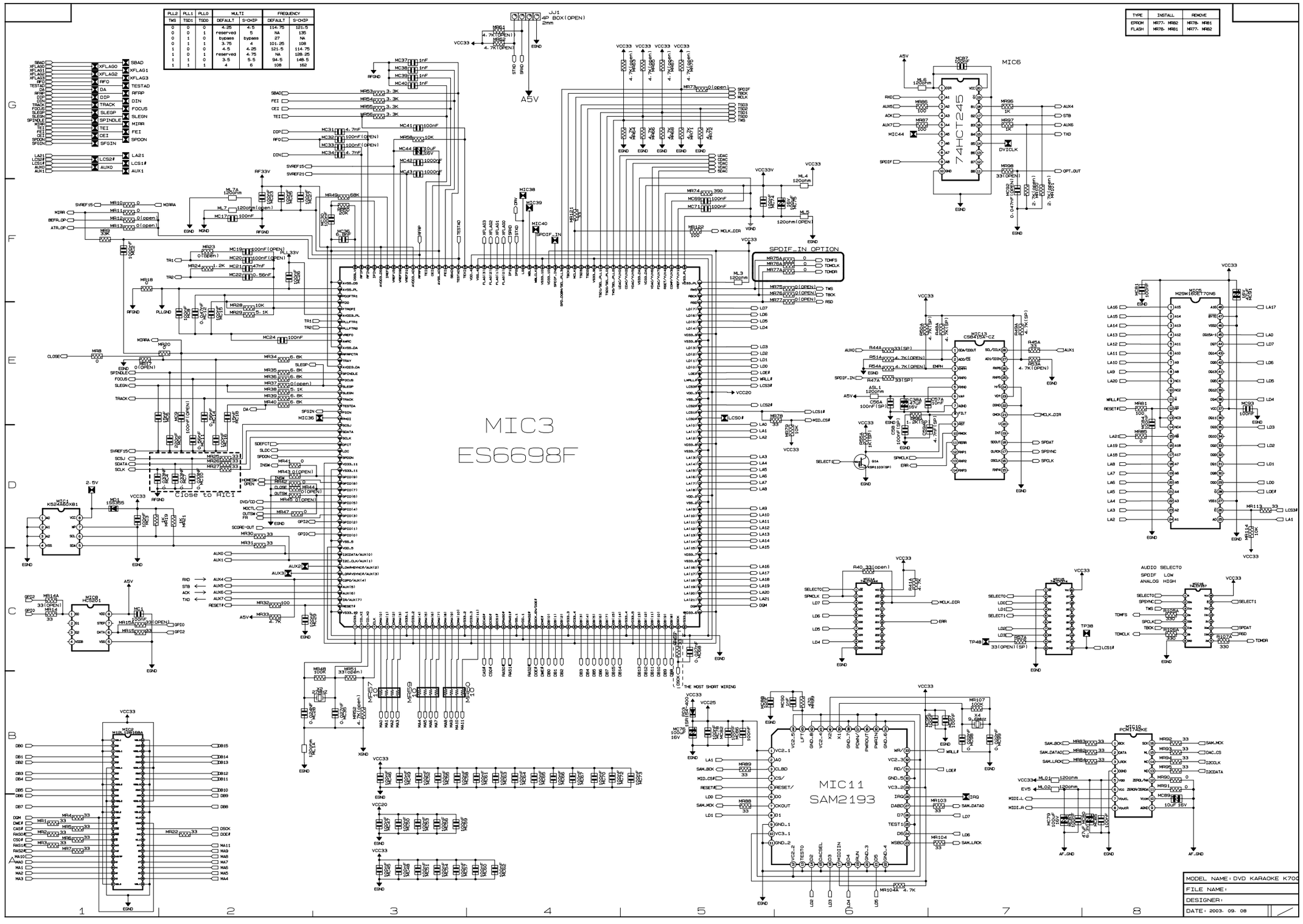



HT-KD800/KD800W AMP PCB(REV.0)
POP NOISE OPTION
REGULATOR OPTION
<Auto Recovery>
TO FAN
FROM MAIN
TO ACTIVE SW
FROM SMPS
HEAD PHONE
SCART OPTION(HT-DS700)
ACTIVE S/W OPTION(HT-SK4)
SPK1
12P
F-L
F-R
R-L
R-R
C
SW
SW-L
SW-R
SPK2
2P
HT-DS660T OPTION
DS610/650
DS700/750
KD800
DS660T
DS760T
CJ1 CJ2
R11C C12C
C16C R13C
O
X
SJ1 SJ2
R11CC C12CC
C16CC R13CC
X
O
MODEL NAME:HT-KD800/800W
FILE NAME:
DESIGNER: SON SOO-WAN
DATE: 2004.8.4.

HT-KD800 MICOM SCHEMATIC DIAGRAM (REV.0)

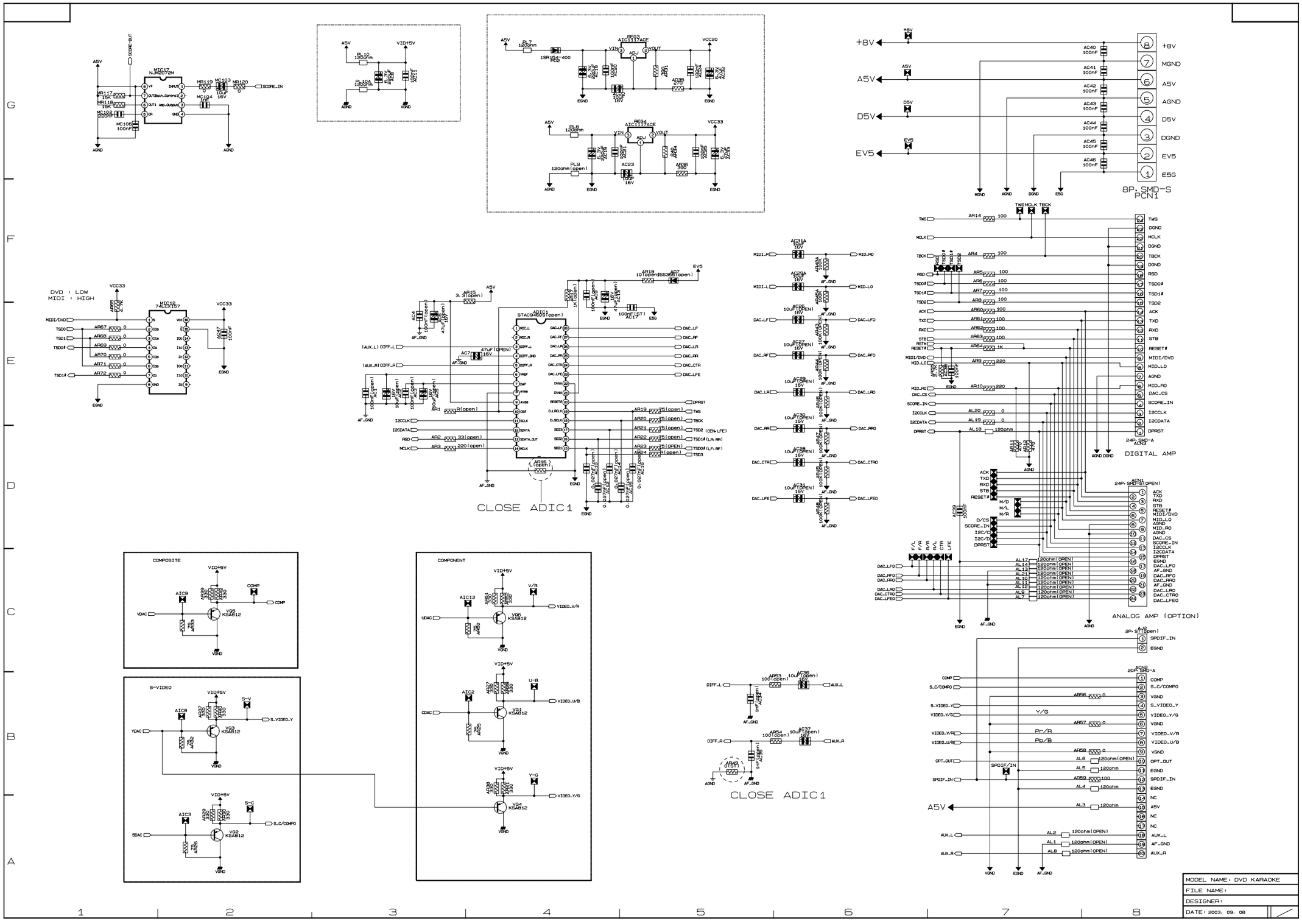



HT-KD800 FRONTEND PART
DON'T PARALLEL NET A B C D E F
S-21 PICK UP
RIC1
ES6603SF
PICK UP OPTION
MODEL NAME : KD700
FILE NAME : KD700.CIR
DESIGNER : HAM KWANG HUN
DATE : 2004.03.30



PLL2	PLL1	PLL0	MULTI		FREQUENCY	
TM5	TSD1	TSD0	DEFAULT	S-CHIP	DEFAULT	S-CHIP
0	0	0	4.25	4.5	114.75	121.5
0	0	1	reserved	5	NA	135
0	1	0	bypass	bypass	27	NA
0	1	1	3.75	4	101.25	108
1	0	0	4.5	4.25	121.5	114.75
1	0	1	reserved	4.75	NA	128.25
1	1	0	3.5	5.5	94.5	148.5
1	1	1	4	6	108	162

TYPE	INSTALL	REMOVE
EPROM	MR77, MR82	MR78, MR81
FLASH	MR78, MR81	MR77, MR82


MODEL NAME : DVD KARAOKE
FILE NAME :
DESIGNER :
DATE : 2003. 09. 08
CLOSE ADIC1
CLOSE ADIC1
DIGITAL AMP
ANALOG AMP (OPTION)
COMPOSITE
S-VIDEO
COMPONENT
DVD : LOW
MIDI : HIGH