

POWER PATH & BAT_LEARN

90 WATT

● **AD_IN Threshold** 2.048Vmax AD_DOCK_IN
 > 17.44V active
 Adapter Inputmax = [0.075V/Rsense(Adm)]*(VCLS_VREF)
 Resense(Adm)=0.0008m
 VCLS=2.534V
 => Resense=4.5A
 => Constant Power = 13" * 4.5 = 58.5W
 => RST to 20V RST to 20V
 Charge Current Ichg = [0.075V/Rsense(CHG)]*(VCLT3_V)
 Resense(CHG)=0.0008m
 VCLT3=3V => Ichg = 2.5A
 VCLT1=5.68V => Ichg = 1.4A
 Vbat = CH1 (Vbat=(VCLT1-1.8V)/0.92)
 VCLT1=5.68V
 => Vbat = 4.2V
 Mode pin: Vmode > 2.8V (bit to LDO pin) => 4 CH1
 2.0 > Vmode > 1.6V (Rating) => 3 CH1s
 0.8 > Vmode (bit to GND) => Loading mode
 VCLT1 0.8V or DCIN = 7V => Charger Disable
 Precharge current=150mA

3/6 change for
 382P/383P only 4/4
 deleted

30 PRECHG

30 CHG_ENH

AC_APP_LDO

BAT_LEARN

MAX8725_LDO

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<< Kennedy_Zhang >>

ASUS		Title :POWER_CHARGER	
ASUSTek COMPUTER INC. MS		Engineer: Gary Lin	
Size	Project Name	Rev	
C		M50S/X55S	
Date: March, September 1A, 2007		Sheet	88 of 88