

SPECIFICATION

Device Name : Power MOSFET



Type Name : FMH23N50ES

Spec. No. : **MS5F7237**

Date : *Oct.-14-2008*

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| | DATE | NAME | APPROVED | Fuji Electric Device Technology Co.,Ltd. | |
|---------|--------------------|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------|------|
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| CHECKED | <i>Oct.-14-'08</i> | <i>O. Tanaka</i> | | | |

Revised Records

| Date | Classification | Index | Content | Drawn | Checked | Checked | Approved |
|-----------------|----------------|-------|---------|---------|-------------|-----------|------------|
| Oct.-14 2008 | enactment | — | ————— | Y. Hara | A. Kitamura | O. Hamada | G. Yada |
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- 1.Scope** This specifies Fuji Power MOSFET FMH23N50ES
- 2.Construction** N-Channel enhancement mode power MOSFET
- 3.Applications** for Switching
- 4.Outview** TO-3P Outview See to 11/15 page

5.Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)

| Description | Symbol | Characteristics | Unit | Remarks |
|---------------------------------------------------------|------------------|-----------------|-------|-----------------------|
| Drain-Source Voltage | V _{DS} | 500 | V | |
| | V _{DSX} | 500 | V | V _{GS} =-30V |
| Continuous Drain Current | I _D | ± 23 | A | |
| Pulsed Drain Current | I _{DP} | ± 92 | A | |
| Gate-Source Voltage | V _{GS} | ± 30 | V | |
| Repetitive and Non-Repetitive Maximum Avalanche Current | I _{AR} | 23 | A | Note *1 |
| Non-Repetitive Maximum Avalanche Energy | E _{AS} | 767.3 | mJ | Note *2 |
| Repetitive Maximum Avalanche Energy | E _{AR} | 31.5 | mJ | Note *3 |
| Peak Diode Recovery dV/dt | dV/dt | 5.4 | kV/μs | Note *4 |
| Peak Diode Recovery -di/dt | -di/dt | 100 | A/μs | Note *5 |
| Maximum Power Dissipation | P _D | 2.50 | W | Ta=25°C |
| | | 315 | | Tc=25°C |
| Operating and Storage Temperature range | T _{ch} | 150 | °C | |
| | T _{stg} | -55 to +150 | °C | |

6.Electrical Characteristics at Tc=25°C (unless otherwise specified)

Static Ratings

| Description | Symbol | Conditions | min. | typ. | max. | Unit |
|----------------------------------|---------------------|------------------------------------------------------------------------|------|-------|-------|------|
| Drain-Source Breakdown Voltage | BV _{DSS} | I _D =250 μA | | | | |
| | | V _{GS} =0V | 500 | - | - | V |
| Gate Threshold Voltage | V _{GS(th)} | I _D =250 μA | | | | |
| | | V _{DS} =V _{GS} | 3.7 | 4.2 | 4.7 | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =500V V _{GS} =0V T _{ch} =25°C | - | - | 25 | μA |
| | | V _{DS} =400V V _{GS} =0V T _{ch} =125°C | - | - | 250 | |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} = ± 30V V _{DS} =0V | - | 10 | 100 | nA |
| Drain-Source On-State Resistance | R _{DS(on)} | I _D =11.5A V _{GS} =10V | - | 0.209 | 0.245 | Ω |

Dynamic Ratings

| Description | Symbol | Conditions | min. | typ. | max. | Unit |
|------------------------------|--------------|------------------------------|------|------|-------|------|
| Forward Transconductance | g_{fs} | $I_D=11.5A$ $V_{DS}=25V$ | 8.5 | 17 | - | S |
| Input Capacitance | C_{iss} | $V_{DS}=25V$ | - | 2700 | 4050 | pF |
| Output Capacitance | C_{oss} | $V_{GS}=0V$ | - | 330 | 495 | |
| Reverse Transfer Capacitance | C_{rss} | $f=1MHz$ | - | 20 | 30 | |
| Turn-On Time | $t_{d(on)}$ | $V_{cc}=300V, V_{GS}=10V$ | - | 42 | 63 | ns |
| | t_r | $I_D=11.5A, R_{GS}=10\Omega$ | - | 36 | 54 | |
| Turn-Off Time | $t_{d(off)}$ | See Fig.3 and Fig.4 | - | 94 | 141 | |
| | t_f | | - | 17 | 25.5 | |
| Total Gate Charge | Q_G | $V_{cc}=250V, I_D=23A$ | - | 73 | 109.5 | nC |
| Gate-Source Charge | Q_{GS} | $V_{GS}=10V$ | - | 24 | 36 | |
| Gate-Drain Charge | Q_{GD} | See Fig.5 | - | 27 | 40.5 | |
| Gate-Drain Crossover Charge | Q_{SW} | | - | 10 | 15 | |

Reverse Diode

| Description | Symbol | Conditions | min. | typ. | max. | Unit |
|--------------------------|----------|----------------------------------------------------------------|------|------|------|---------|
| Avalanche Capability | I_{AV} | $L=1.16mH, T_{ch}=25^\circ C$ See Fig.1 and Fig.2 | 23 | - | - | A |
| Diode Forward On-Voltage | V_{SD} | $I_F=23A$ $V_{GS}=0V, T_{ch}=25^\circ C$ | - | 0.90 | 1.35 | V |
| Reverse Recovery Time | t_{rr} | $I_F=23A, V_{GS}=0V$ $-di/dt=100A/\mu s, T_{ch}=25^\circ C$ | - | 0.5 | - | μs |
| Reverse Recovery Charge | Q_{rr} | See Fig.6 | - | 8.0 | - | μC |

7.Thermal Resistance

| Description | Symbol | min. | typ. | max. | Unit |
|--------------------|----------------|------|------|------|--------------|
| Channel to Case | $R_{th(ch-c)}$ | | | 0.40 | $^\circ C/W$ |
| Channel to Ambient | $R_{th(ch-a)}$ | | | 50.0 | $^\circ C/W$ |

Note *1 : $T_{ch} \leq 150^\circ C$, See Fig.1 and Fig.2

Note *2 : Stating $T_{ch}=25^\circ C$, $I_{AS}=10A$, $L=14.1mH$, $V_{cc}=50V$, $R_G=50\Omega$, See Fig.1 and Fig.2
 E_{AS} limited by maximum channel temperature and avalanche current.

See to 'Avalanche Energy' graph of page 9/15.

Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Thermal impedance' graph of page 9/15.

Note *4 : $I_F \leq I_D$, $-di/dt=100A/\mu s$, $V_{cc} \leq BV_{DSS}$, $T_{ch} \leq 150^\circ C$.

Note *5 : $I_F \leq I_D$, $dv/dt=5.4kV/\mu s$, $V_{cc} \leq BV_{DSS}$, $T_{ch} \leq 150^\circ C$.

8. Reliability test items

All guaranteed values are under the categories of reliability per non-assembled(only MOSFETs). Each categories under the guaranteed reliability conform to EIAJ ED4701/100 method104 standards.

(Test items required without fail
Humidification treatment (85±2°C,65±5%RH,168±24hr)
Heat treatment of soldering (Solder Dipping,260±5°C(265°Cmax.),10±1sec,2 times))

| | Test No. | Test Items | Testing methods and Conditions | Reference Standard | Sampling number | Acceptance number |
|--------------------------------|----------|------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------|-----------------|-------------------|
| Mechanical test methods | 1 | Terminal Strength (Tensile) | Pull force TO-220,TO-220F : 10N TO-3P,TO-3PF,TO-247 : 25N TO-3PL : 45N T-Pack,K-Pack : 10N Force maintaining duration :30±5sec | EIAJ ED4701/400 method 401 | 15 | (0:1) |
| | 2 | Terminal Strength (Bending) | Load force TO-220,TO-220F : 5N TO-3P,TO-3PF,TO-247 : 10N TO-3PL : 15N T-Pack,K-Pack : 5N Number of times :2times(90deg./time) | EIAJ ED4701/400 method 401 | 15 | |
| | 3 | Mounting Strength | Screwing torque value: (M3) TO-220,TO-220F : 40±10N·cm TO-3P,TO-3PF,TO-247 : 50±10N·cm TO-3PL : 70±10N·cm | EIAJ ED4701/400 method 402 | 15 | |
| | 4 | Vibration | frequency : 100Hz to 2kHz Acceleration : 200m/s ² Sweeping time : 4min. 48min. for each X,Y&Z directions. | EIAJ ED4701/400 method 403 | 15 | |
| | 5 | Shock | Peak amplitude: 15km/s ² Duration time : 0.5ms 3times for each X,Y&Z directions. | EIAJ ED4701/400 method 404 | 15 | |
| | 6 | Solderability | Solder temp. : 245±5°C Immersion time : 5±0.5sec Each terminal shall be immersed in the solder bath within 1 to 1.5mm from the body. | ----- | 15 | |
| | 7 | Resistance to Soldering Heat | Solder temp. : 260±5°C Immersion time : 10±1sec Number of times : 1times | EIAJ ED4701/300 method 302 | 15 | |

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| | Test No. | Test Items | Testing methods and Conditions | Reference Standard | Sampling number | Acceptance number |
|------------------------|----------|-------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------|-----------------|-------------------|
| Climatic test methods | 1 | High Temp. Storage | Temperature : 150+0/-5°C Test duration : 1000hr | EIAJ ED4701/200 method 201 | 22 | (0:1) |
| | 2 | Low Temp. Storage | Temperature : -55+5/-0°C Test duration : 1000hr | EIAJ ED4701/200 method 202 | 22 | |
| | 3 | Temperature Humidity Storage | Temperature : 85±2°C Relative humidity : 85±5% Test duration : 1000hr | EIAJ ED4701/100 method 103 | 22 | |
| | 4 | Temperature Humidity BIAS | Temperature : 85±2°C Relative humidity : 85±5% Bias Voltage : $V_{DS(max)} \times 0.8$ Test duration : 1000hr | EIAJ ED4701/100 method 103 | 22 | |
| | 5 | Unsaturated Pressurized Vapor | Temperature : 130±2°C Relative humidity : 85±5% Vapor pressure : 230kPa Test duration : 48hr | EIAJ ED4701/100 method 103 | 22 | |
| | 6 | Temperature Cycle | High temp.side : 150±5°C/30min. Low temp.side : -55±5°C/30min. RT : 5°C ~ 35°C/5min. Number of cycles : 100cycles | EIAJ ED4701/100 method 105 | 22 | |
| | 7 | Thermal Shock | Fluid : pure water(running water) High temp.side : 100+0/-5°C Low temp.side : 0+5/-0°C Duration time : HT 5min,LT 5min Number of cycles : 100cycles | EIAJ ED4701/300 method 307 | 22 | |
| Endurance test methods | 8 | Intermittent Operating Life | $\Delta T_c=90$ degree $T_{ch} \leq T_{ch(max)}$ Test duration : 3000 cycle | EIAJ ED4701/100 method 106 | 22 | (0:1) |
| | 9 | HTRB (Gate-source) | Temperature : $T_{ch}=150+0/-5^\circ C$ Bias Voltage : $+V_{GS(max)}$ Test duration : 1000hr | EIAJ ED4701/100 method 101 | 22 | |
| | 10 | HTRB (Drain-Source) | Temperature : $T_{ch}=150+0/-5^\circ C$ Bias Voltage : $V_{DS(max)}$ Test duration : 1000hr | EIAJ ED4701/100 method 101 | 22 | |

Failure Criteria

| Item | Symbols | Failure Criteria | | Unit | |
|----------------------------|-------------------------------------------|------------------|-------------------------|-------|----------|
| | | Lower Limit | Upper Limit | | |
| Electrical Characteristics | Breakdown Voltage | BVDSS | LSL | ----- | V |
| | Zero gate Voltage Drain-Source Current | IDSS | ----- | USL | A |
| | Gate-Source Leakage Current | IGSS | ----- | USL | A |
| | Gate Threshold Voltage | VGS(th) | LSL | USL | V |
| | Drain-Source on-state Resistance | RDS(on) | ----- | USL | Ω |
| | Forward Transconductance | gfs | LSL | ----- | S |
| | Diode forward on-Voltage | VSD | ----- | USL | V |
| Outview | Marking Soldering and other damages | ----- | With eyes or Microscope | | ----- |

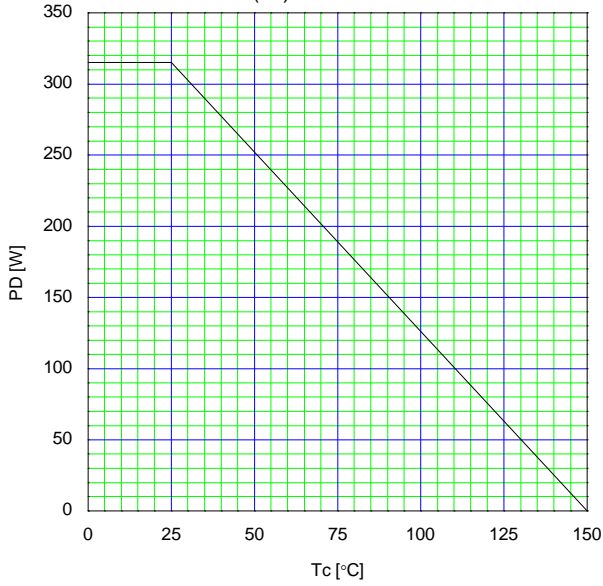
* LSL : Lower Specification Limit

* USL : Upper Specification Limit

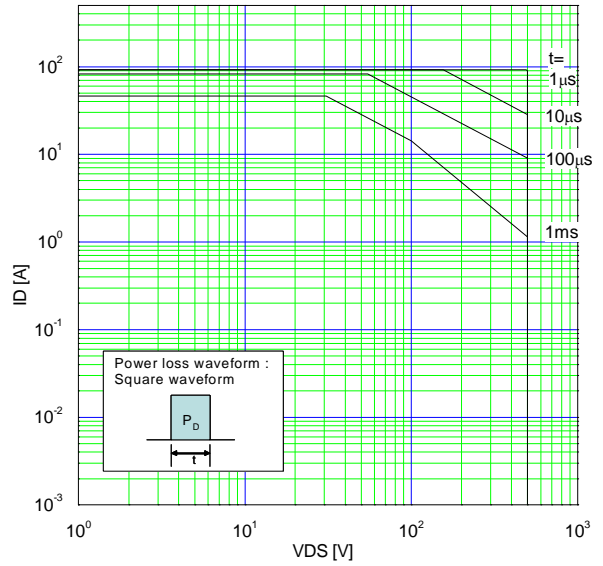
* Before any of electrical characteristics measure, all testing related to the humidity have conducted after drying the package surface for more than an hour at 150°C.

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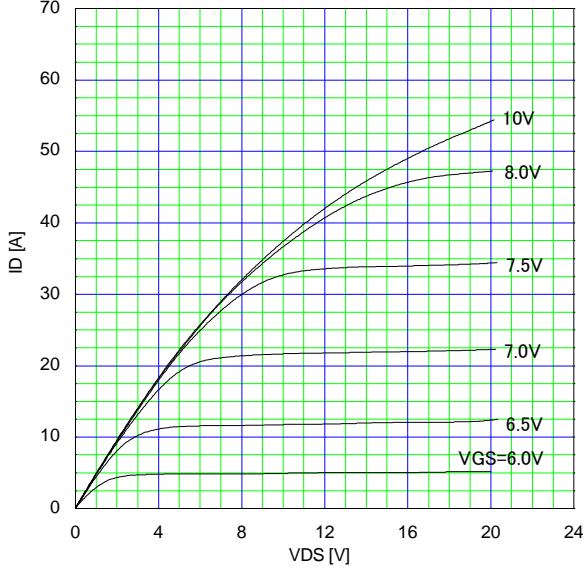
Allowable Power Dissipation
 $P_D=f(T_c)$



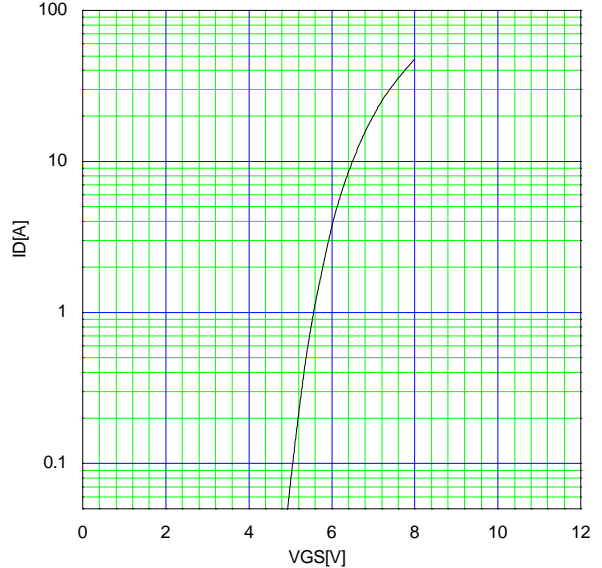
Safe Operating Area
 $I_D=f(V_{DS}): \text{Duty}=0(\text{Single pulse}), T_c=25^\circ\text{C}$



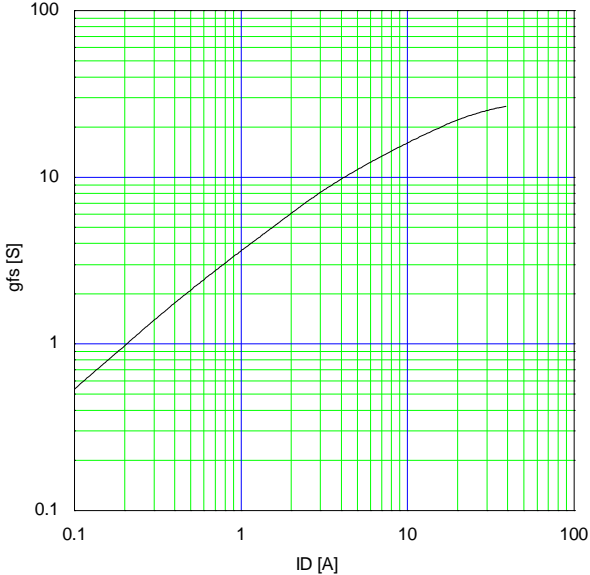
Typical Output Characteristics
 $I_D=f(V_{DS}): 80 \mu\text{s pulse test}, T_{ch}=25^\circ\text{C}$



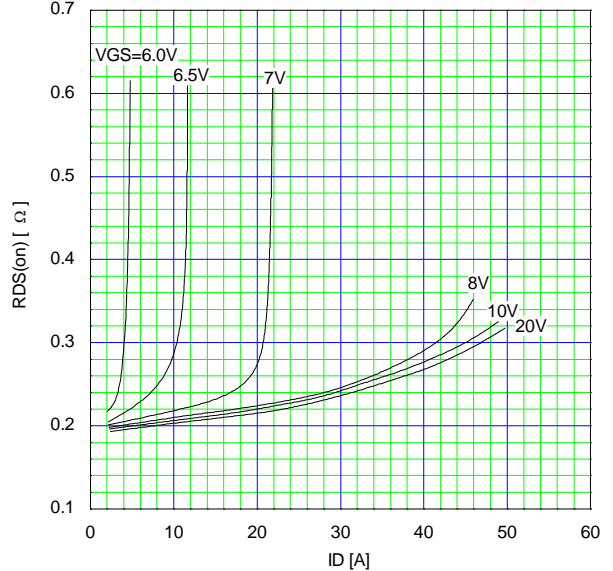
Typical Transfer Characteristic
 $I_D=f(V_{GS}): 80 \mu\text{s pulse test}, V_{DS}=25\text{V}, T_{ch}=25^\circ\text{C}$



Typical Transconductance
 $g_{fs}=f(I_D): 80 \mu\text{s pulse test}, V_{DS}=25\text{V}, T_{ch}=25^\circ\text{C}$

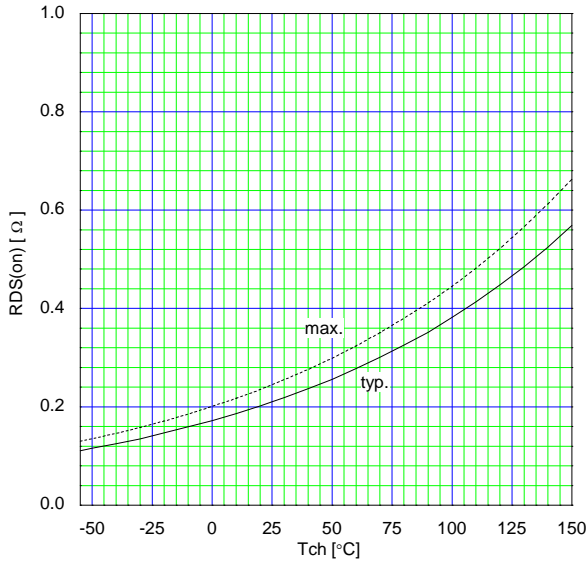


Typical Drain-Source on-state Resistance
 $R_{DS(on)}=f(I_D): 80 \mu\text{s pulse test}, T_{ch}=25^\circ\text{C}$

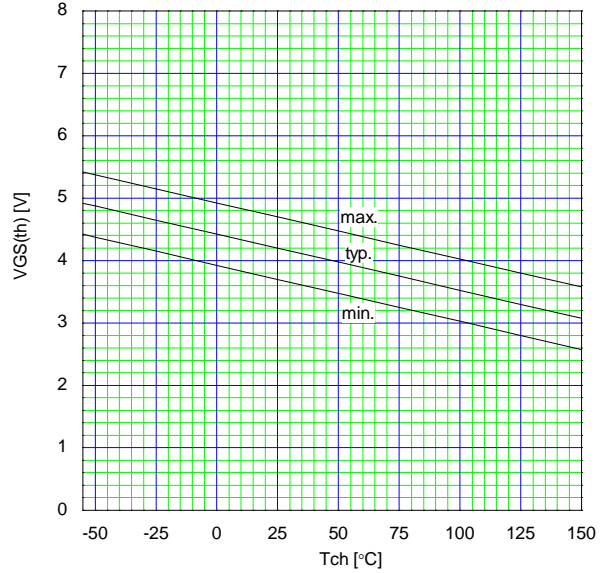


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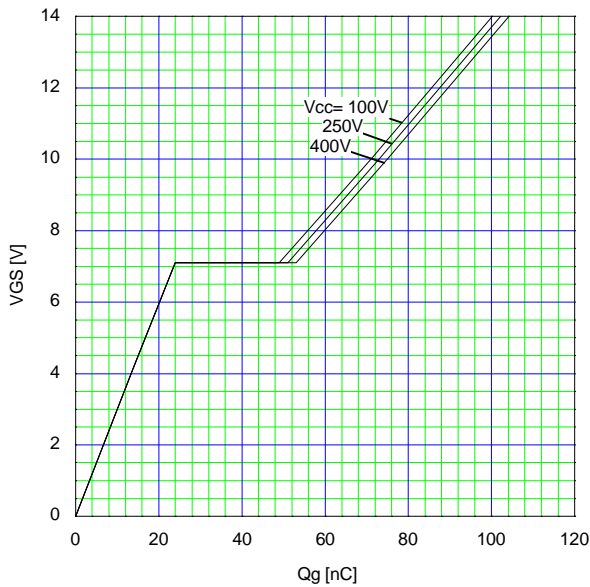
Drain-Source On-state Resistance
 $R_{DS(on)}=f(T_{ch}):I_D=11.5A, V_{GS}=10V$



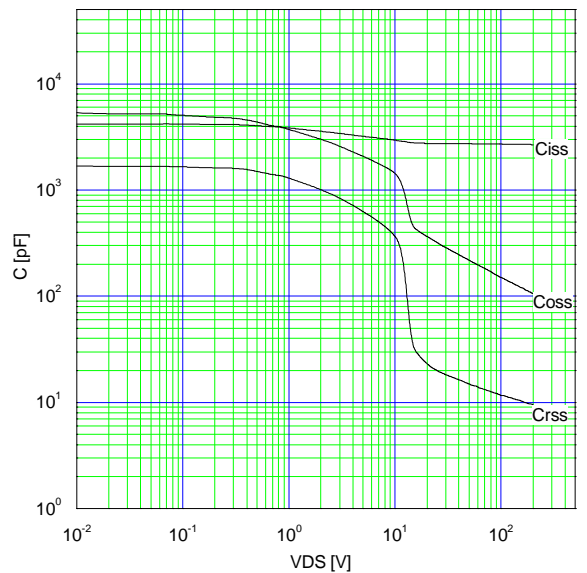
Gate Threshold Voltage vs. T_{ch}
 $V_{GS(th)}=f(T_{ch}):V_{DS}=V_{GS}, I_D=250\mu A$



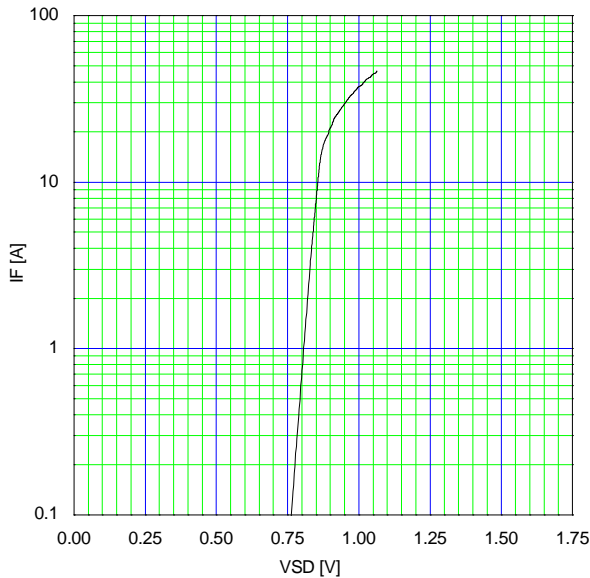
Typical Gate Charge Characteristics
 $V_{GS}=f(Q_g):I_D=23A, T_{ch}=25^{\circ}C$



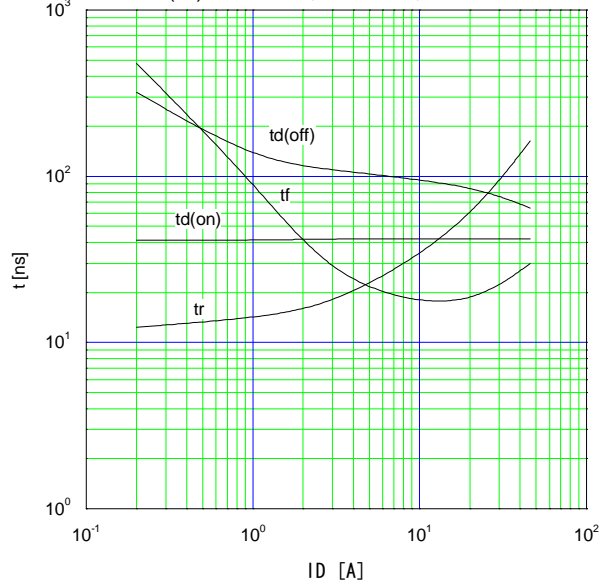
Typical Capacitance
 $C=f(V_{DS}):V_{GS}=0V, f=1MHz$



Typical Forward Characteristics of Reverse Diode
 $I_F=f(V_{SD}):80\mu s$ pulse test, $T_{ch}=25^{\circ}C$

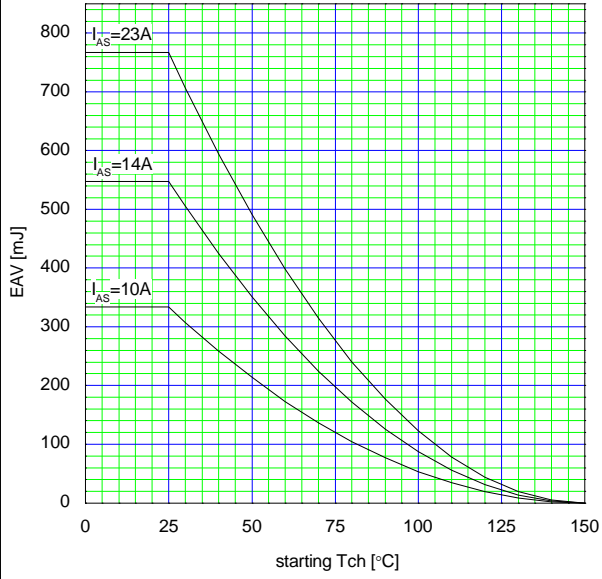


Typical Switching Characteristics vs. I_D
 $t=f(I_D):V_{CC}=300V, V_{GS}=10V, R_G=8.2\Omega$



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Maximum Avalanche Energy vs. starting Tch
 $E(AV)=f(\text{starting Tch}):V_{cc}=50V, I(AV)\leq 23A$



Maximum Transient Thermal Impedance
 $Z_{th(ch-c)}=f(t):D=0$

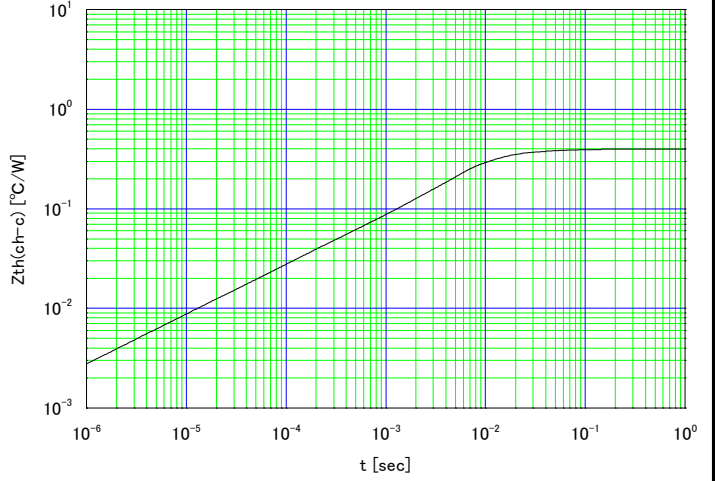


Fig.1 Avalanche Test circuit

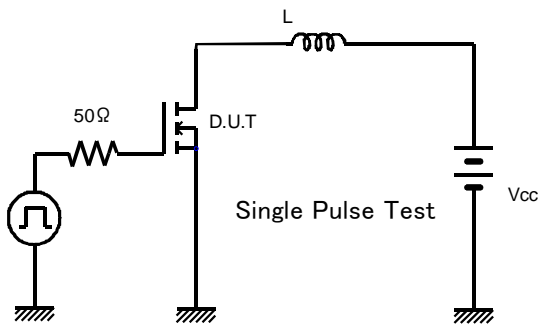


Fig.2 Operating waveforms of Avalanche Test

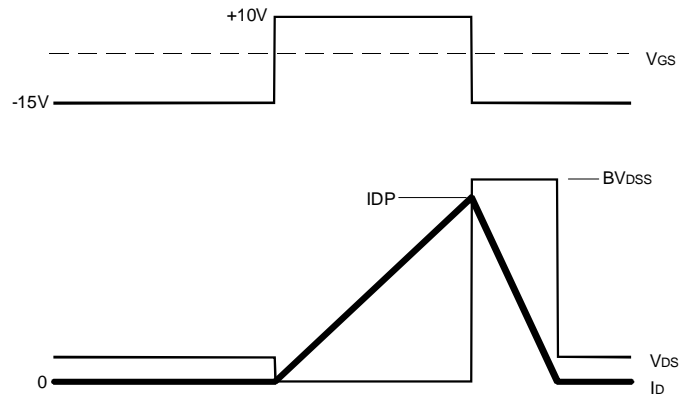


Fig.3 Switching Test circuit

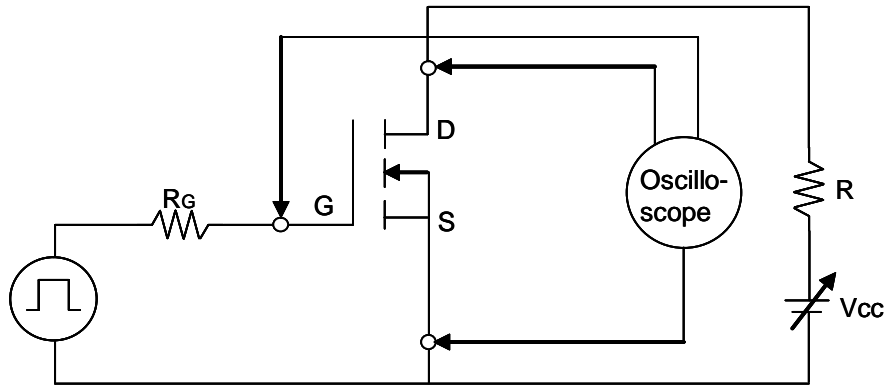


Fig.4 Operating waveform of Switching Test

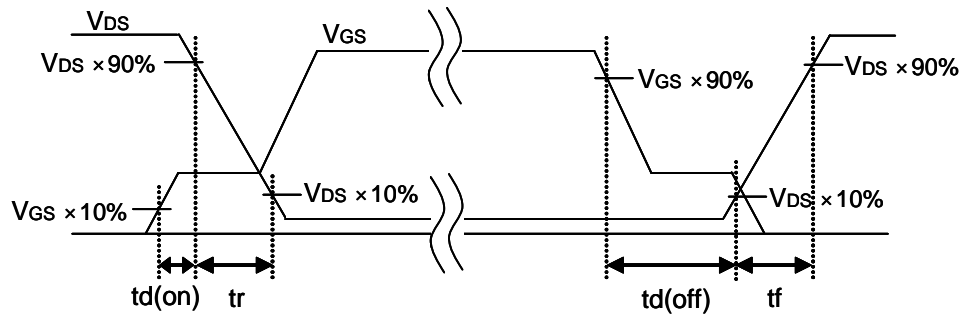


Fig.5 Operating waveform of Gate charge Test

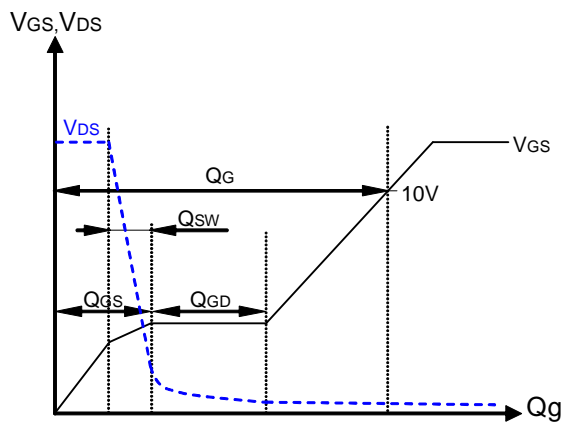
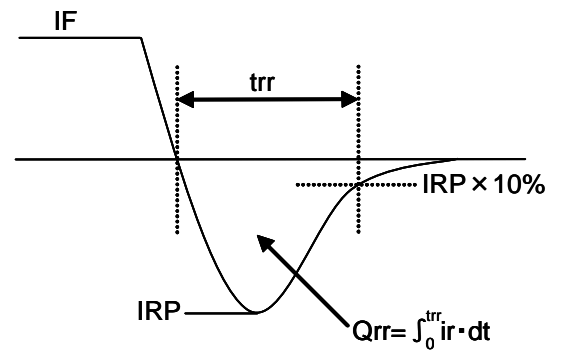
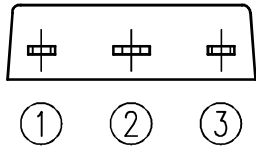
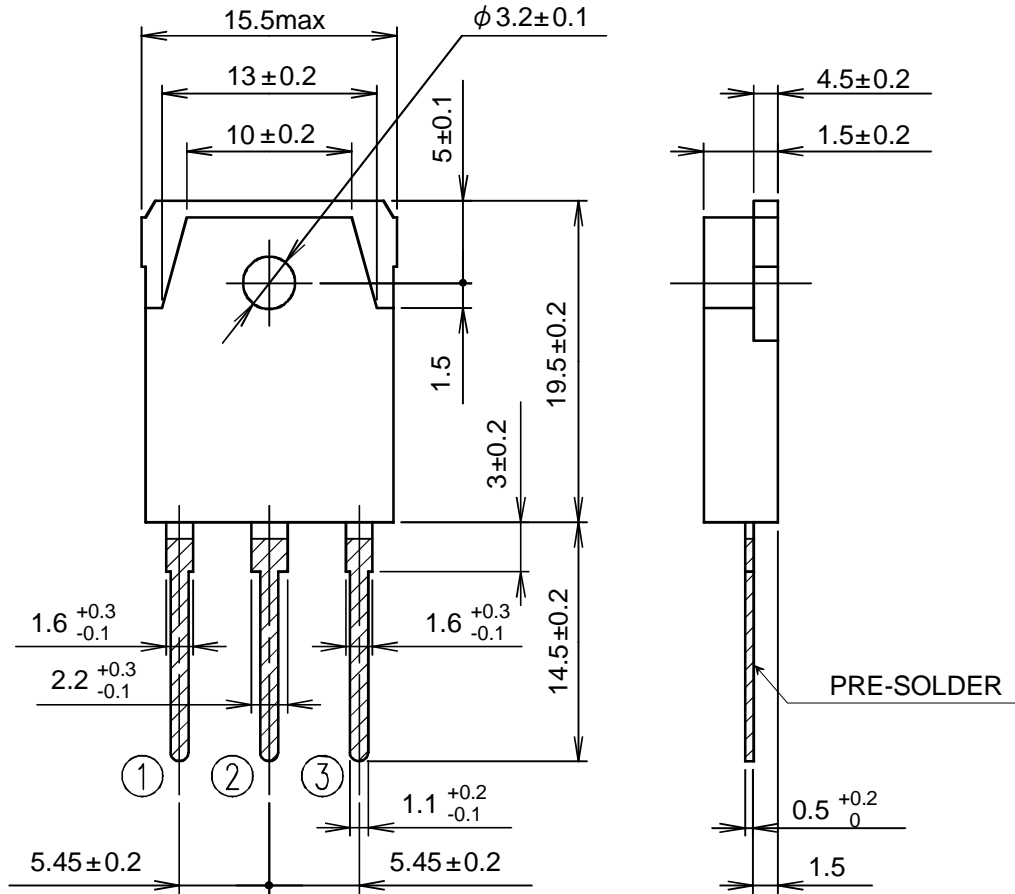


Fig.6 Operating waveform of Body diode Recovery Test



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Outview: TO-3P Package



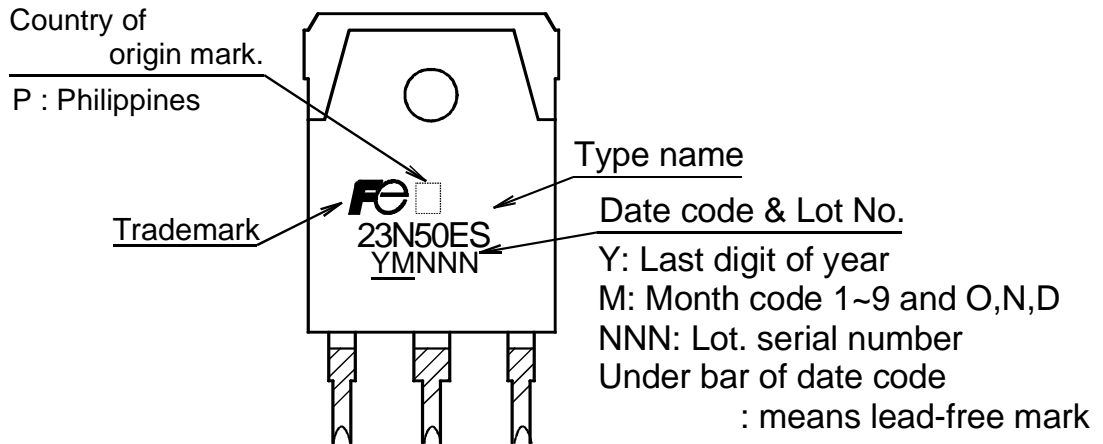
CONNECTION

- ① GATE
- ② DRAIN
- ③ SOURCE

JEDEC : TO-3P

DIMENSIONS ARE IN MILLIMETERS.

Marking



* The font (font type,size) and the trademark-size might be actually different.

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9. Cautions

- Although Fuji Electric is continually improving product quality and reliability, a small percentage of semiconductor products may become faulty. When using Fuji Electric semiconductor products in your equipment, you are requested to take adequate safety measures to prevent the equipment from causing physical injury, fire, or other problem in case any of the products fail. It is recommended to make your design fail-safe, flame retardant, and free of malfunction.
- The products described in this Specification are intended for use in the following electronic and electrical equipment which has normal reliability requirements.
 - Computers · OA equipment · Communications equipment(Terminal devices)
 - Machine tools · AV equipment · Measurement equipment
 - Personal equipment · Industrial robots · Electrical home appliances etc.
- The products described in this Specification are not designed or manufactured to be used in equipment or systems used under life-threatening situations. If you are considering using these products in the equipment listed below, first check the system construction and required reliability, and take adequate safety measures such as a backup system to prevent the equipment from malfunctioning.
 - Backbone network equipment · Transportation equipment (automobiles, trains, ships, etc.)
 - Traffic-signal control equipment · Gas alarms, leakage gas auto breakers
 - Submarine repeater equipment · Burglar alarms, fire alarms, emergency equipment
 - Medical equipment · Nuclear control equipment etc.
- Do not use the products in this Specification for equipment requiring strict reliability such as (but not limited to):
 - Aerospace equipment · Aeronautical equipment

10. Warnings

- The MOSFETs should be used in products within their absolute maximum rating (voltage, current, temperature, etc.).
- The MOSFETs may be destroyed if used beyond the rating.
- We only guarantee the non-repetitive and repetitive Avalanche capability and not for the continuous Avalanche capability which can be assumed as abnormal condition. Please note the device may be destructed from the Avalanche over the specified maximum rating.
- The equipment containing MOSFETs should have adequate fuses or circuit breakers to prevent the equipment from causing secondary destruction.
- Use the MOSFETs within their reliability and lifetime under certain environments or conditions. The MOSFETs may fail before the target lifetime of your products if used under certain reliability conditions.
- You must careful handling of MOSFETs for ESD damage is an important consideration.
- When handling MOSFETs, hold them by the case (package) and don't touch the leads and terminals.
- It is recommended that any handling of MOSFETs is done while used electrically conductive floor and tablemats that are grounded.

- Before touching a MOSFET's terminal, discharge any static electricity from your body and clothes by grounding out through a high impedance resistor (about 1MΩ)
- When soldering, in order to protect the MOSFETs from static electricity, ground the soldering iron or soldering bath through a low impedance resistor.
- You must design the MOSFETs to be operated within the specified maximum ratings(voltage, current, temperature, etc.) to prevent possible failure or destruction of devices.
- Consider the possible temperature rise not only for the channel and case, but also for the outer leads.
- Do not directly touch the leads or package of the MOSFETs while power is supplied or during operation ,to avoid electric shock and burns.
- The MOSFETs are made of incombustible material. However, if a MOSFET fails, it may emit smoke or flame. Also, operating the MOSFETs near any flammable place or material may cause the MOSFETs to emit smoke or flame in case the MOSFETs become even hotter during operation. Design the arrangement to prevent the spread of fire.
- The MOSFETs should not used in an environment in the presence of acid, organic matter, or corrosive gas(hydrogen sulfide, sulfurous acid gas etc.)
- The MOSFETs should not used in an irradiated field since they are not radiation-proof.

Installation

- Soldering involves temperatures which exceed the device storage temperature rating. To avoid device damage and to ensure reliability, observe the following guidelines from the quality assurance standard.

Solder temperature and duration (through-hole package)

| Solder temperature | Duration |
|--------------------|-----------------|
| 260±5 °C | 10±1 seconds |
| 350±10 °C | 3.5±0.5 seconds |

- The immersion depth of the lead should basically be up to the lead stopper and the distance should be a maximum of 1.5mm from the device.
- When flow-soldering, take care to avoid immersing the package in the solder bath.
- Refer to the following torque reference When mounting the device on a heat sink. Excess torque applied to the mounting screw causes damage to the device and weak torque will increase the thermal resistance, both of which conditions may destroy the device.

Table 1: Recommended tightening torques.

| Package style | Screw | Tightening torques | Note |
|---------------------------|-------|--------------------|-----------------------------------------------------------------------------|
| TO-220 TO-220F | M3 | 30 – 50 Ncm | flatness : < ±30μm roughness : <10μm Plane off the edges : C<1.0mm |
| TO-3P TO-3PF TO-247 | M3 | 40 – 60 Ncm | |
| TO-3PL | M3 | 60 –80 Ncm | |

- The heat sink should have a flatness within $\pm 30\mu\text{m}$ and roughness within $10\mu\text{m}$. Also, keep the tightening torque within the limits of this specification.
- Improper handling may cause isolation breakdown leading to a critical accident.
ex.) Over plane off the edges of screw hole. (We recommend plane off the edge is $C < 1.0\text{mm}$)
- We recommend the use of thermal compound to optimize the efficiency of heat radiation. It is important to evenly apply the compound and to eliminate any air voids.

Storage

- The MOSFETs must be stored at a standard temperature of 5 to 35°C and relative humidity of 45 to 75%.
- If the storage area is very dry, a humidifier may be required. In such a case, use only deionized water or boiled water, since the chlorine in tap water may corrode the leads.
- The MOSFETs should not be subjected to rapid changes in temperature to avoid condensation on the surface of the MOSFETs. Therefore store the MOSFETs in a place where the temperature is steady.
- The MOSFETs should not be stored on top of each other, since this may cause excessive external force on the case.
- The MOSFETs should be stored with the lead terminals remaining unprocessed. Rust may cause presoldered connections to go fail during later processing.
- The MOSFETs should be stored in antistatic containers or shipping bags.

11. Compliance with pertaining to restricted substances

11-1) Compliance with the RoHS Regulations and Exemptions

This product will be fully compliant with the RoHS directive (Directive 2002/95/EC of the European Parliament and the Council of 27 January 2003).

Five out of six substances below which are regulated by the RoHS directive in Europe are not included in this product. The exception is only lead.

The RoHS directive has some exemptions. The following relates to this product :

Lead in high melting temperature type solders (Sn-Pb solder alloy which contains more than 85%)

This product is used to the high melting temperature type solders (Sn-Pb solders) for die-bonding. Moreover, the terminals used lead-free solder.

* The six substances regulated by the RoHS Directive are:

Lead, Mercury, Hexavalent chromium, Cadmium, PBB (polybrominated biphenyls),
PBDE (polybrominated diphenyl ethers).

The maximum concentration value of the six substances in this product conforms to the Commission decision 2005/618/EC of EU of 18 August 2005.

11-2) Compliance with the class-1 ODS and class-2 ODS. (ODS: Ozone-Depleting Substances)

This product does not contain and used the "Law concerning the Protection of the Ozone Layer through the Control of Specified Substances and Other Measures (JAPAN)", and the Montreal Protocol.

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