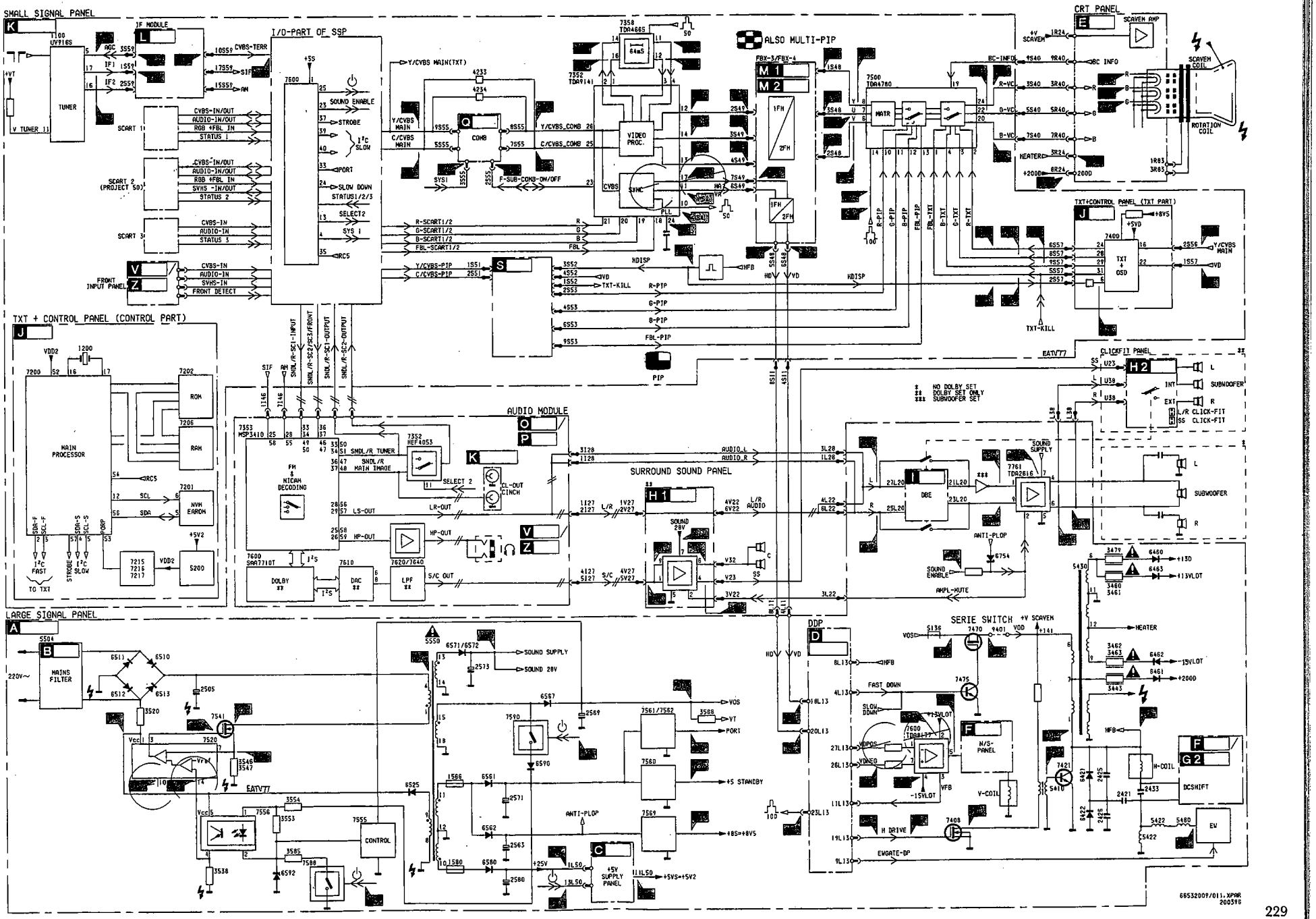
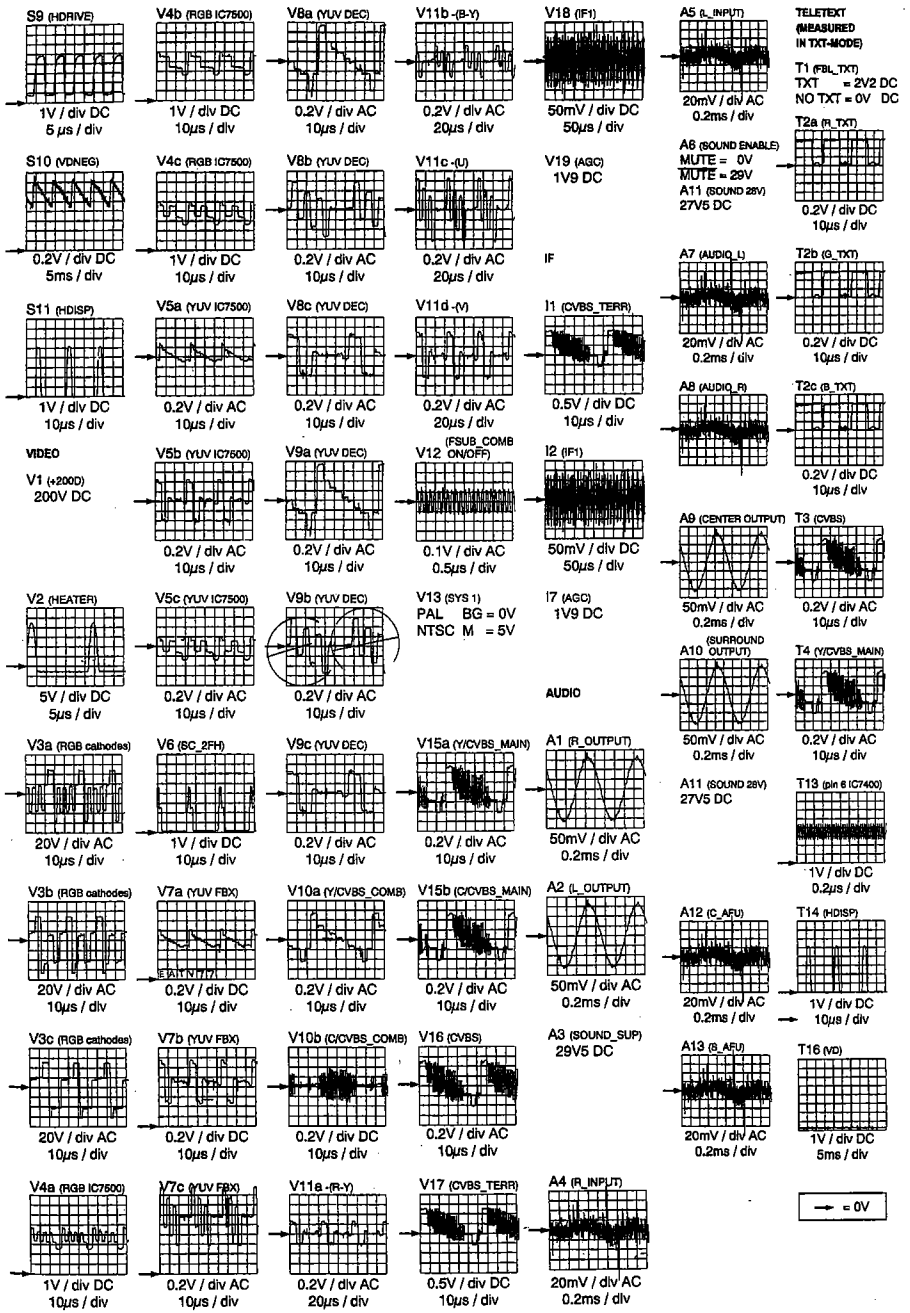
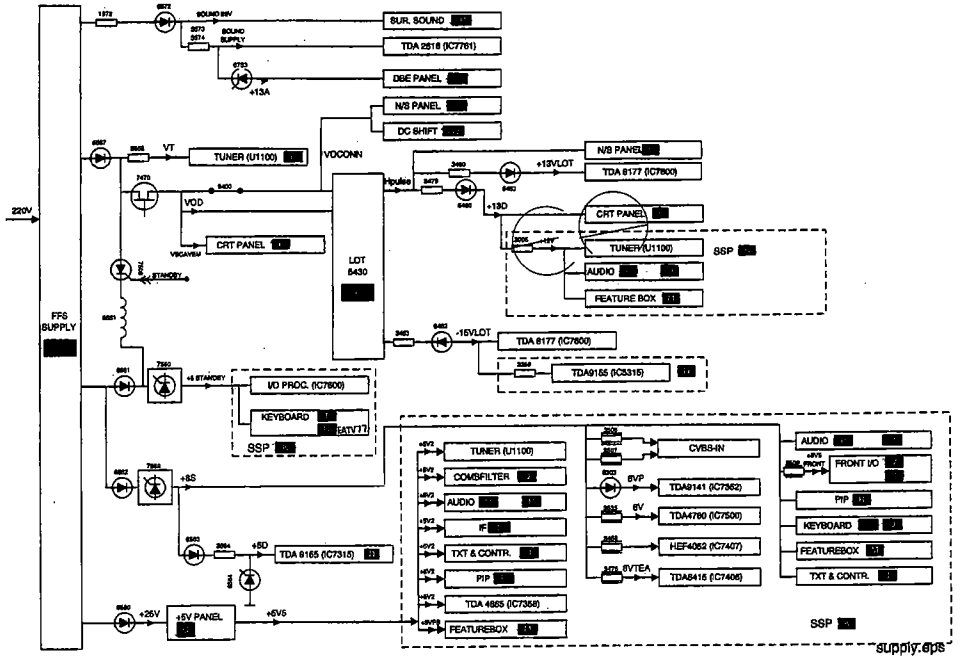
Block diagram

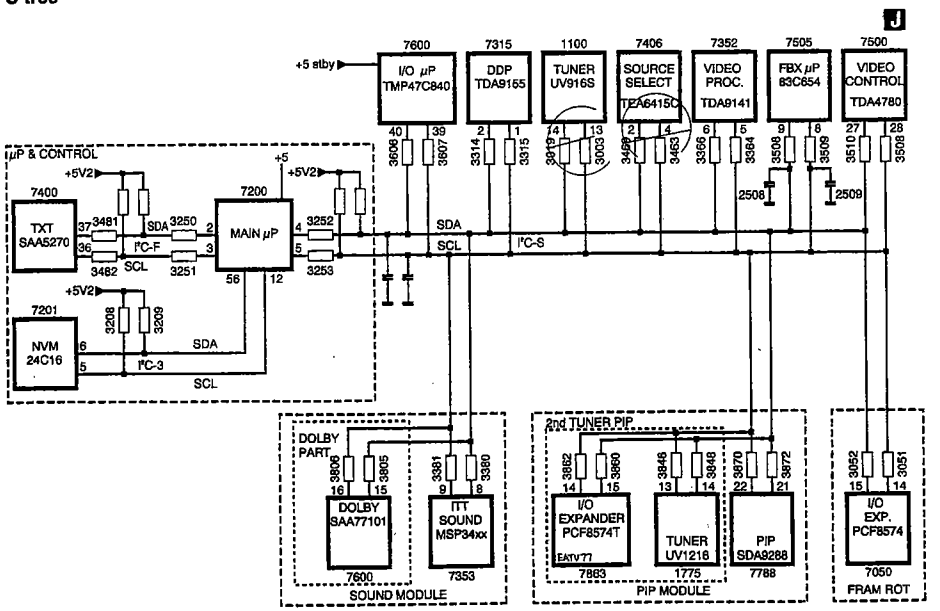




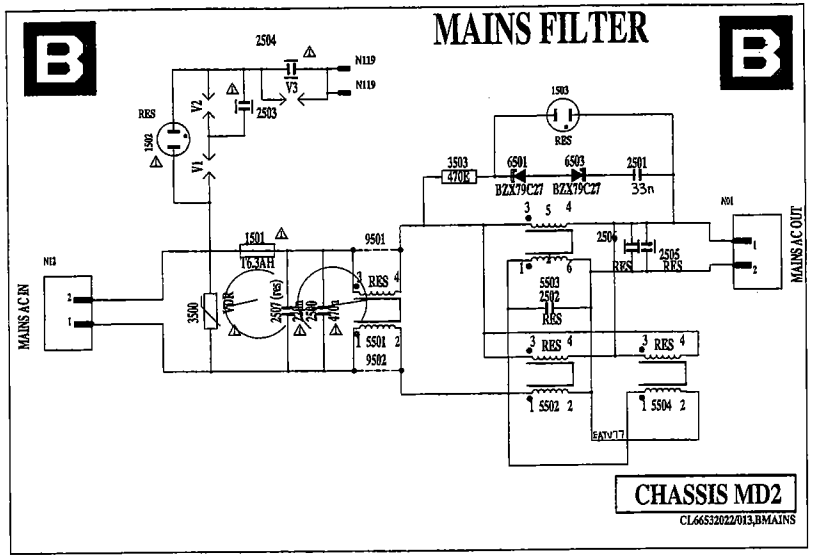
Block diagram supply voltages



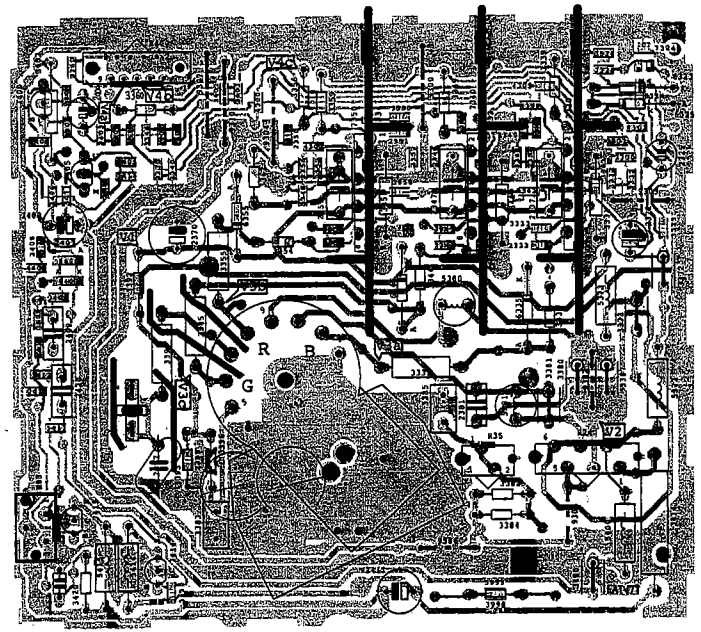
I²C tree



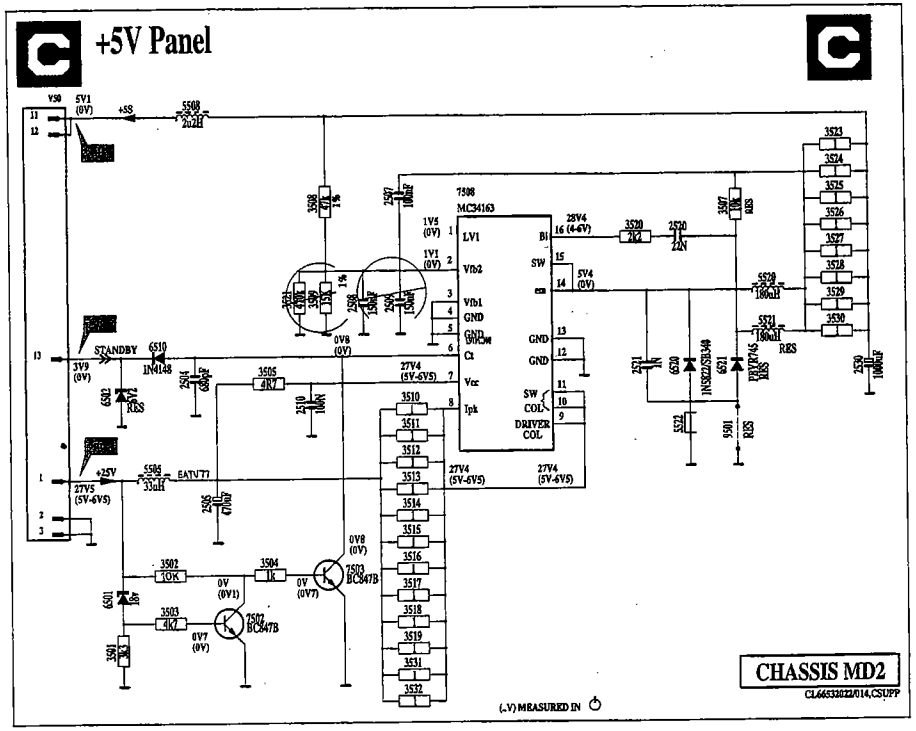
Mains filter panel



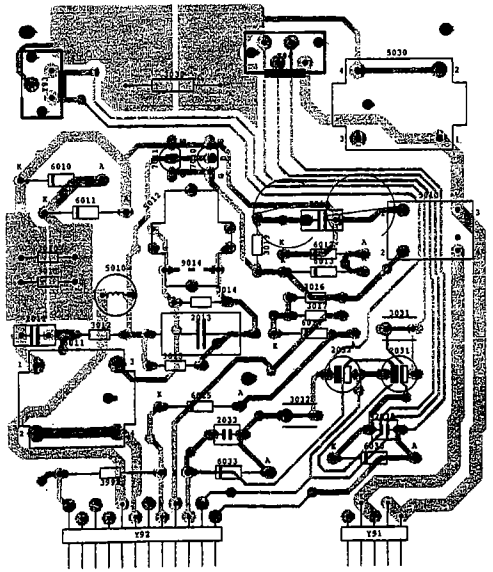
PICTURE TUBE & SCAVEM PANEL



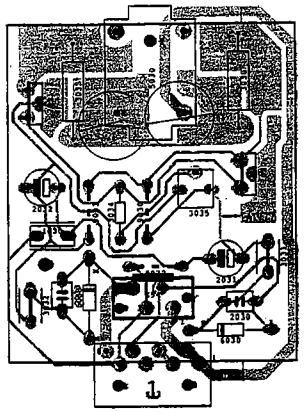
+5V Supply panel



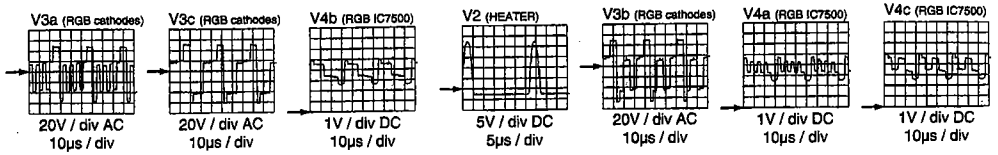
N/S & DC-SHIFT PANEL



EXTERNAL DC-SHIFT PANEL

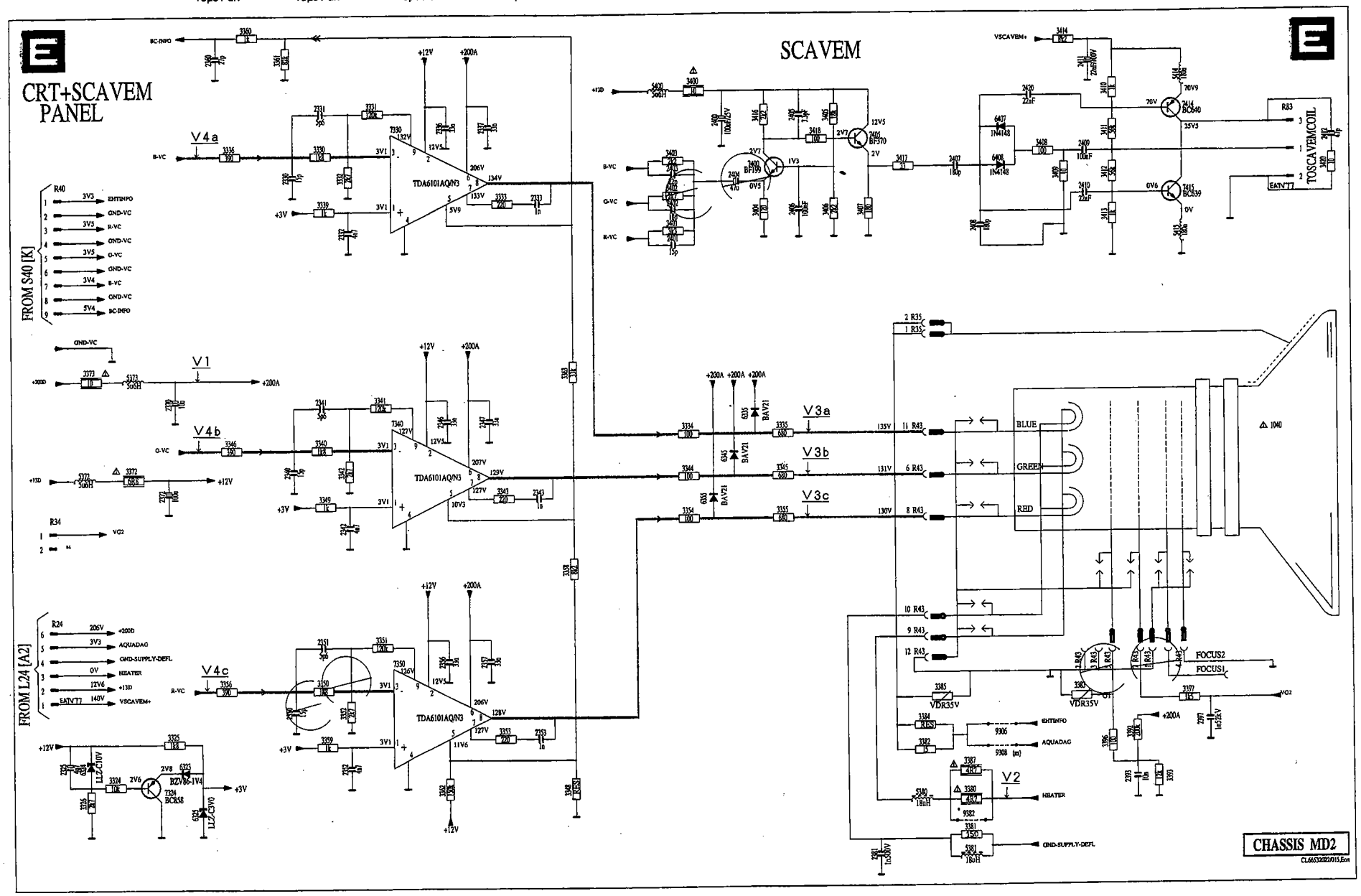


V1 (+200D)
200V DC

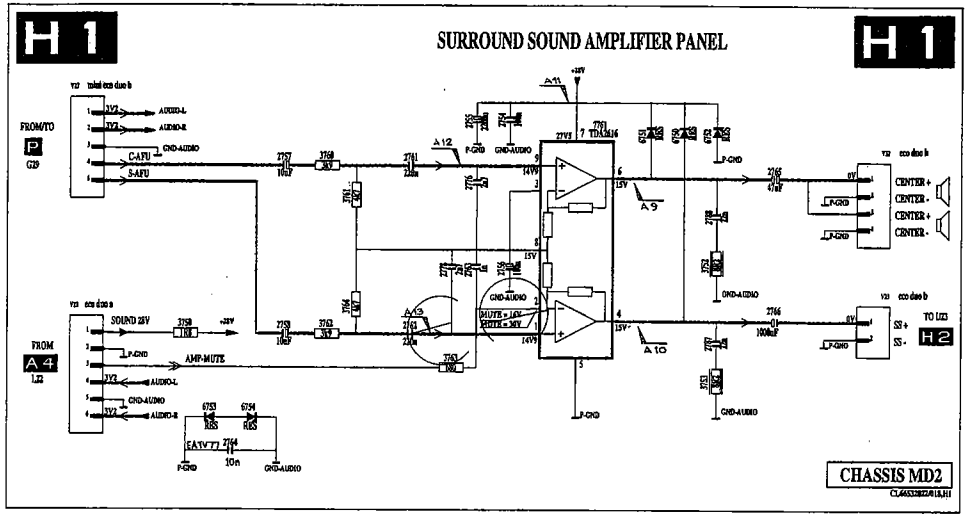


= 0V

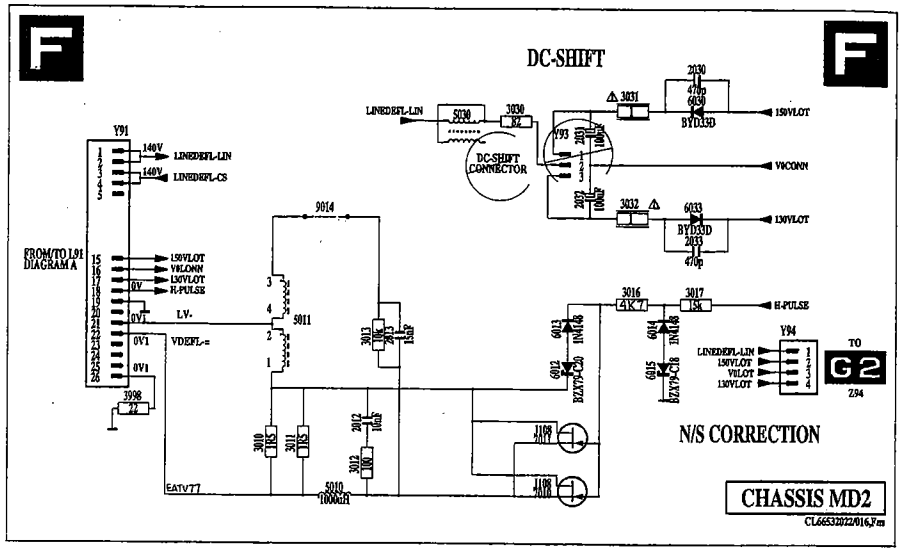
Picture tube & SCAVEM panel



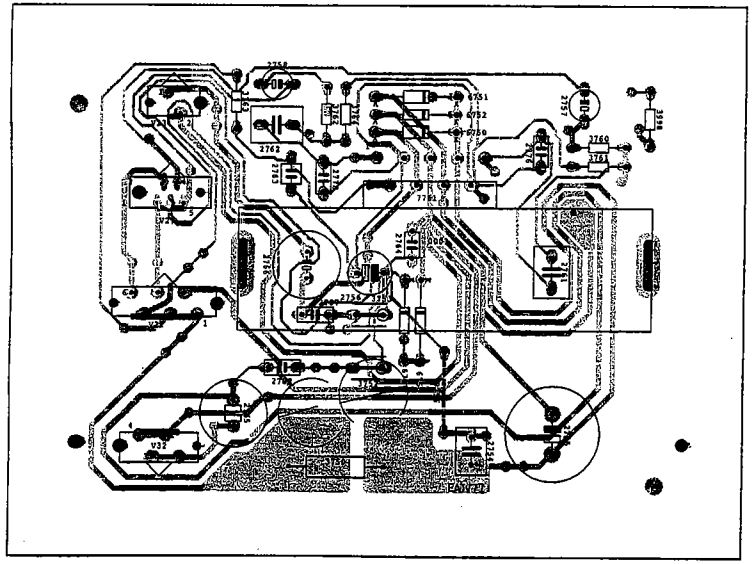
Surround sound panel

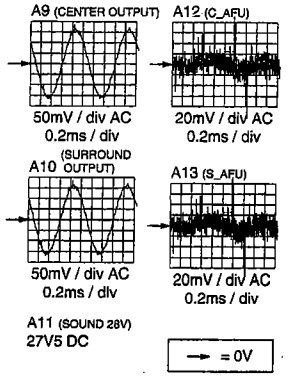


N/S & DC-Shift panel

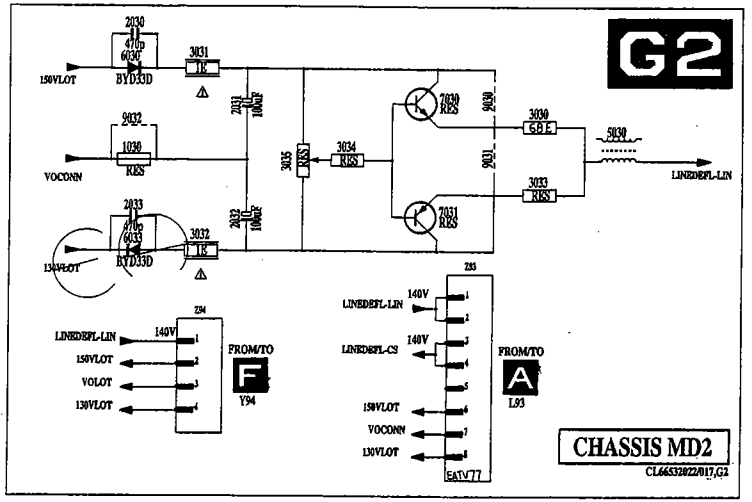


SURROUND SOUND PANEL

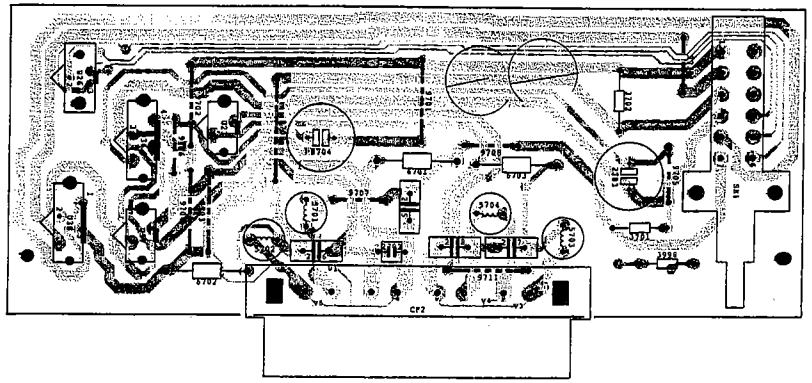




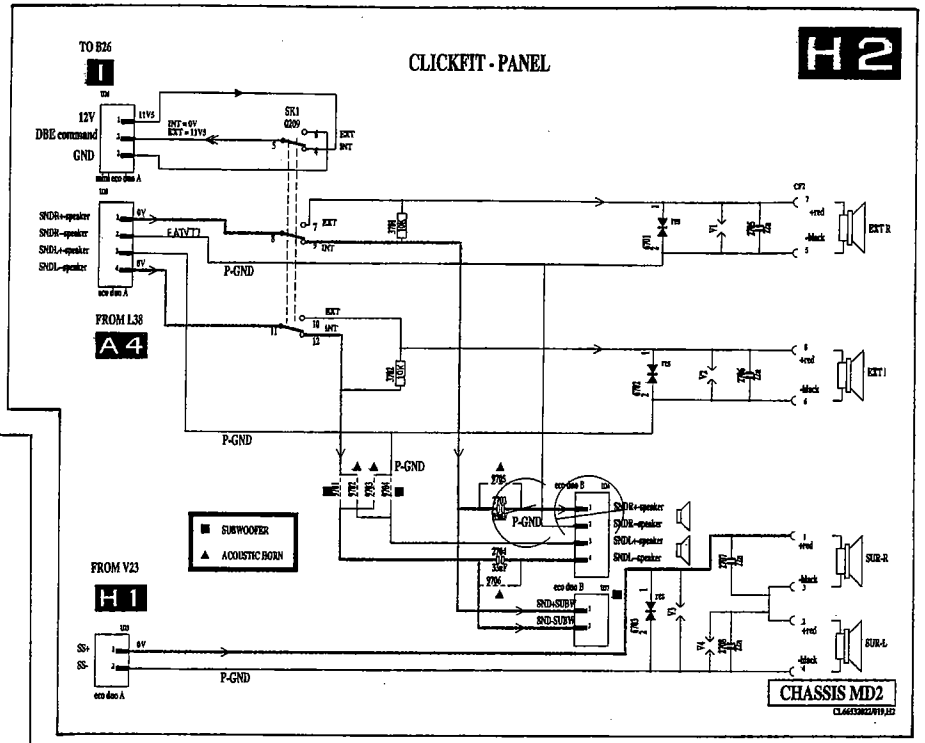
External DC shift panel



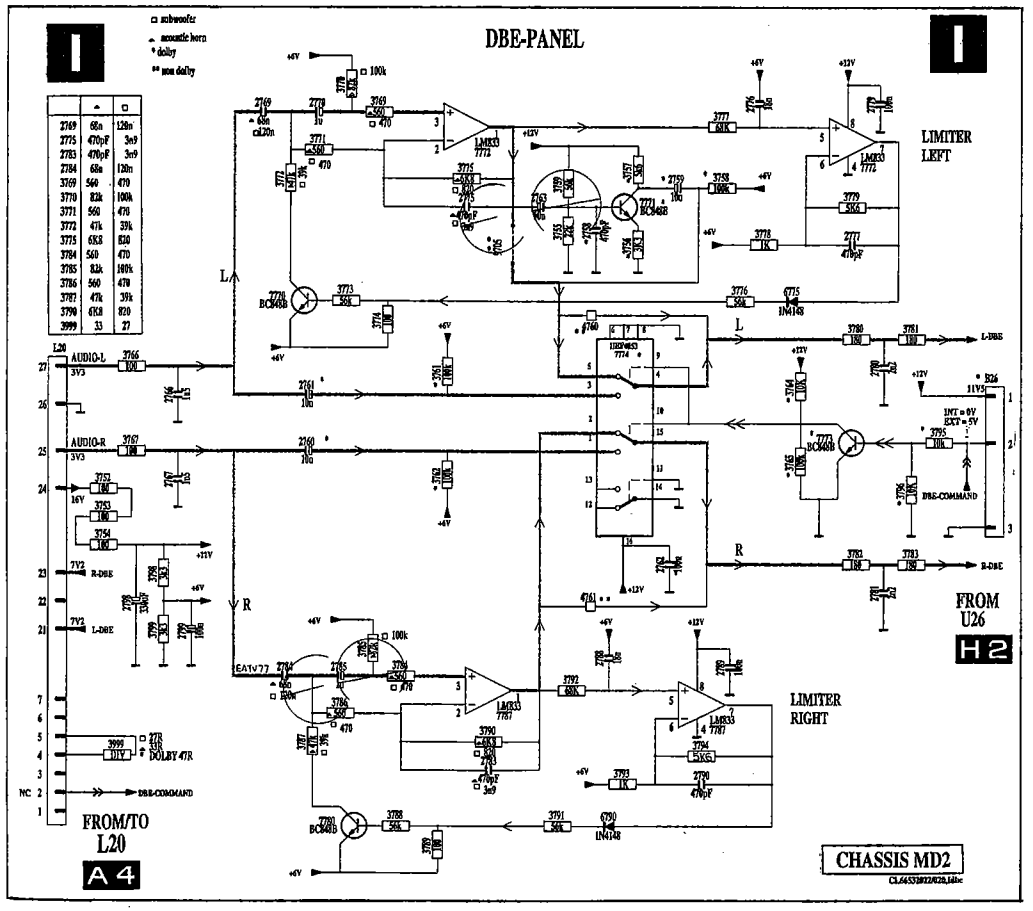
CLICKFIT PANEL



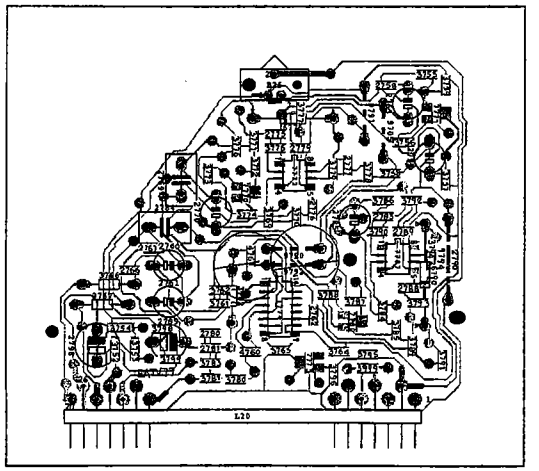
Clickfit panel Telaio MD 2.11/12/13 E AA **PHONOLA PHILIPS**



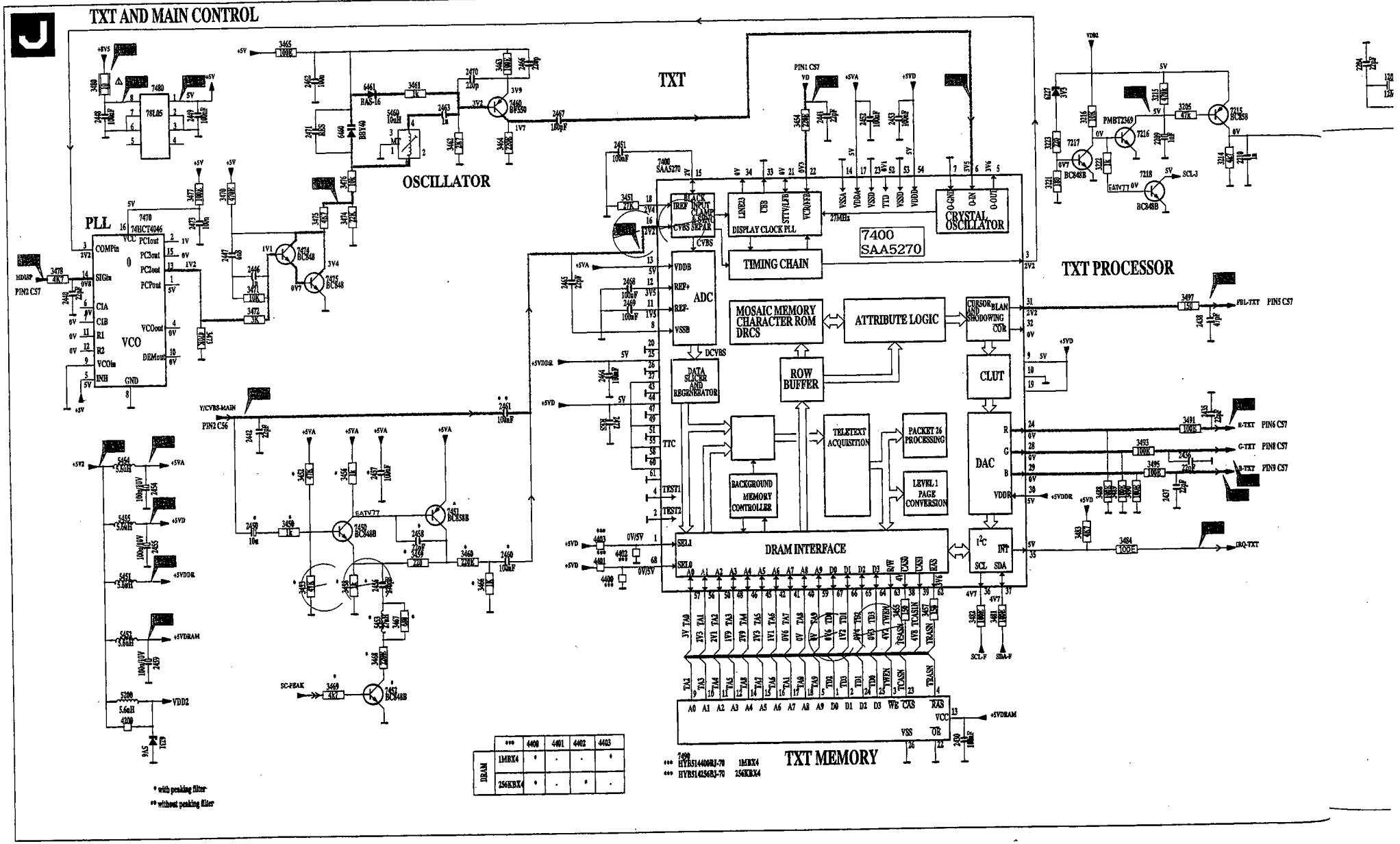
DBE panel



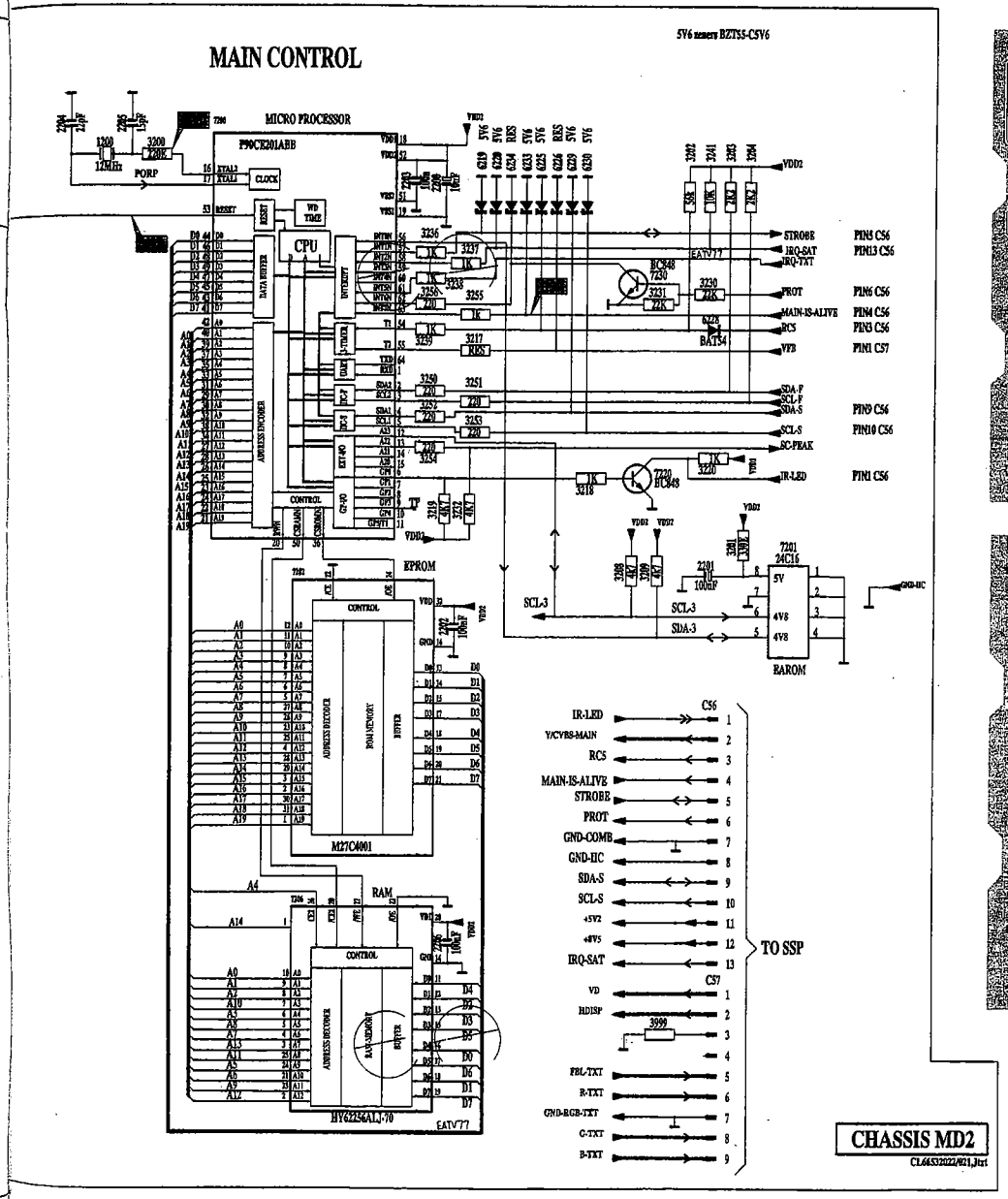
DBE PANEL

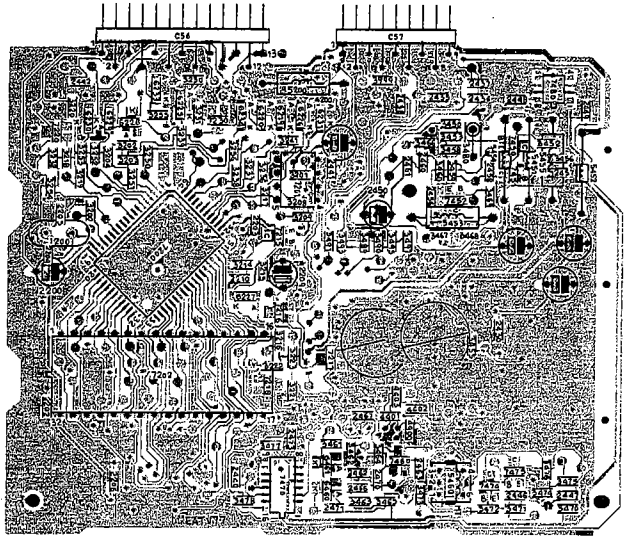


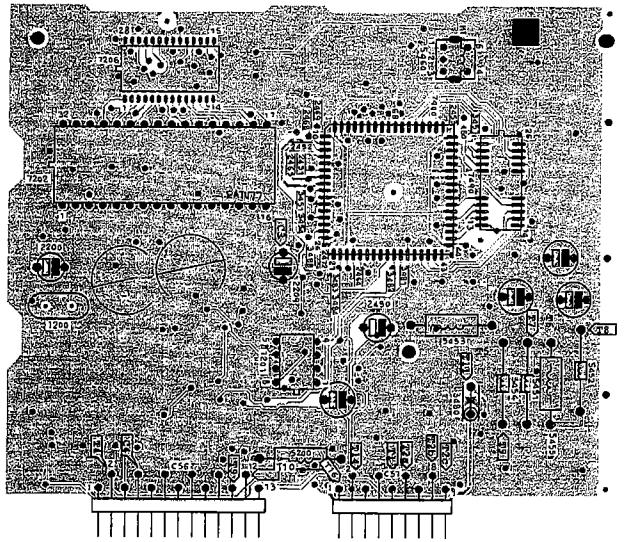
TXT & Control panel



TXT & Control panel







Measured in TXT- mode

T1 (FBL_TXT)
TXT = 2V2 DC
NO TXT = 0V DC

T9 (+6V2) 5V DC
T10 (+8V5) 8V5 DC
T11 8V3 DC
T12 (+5V) 4V9 DC

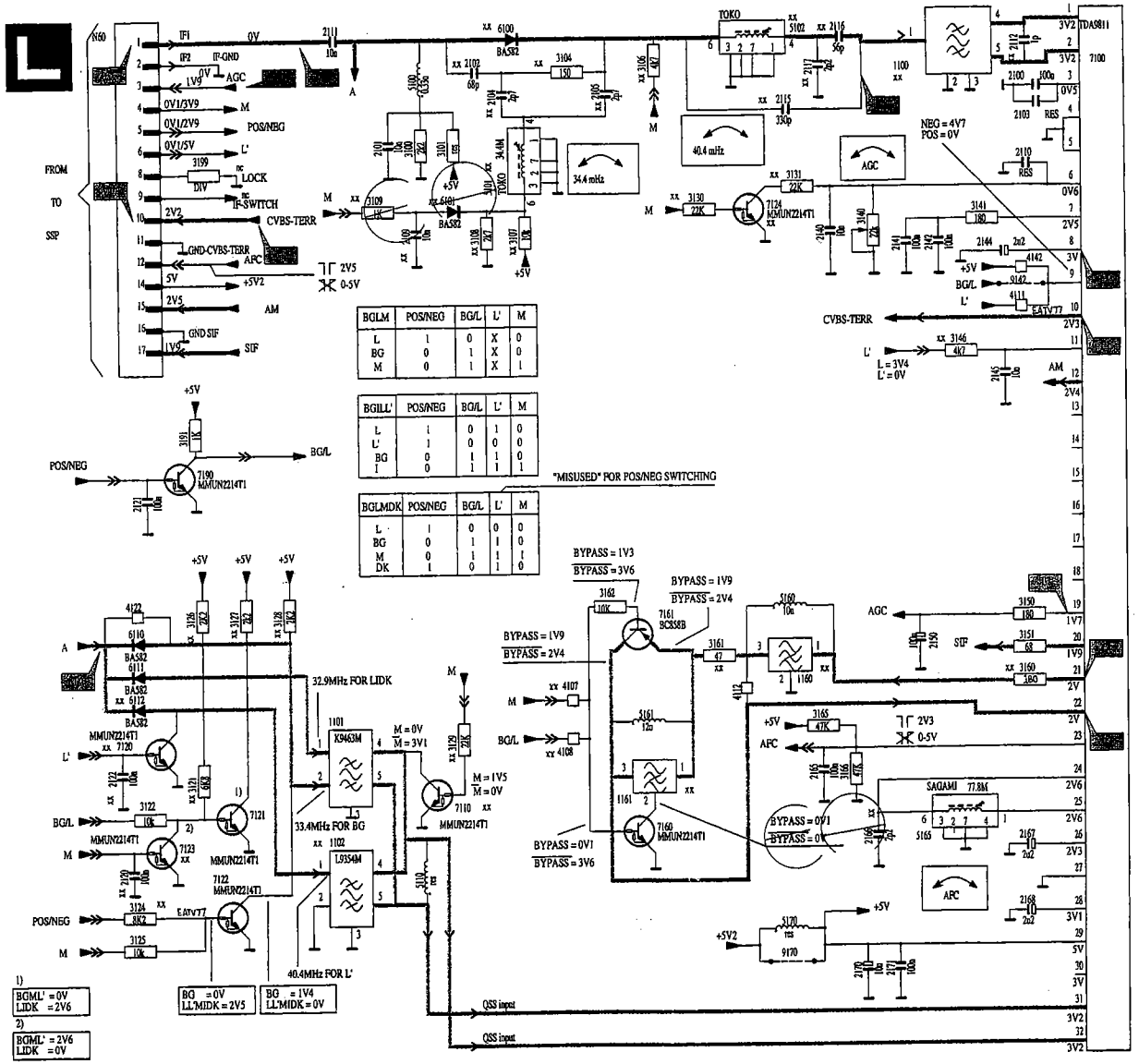
T2a (R_TXT) 0.2V / div DC 10µs / div
T13 (pin 6 IC7400) 1V / div DC 0.2µs / div
T2b (G_TXT) 0.2V / div DC 10µs / div
T14 (HDISP) 1V / div DC 10µs / div
T2c (B_TXT) 0.2V / div DC 10µs / div
T15 0.5V / div DC 0.2µs / div
T3 (CVBS) 0.2V / div AC 10µs / div
T16 (VD) 1V / div DC 5ms / div
T4 (Y/CVBS_MAIN) 0.2V / div AC 10µs / div
T17 (IRQ_TXT) 2V / div DC 10ms / div

T5 (+5V A) 5V DC
T7 (+5V DDR) 5V DC
T8 (+5V DRAM) 5V DC

→ = 0V

C1 (CLOCK) 0.5V / div DC 0.2ms / div
C2 OFF-ON(PORP) 1V / div DC 50ms / div
C2 ON-OFF(PORP) 1V / div DC 50ms / div
C3 OFF-ON 1V / div DC 0.2s / div
C3 ON-OFF 1V / div DC 0.2s / div
C4 see § 6.5.1 (MAIN IS ALIVE) 1V / div DC 0.1s / div

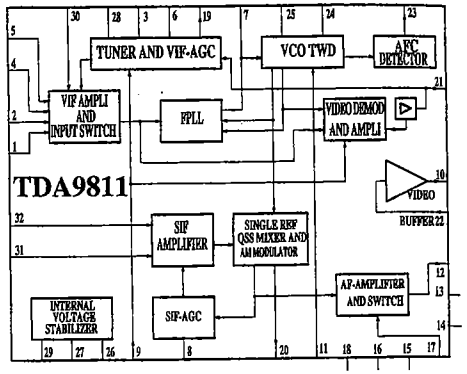
IF module



BGLM	POS/NEG	BG/L	L'	M
L	1	0	X	0
BG	0	1	X	0
M	0	1	X	1

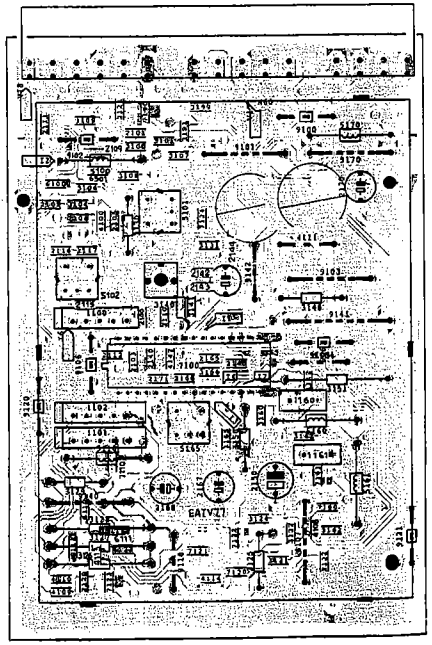
BGILL'	POS/NEG	BG/L	L'	M
L	1	0	1	0
L'	1	0	0	0
BG	0	1	1	0
I	0	1	1	1

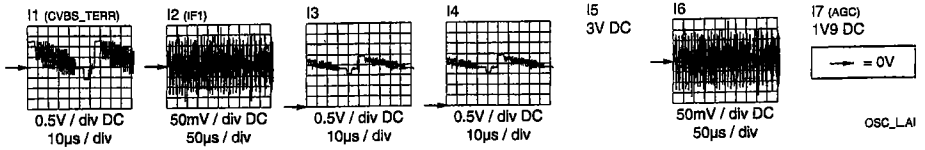
BGLMDK	POS/NEG	BG/L	L'	M
L	1	0	0	0
BG	0	1	1	0
M	0	1	1	1
DK	1	0	1	0



BGLMDK ; LIKE BGLM EXCEPT:	
1100	OFWG3953M
4111	IN
9142	NOT IN

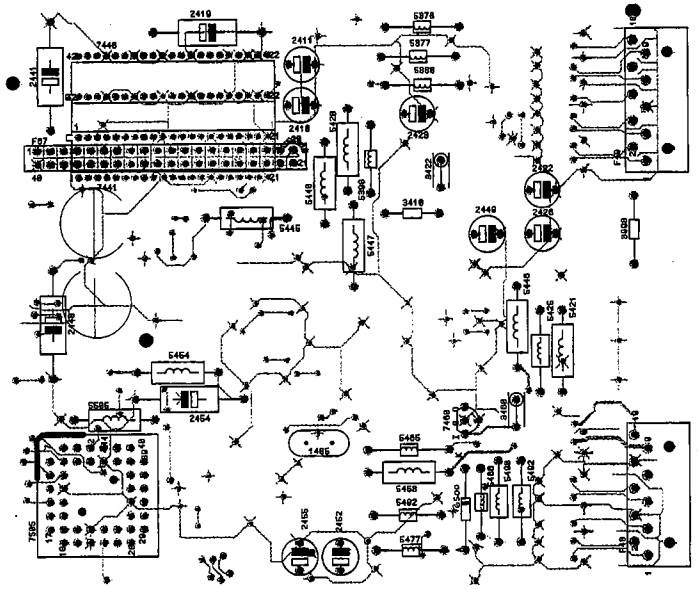
**	BGLM	BGLL'I
1100	OFWG3956M	OFWG3953M
1102	-	OFWG3954M
1160	TPS5,5MW	TPS6,0MB
1161	TPS4,5MB	TPS5,5MW
2102	68pF	JUMPER
2104	2p7	-
2105	2p7	-
2109	10n	-
2112	-	-
2115	JUMPER	330p
2116	-	56p
2117	-	2p2
2120	-	100n
2122	-	100n
3104	150	47
3106	4k7	-
3107	10K	-
3108	2k7	-
3109	1k	-
3121	-	6k8
3124	8k2	6k8
3126	-	2k2
3127	2k2	4k7
3128	2k2	4k7
3129	22k	-
3130	22k	-
3131	22k	-
3146	-	4k7
3160	180	220
3161	47	JUMPER
4107	JUMPER	-
4108	-	4142
4142	-	-
5101	34,4MC	-
5102	-	40.4MC
6100	BA582	-
6101	BA582	-
6112	-	BA582
7110	MMUN2214T1	-
7120	-	MMUN2214T1
7123	-	MMUN2214T1
7124	MMUN2214T1	-

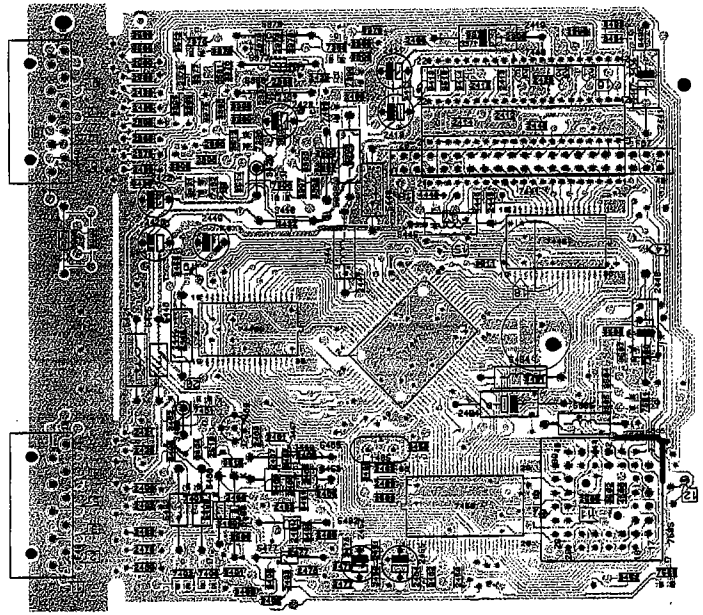




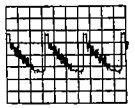
CHASSIS MD2

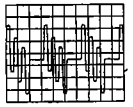
CL66532022/023.Lif

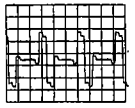


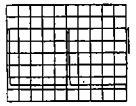


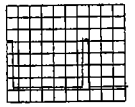
Feature Box (100 Hz Digital scan) panel

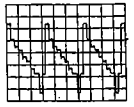
F1

100mV/div
10µs/div

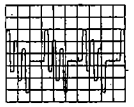
F2

200mV/div
10µs/div

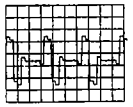
F3

200mV/div
10µs/div

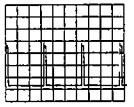
F4

1V/div
2ms/div

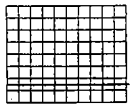
F5

1V/div
5µs/div

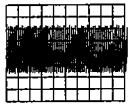
F6

200mV/div
20µs/div

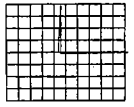
F7

200mV/div
20µs/div

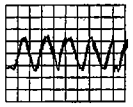
F8

200mV/div
20µs/div

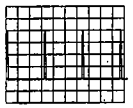
F9

1V/div
20µs/div

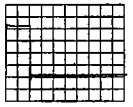
F10

1V/div
5ms/div

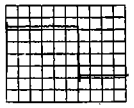
F11

1V/div
0.5µs/div

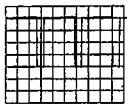
F12

1V/div
50ms/div

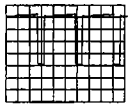
F13

500V/div
50ns/div

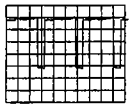
F14

1V/div
20µs/div

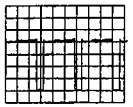
F15

1V/div
0.5µs/div

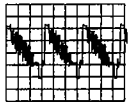
F16

1V/div
0.5µs/div

F17

1V/div
20µs/div

F18

1V/div
10µs/div

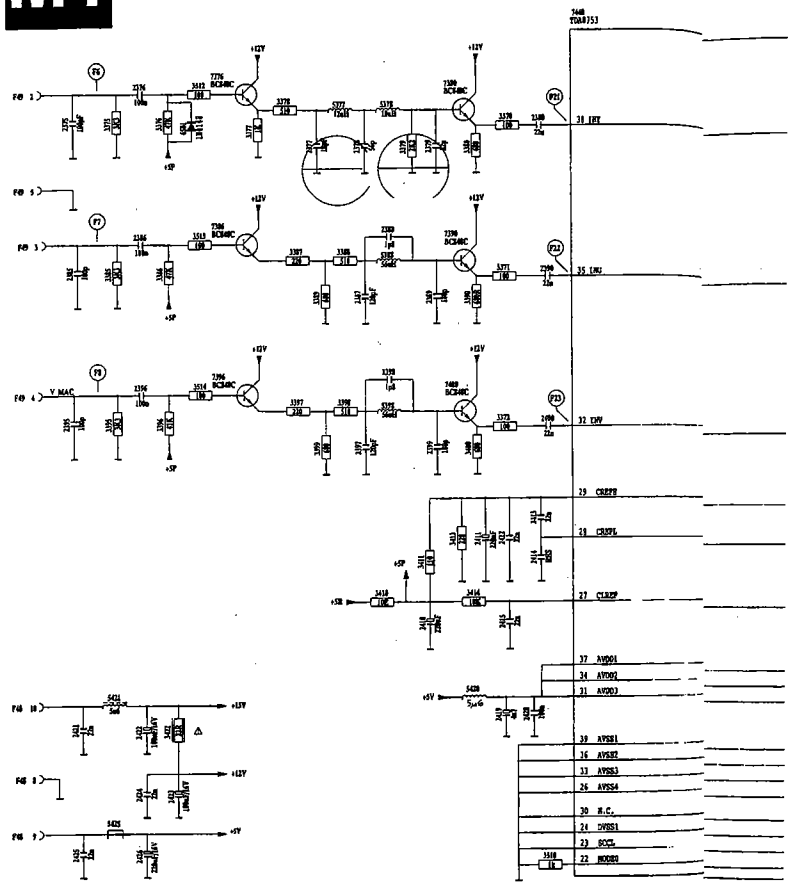
F19

1V/div
10µs/div

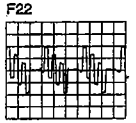
F20

1V/div
10µs/div

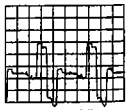
F21

200mV/div
20µs/div

FEATURE BOX 3

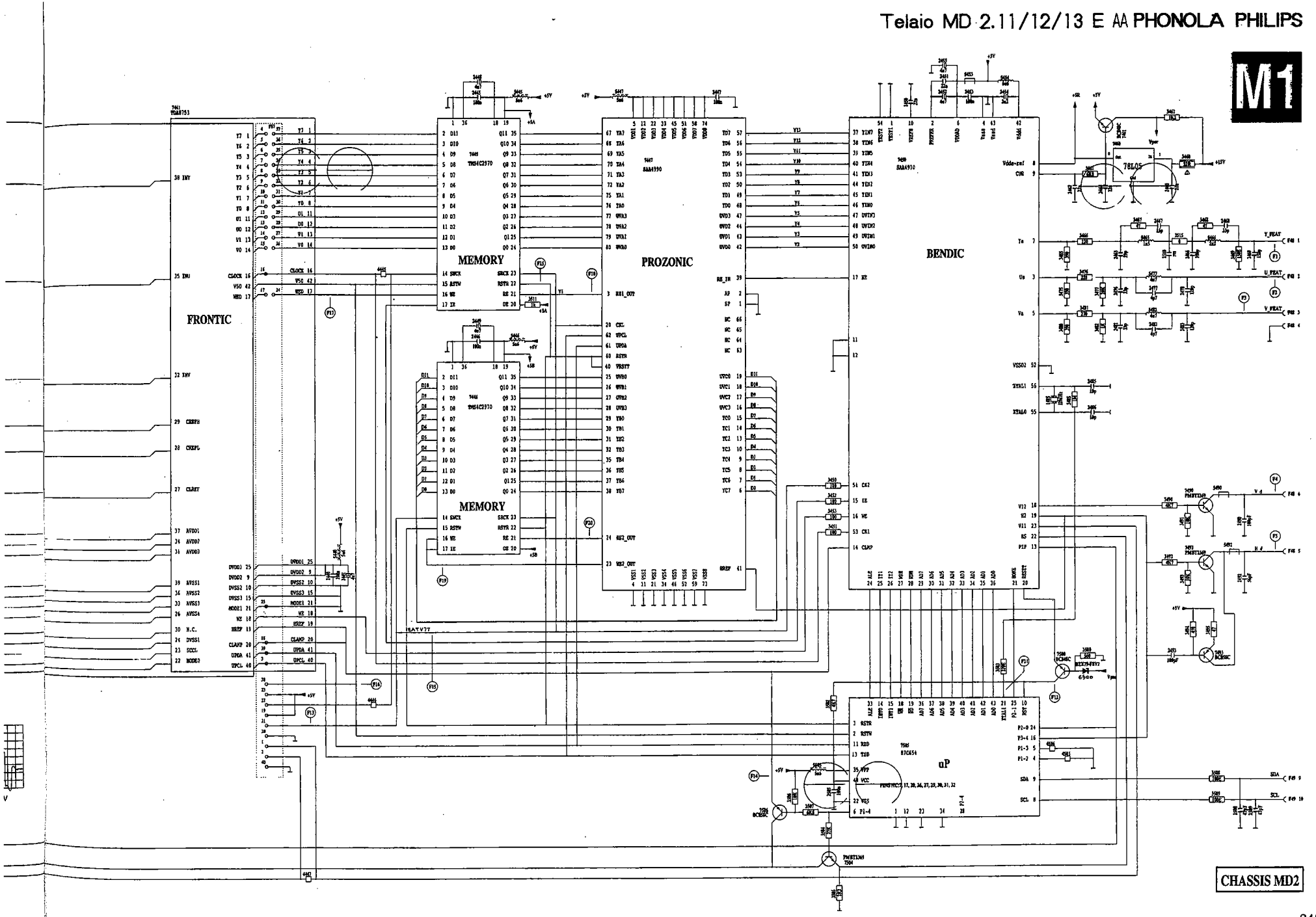
M1

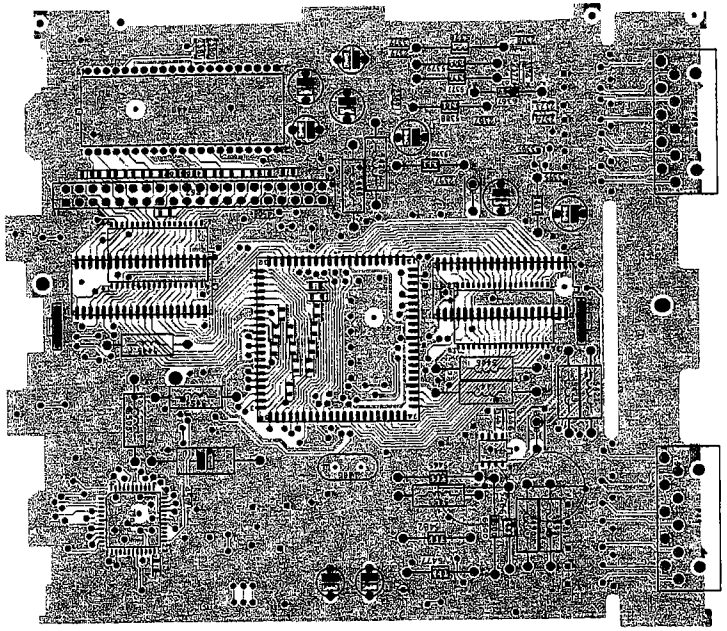


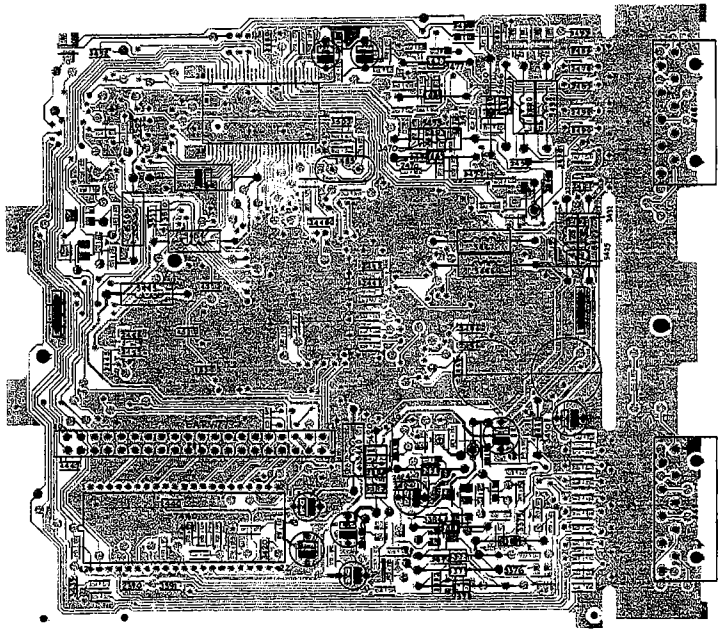
F22

200mV/div
20µs/div

F23

200mV/div
20µs/div

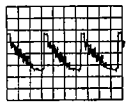
M1


FRONTIC
MEMORY
MEMORY
PROZONIC
BENDIC
uP
78L05
CHASSIS MD2



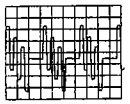


F1



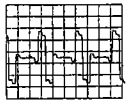
100mV/div
10µs/div

F2



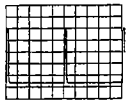
200mV/div
10µs/div

F3



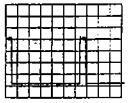
200mV/div
10µs/div

F4



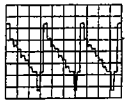
1V/div
2ms/div

F5



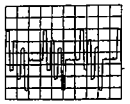
1V/div
5µs/div

F6



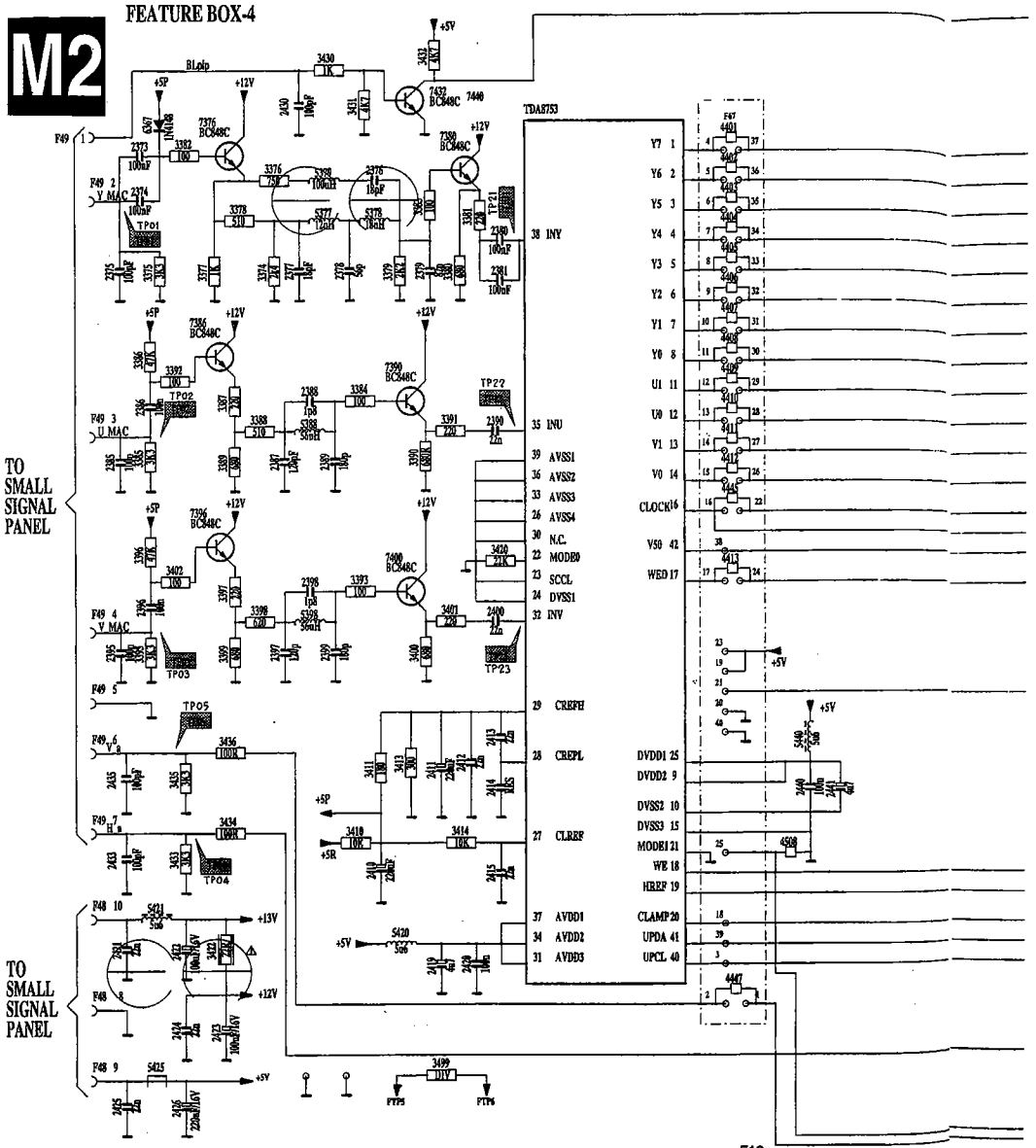
200mV/div
20µs/div

F7

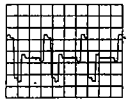


200mV/div
20µs/div

Feature Box (natural motion) panel

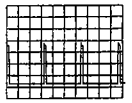


F8



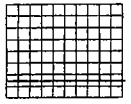
200mV/div
20µs/div

F9



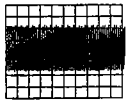
1V/div
20µs/div

F10



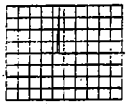
1V/div
5ms/div

F11



1V/div
0.5µs/div

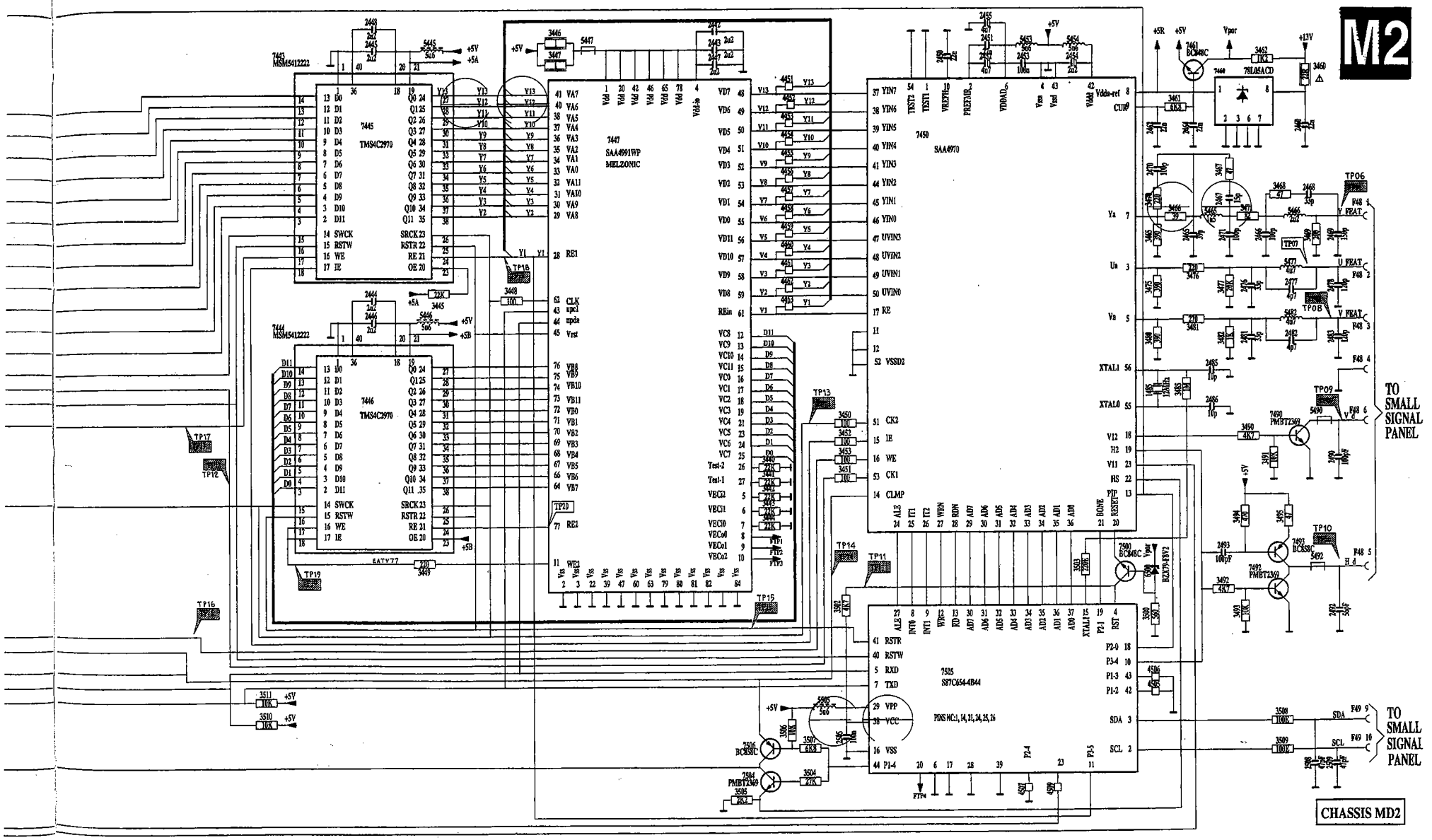
F12



1V/div
50ms/div

F13

M2



F13
500V/div
50ns/div

F14
1V/div
20µs/div

F15
1V/div
0.5µs/div

F16
1V/div
0.5µs/div

F17
1V/div
20µs/div

F18
1V/div
10µs/div

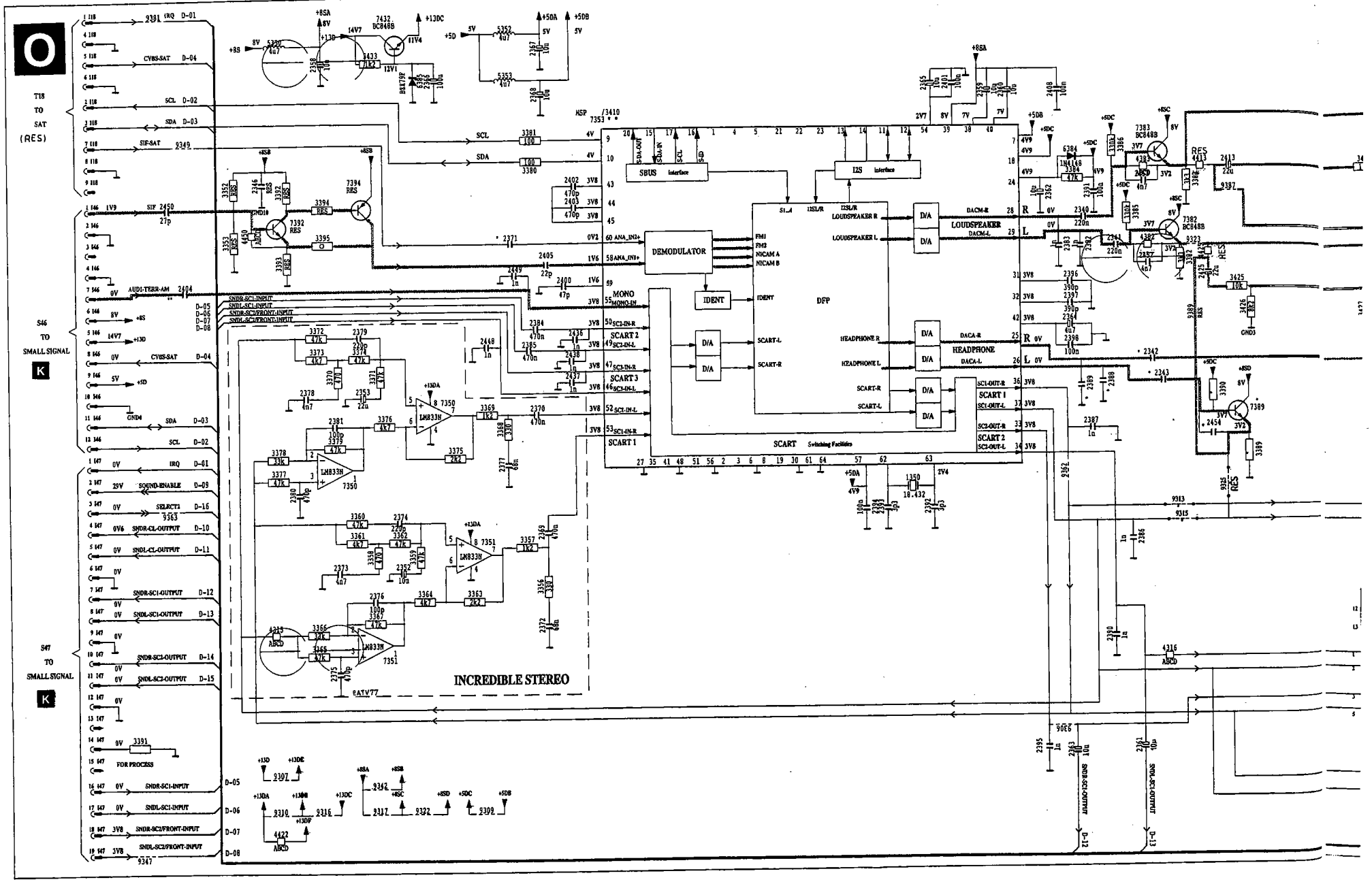
F19
1V/div
10µs/div

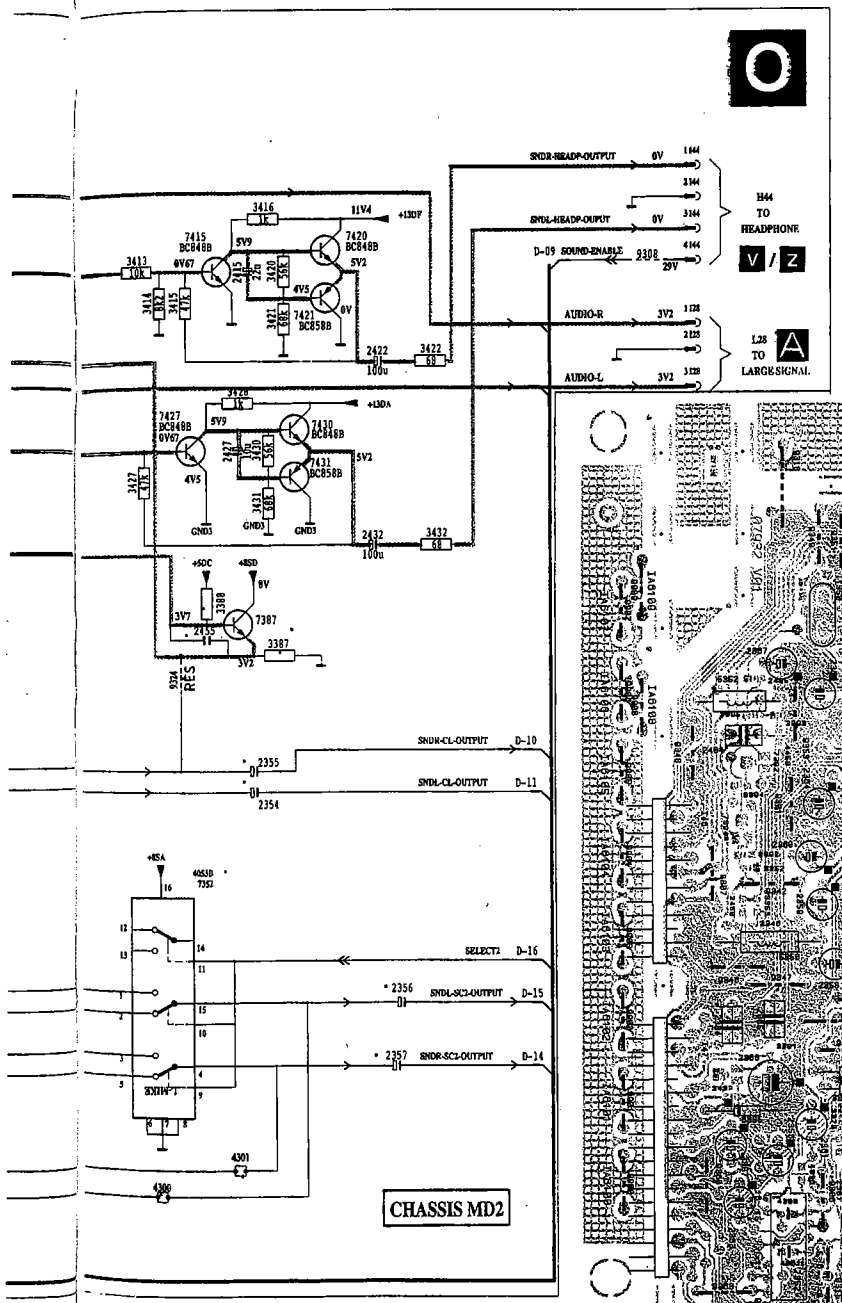
F20
1V/div
10µs/div

F21
200mV/div
20µs/div

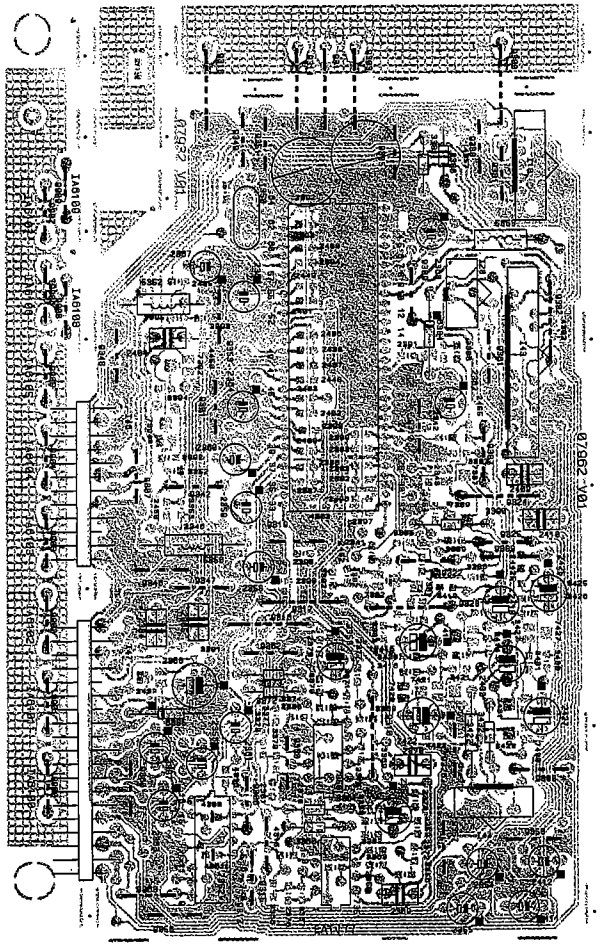
F22
200mV/div
20µs/div

F23
200mV/div
20µs/div

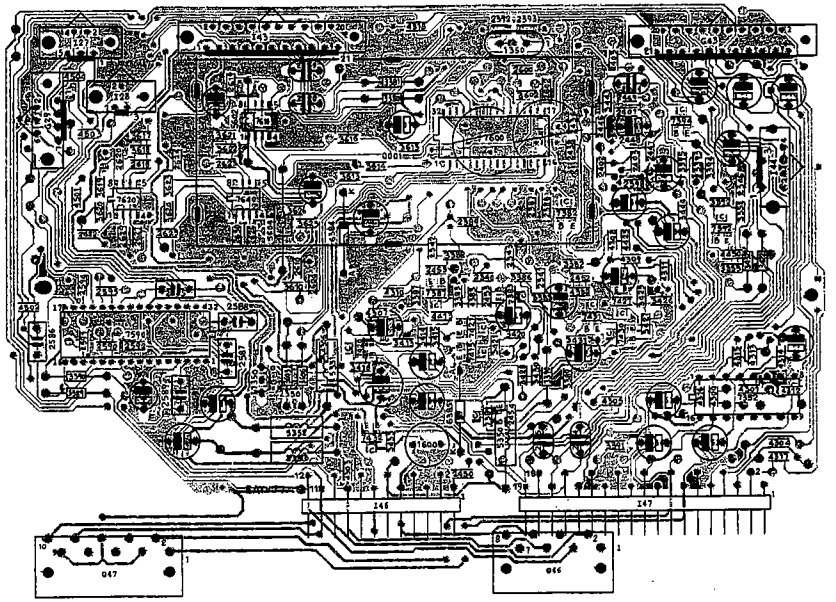

T18 TO SAT (RES)
S46 TO SMALL SIGNAL
S47 TO SMALL SIGNAL
DEMODULATOR
IDENT
DFP
SBUS Interface
I2S Interface
SCART 1
SCART 2
SCART 3
LOUDSPEAKER
HEADPHONE
INCREDIBLE STEREO
LM833N
18.432

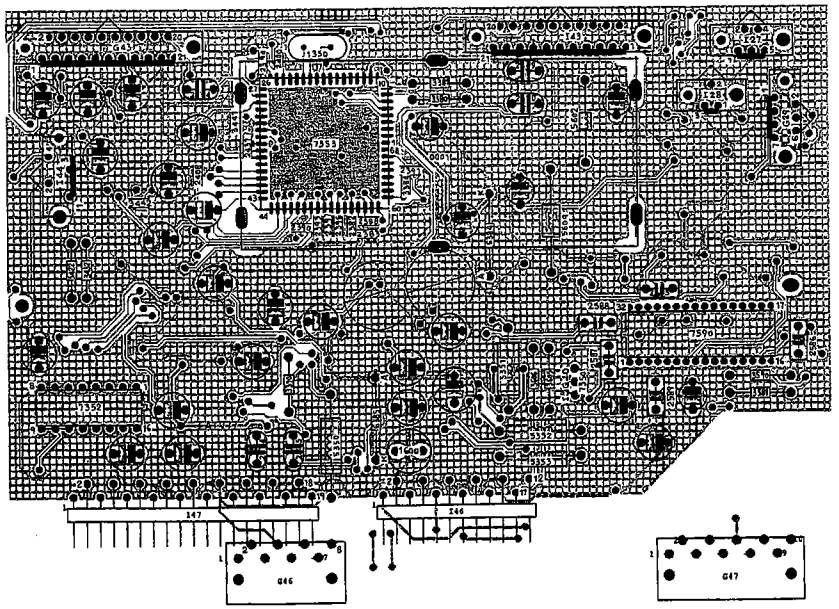


Audio module (no Dolby)

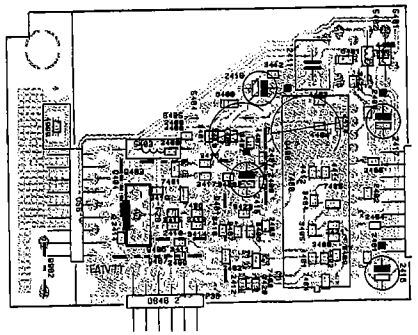


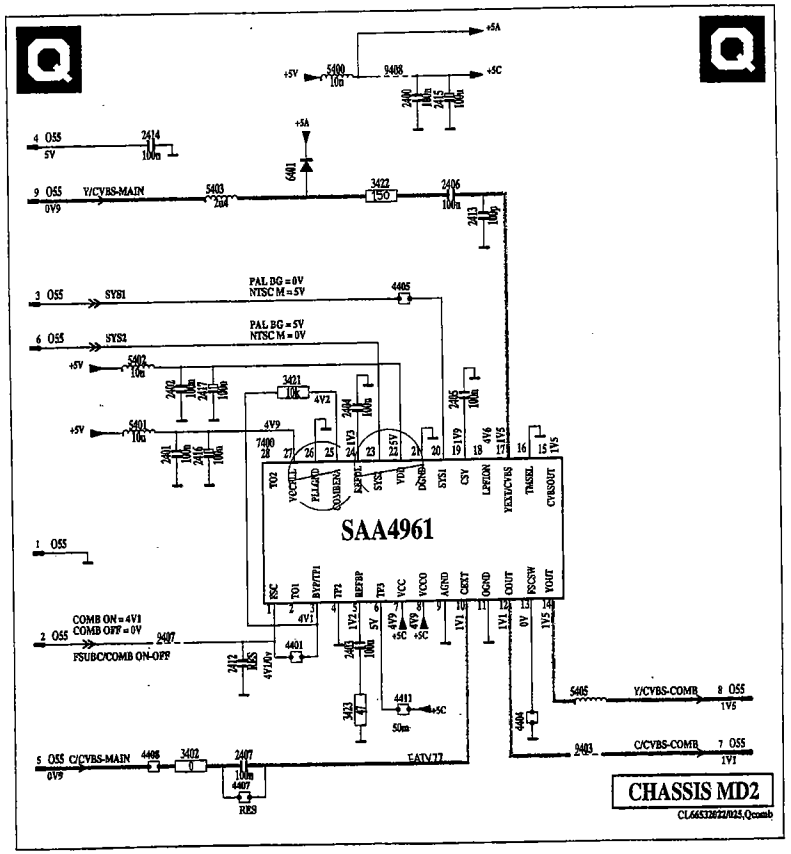
Audio module (with Dolby)



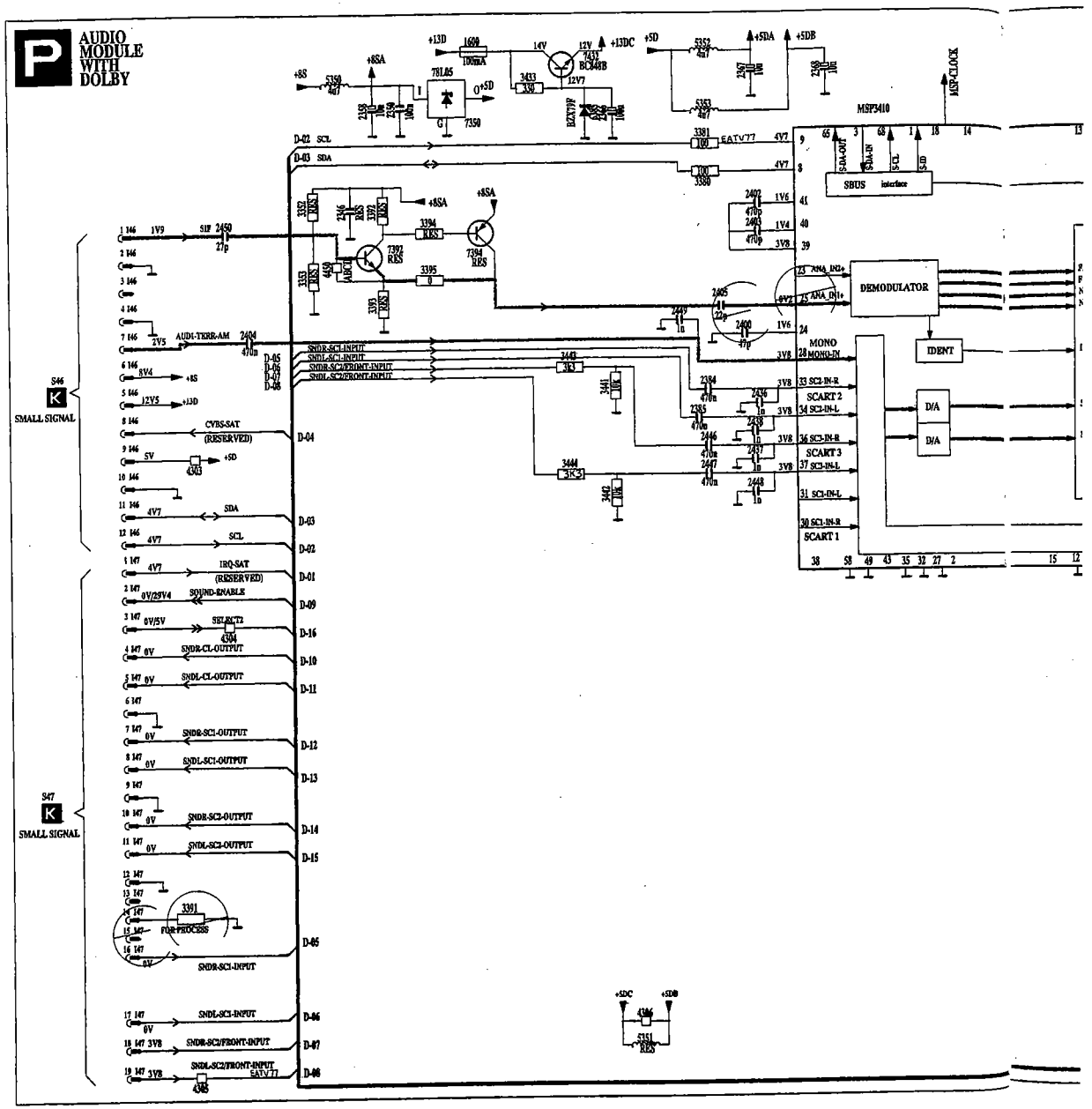


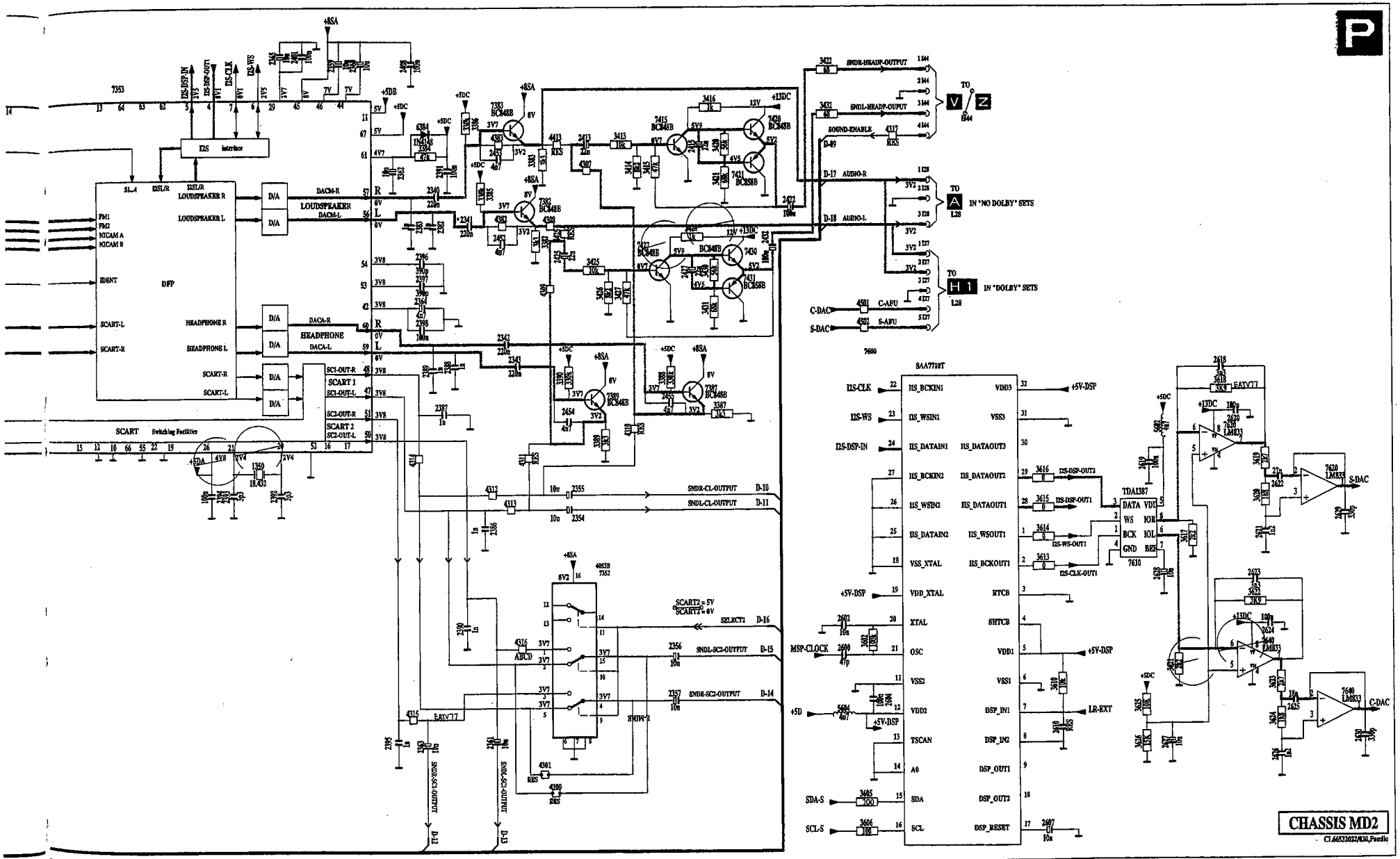
COMB filter module



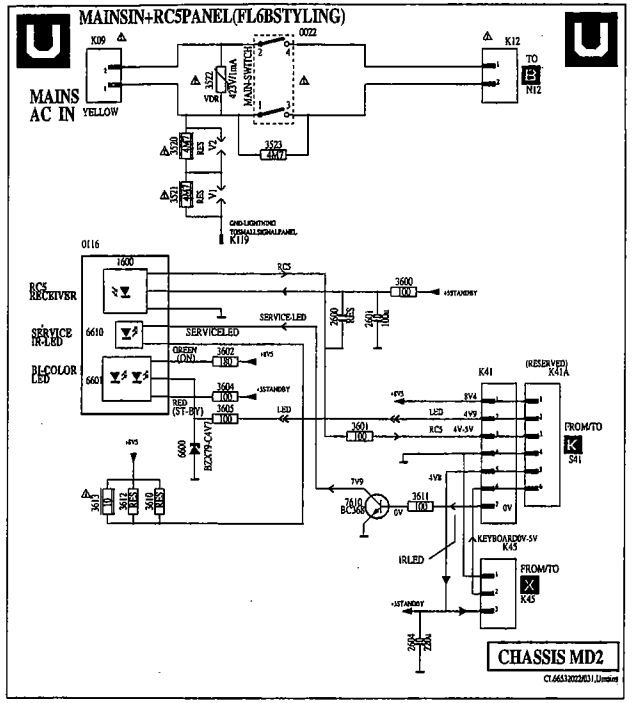


Audio module (dolby)

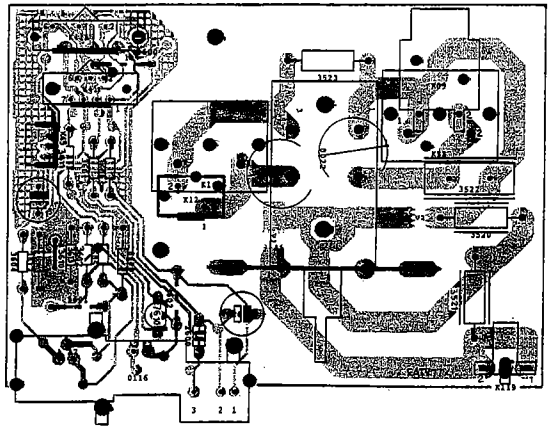



P
CHASSIS MD2
CI.66532022/830,Pandio
I2S Interface
LOUDSPEAKER
HEADPHONE
SCART Switching Facilities
DFP
8AA7710T
TDA1387
LM833
EATV77
TO V/Z
TO A IN "NO DOLBY" SETS
TO H1 IN "DOLBY" SETS
SNDR-HEADP-OUTPUT
SNDL-HEADP-OUTPUT
SOUND-ENABLE
AUDIO-R
AUDIO-L
C-APU
S-APU
C-DAC
S-DAC
SNDR-CL-OUTPUT
SNDL-CL-OUTPUT
SNDL-SC1-OUTPUT
SNDR-SC2-OUTPUT
SCART2 = 5V
SCART2 = 0V
LR-EXT

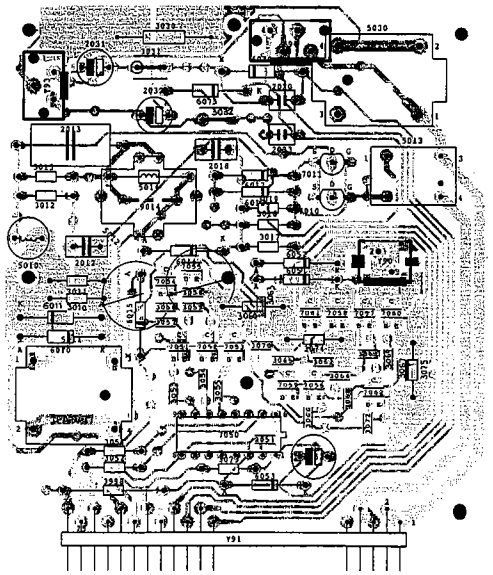
Mains input & RC5 panel (FL6B styling)



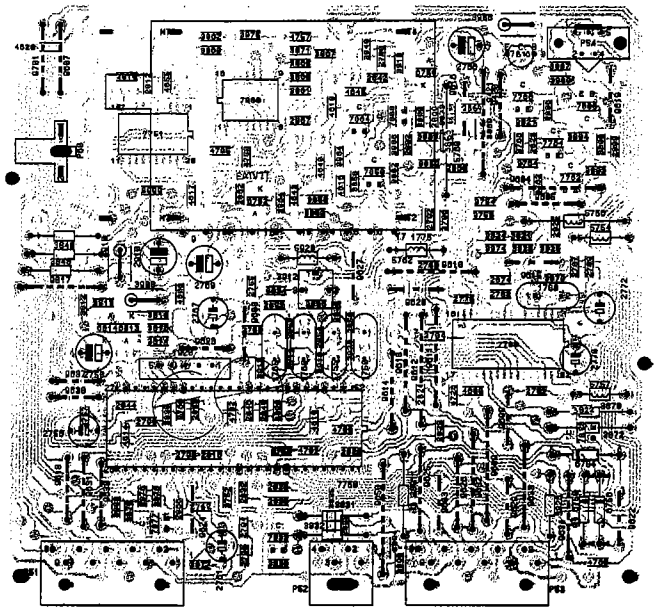
MAINS INPUT & RC5 PANEL (FL6B styling)



N/S ROTATION PANEL

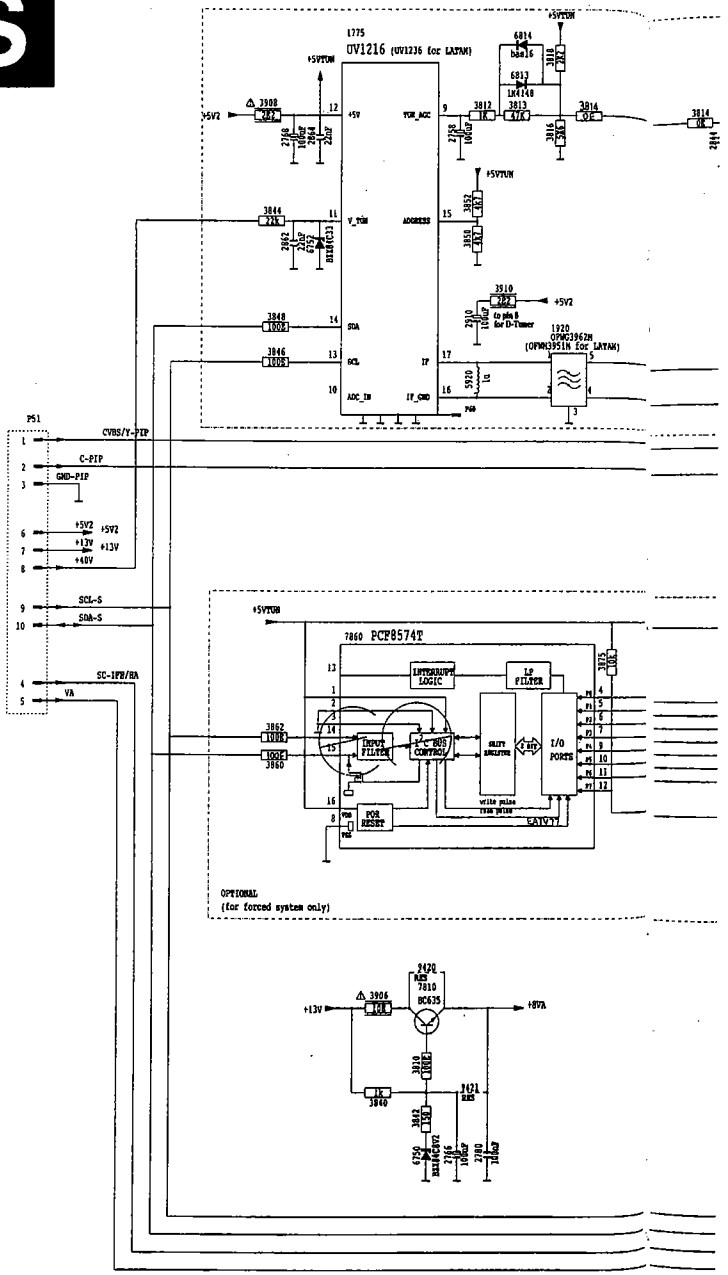


PIP 2 PANEL

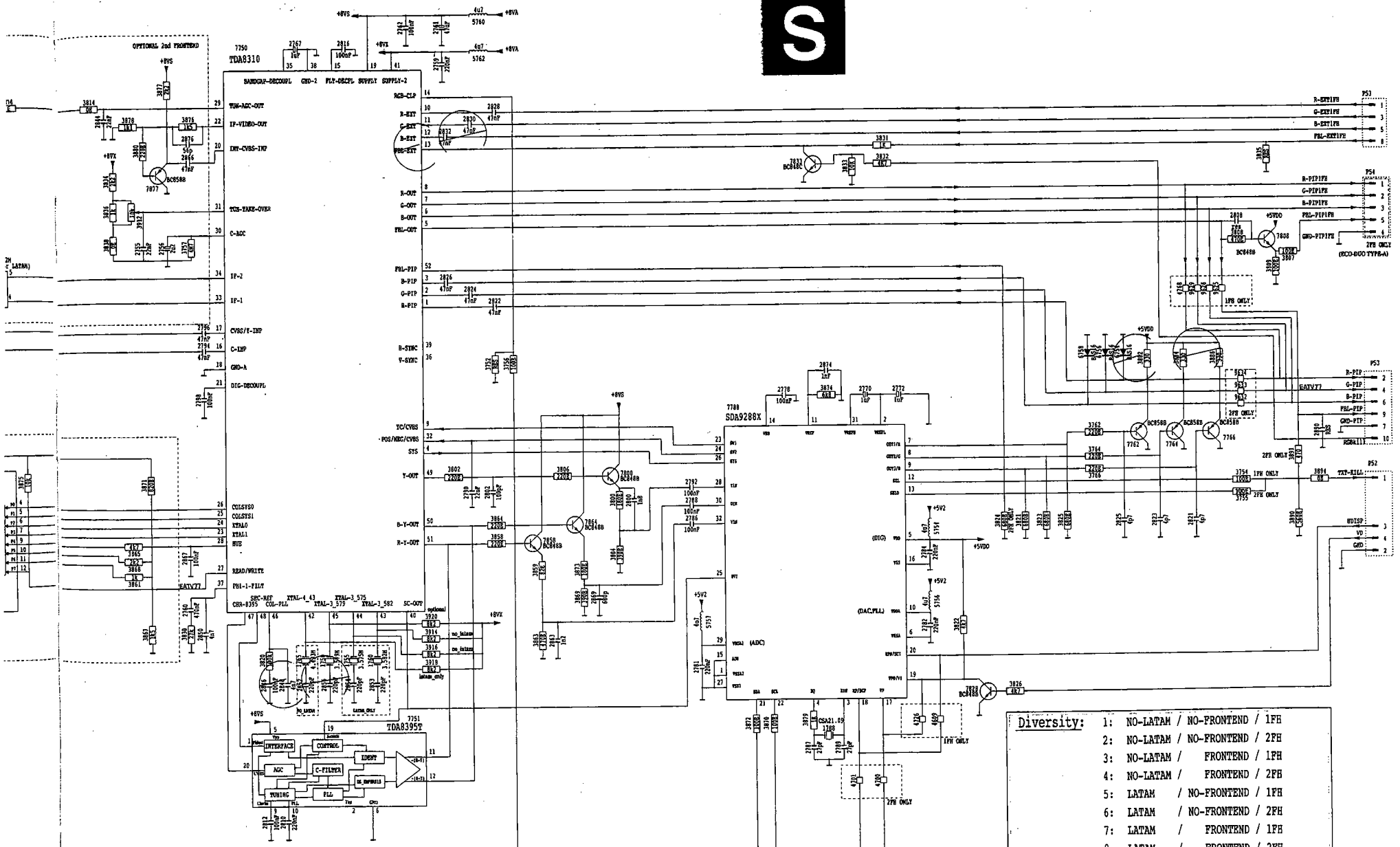


PIP2-Modul

S

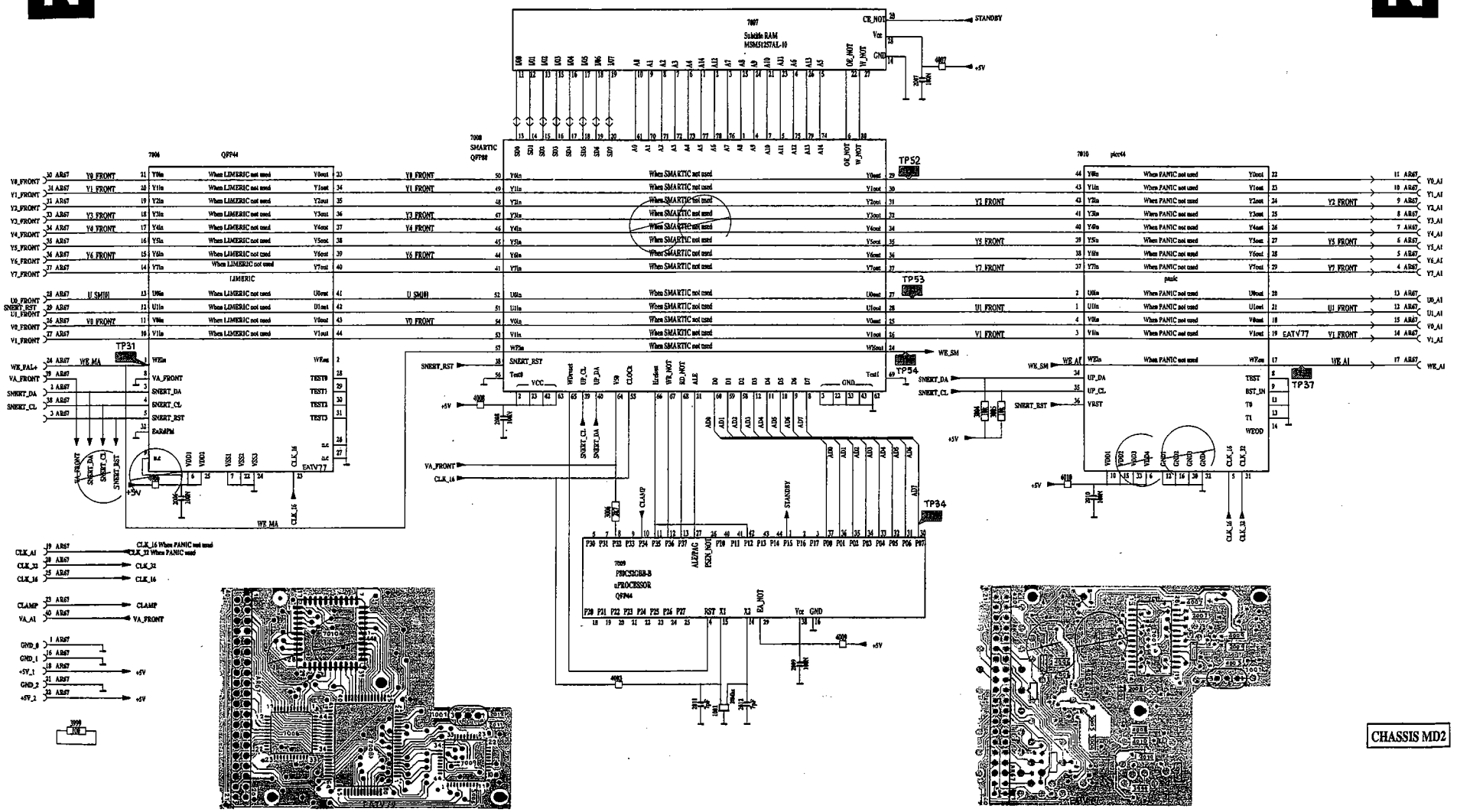


S



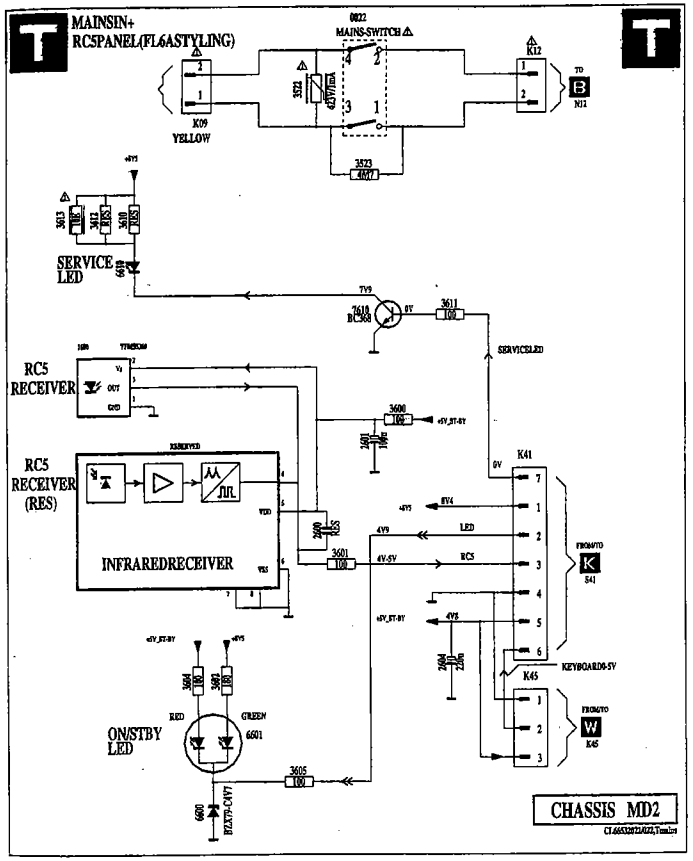
CHASSIS MD2

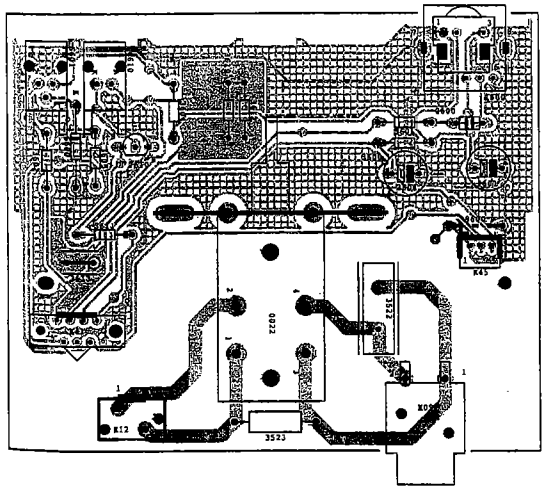
AI panel



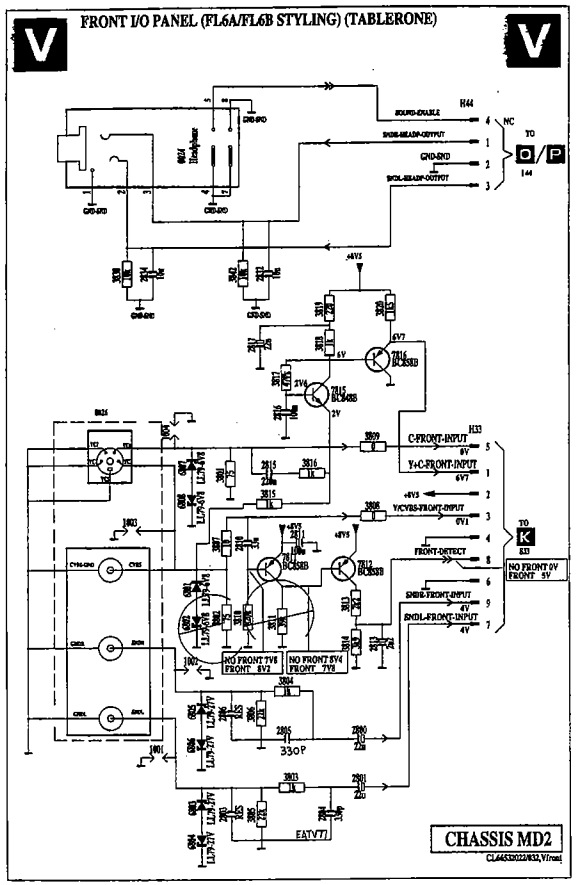
CHASSIS MD2

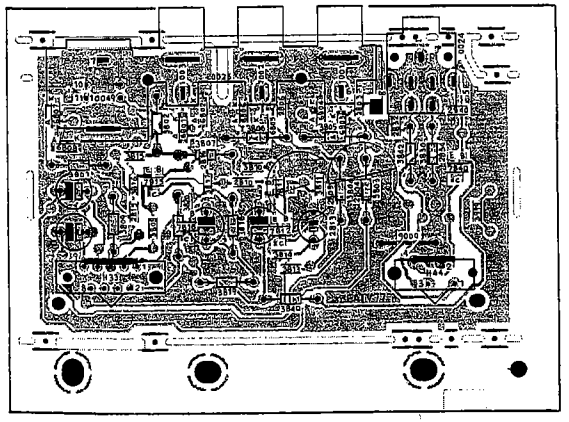
Mains input & RC5 panel (FL6A styling)



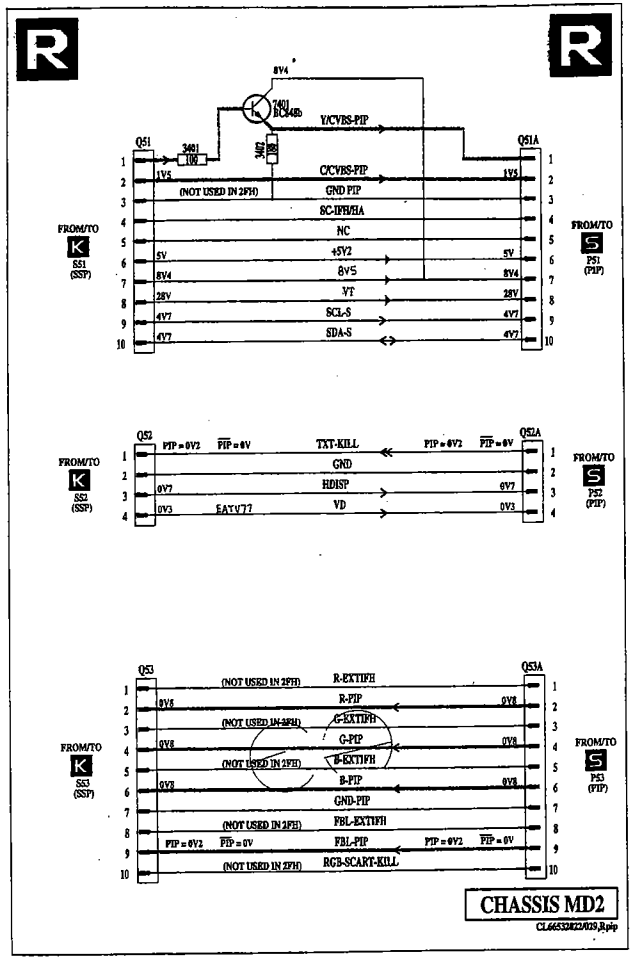


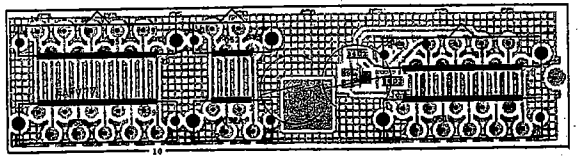
Front input/output panel (FL6A/FL6B styling)



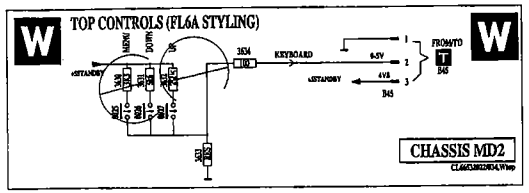


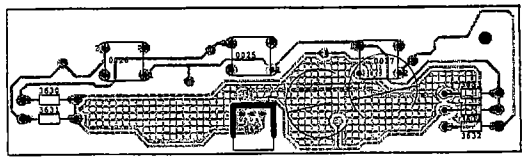
PIP interface panel



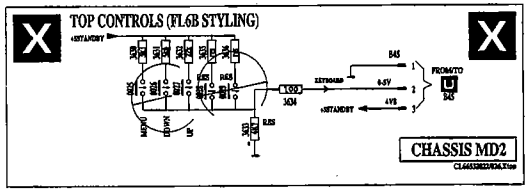


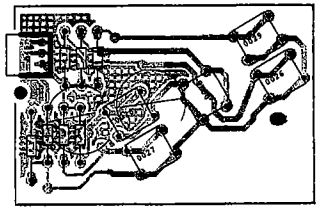
Top control panel (FL6A styling)



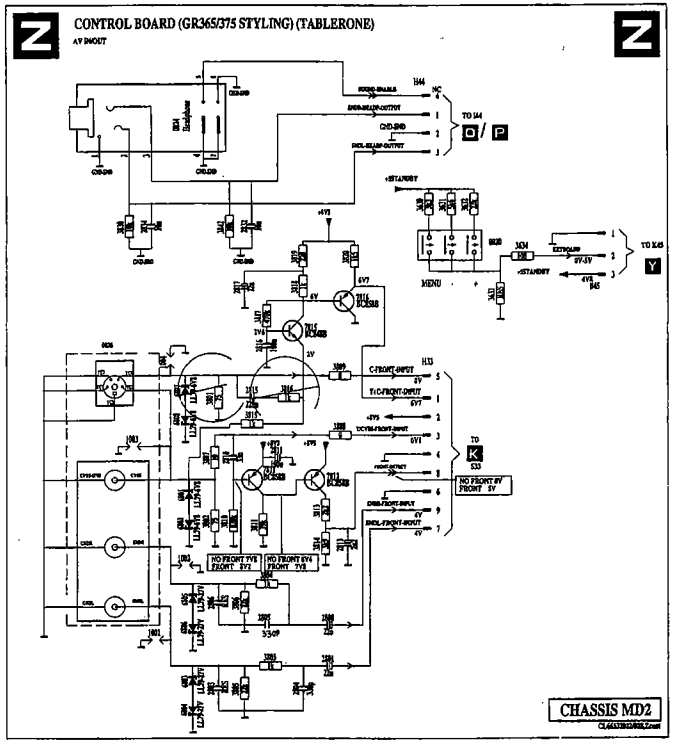


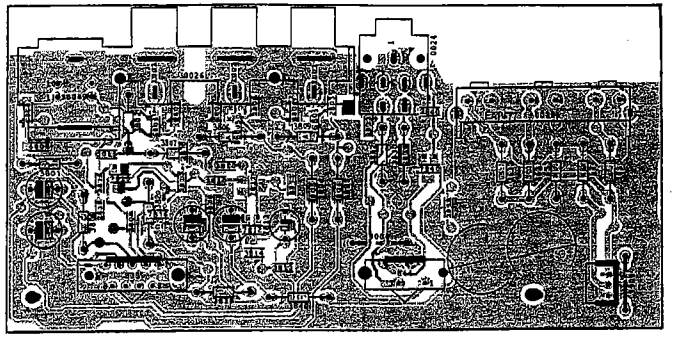
Top control panel (FL6B styling)



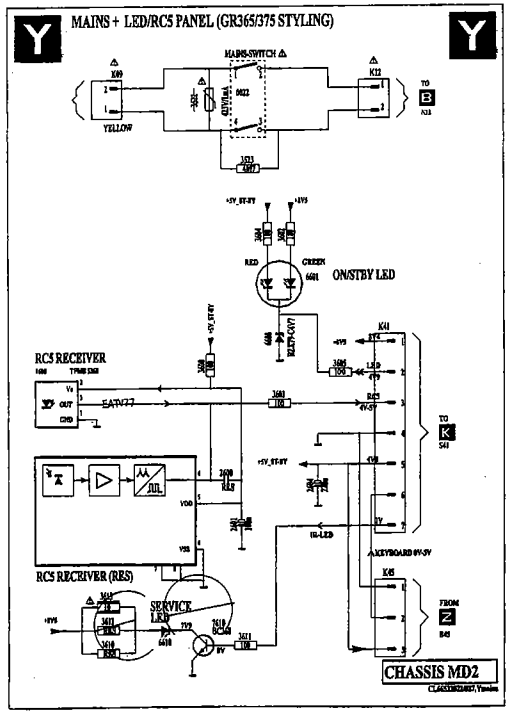


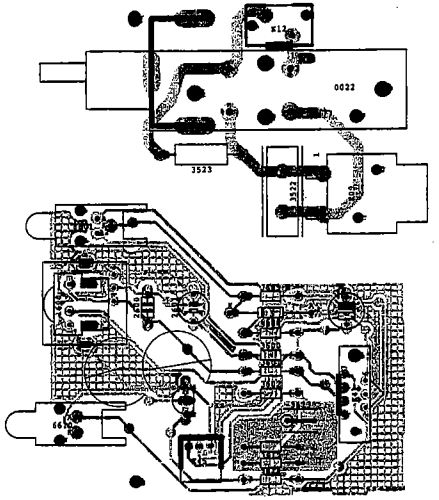
Controle panel (GR365/375 styling)



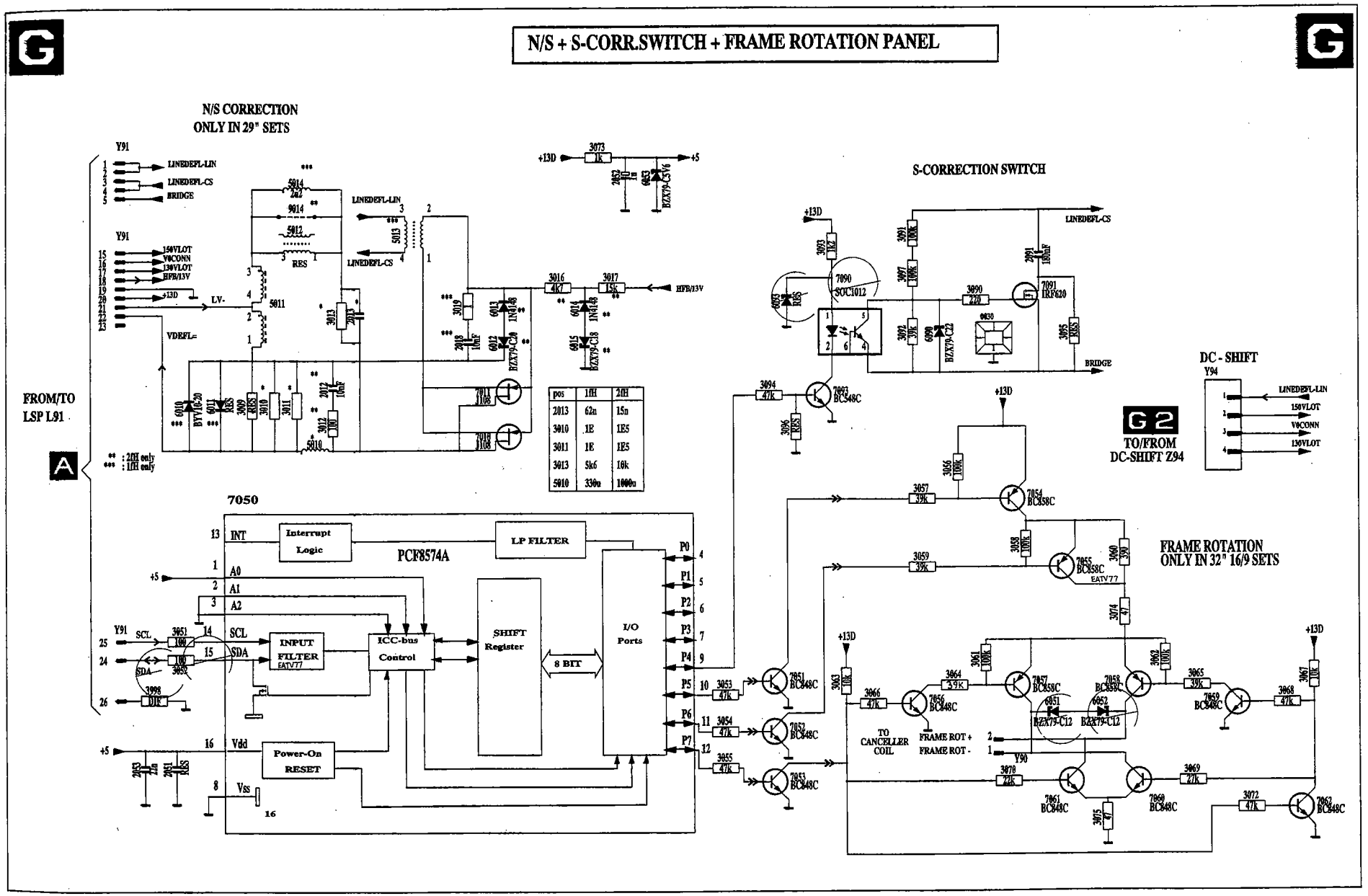


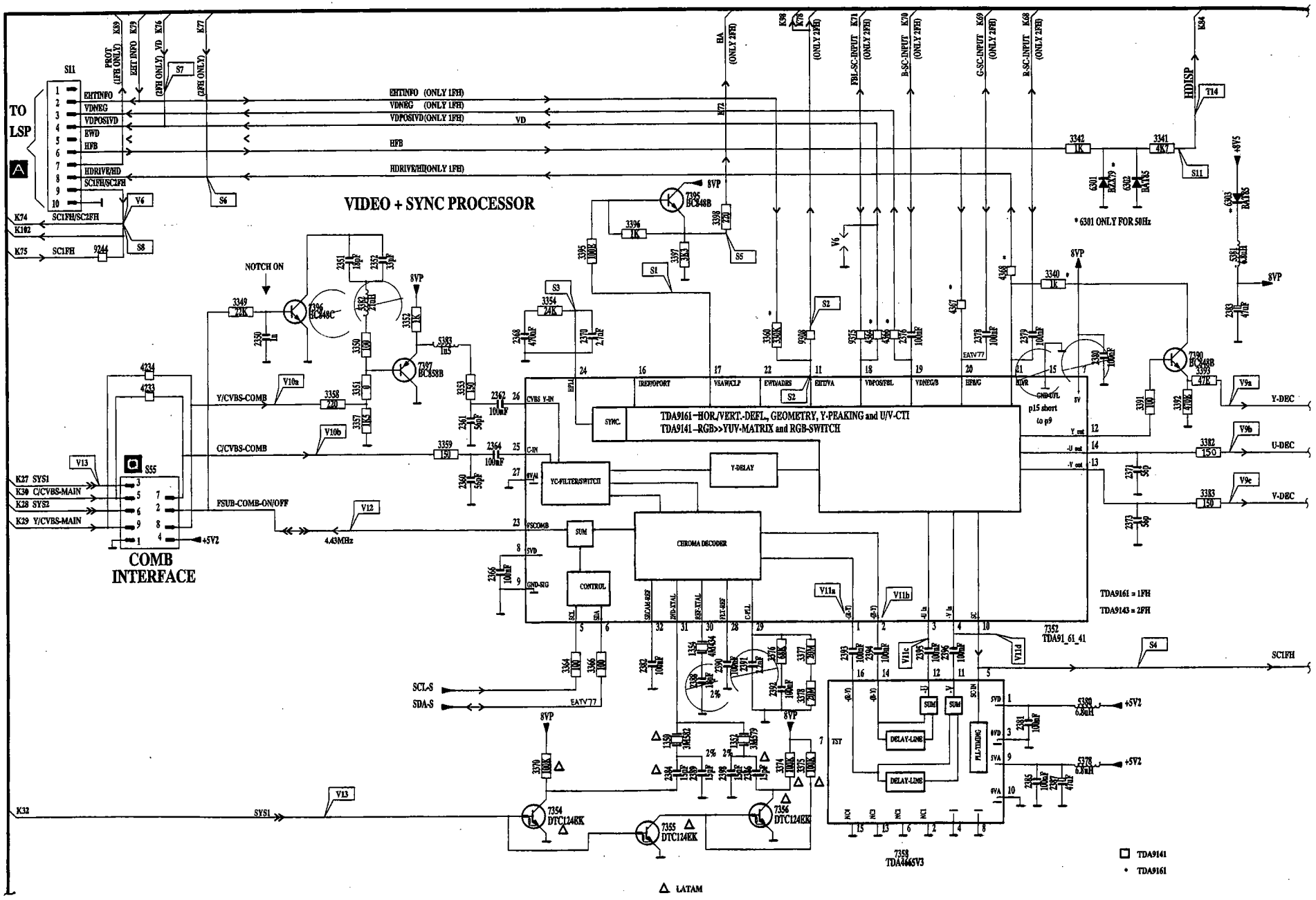
Mains input panel (GR365/375 styling)





N/S + S-CORR.SWITCH + FRAME ROTATION PANEL




VIDEO + SYNC PROCESSOR
TDA9161-HOR./VERT.-DEFL., GEOMETRY, Y-PEAKING and U/V-CTI
TDA9141-RGB>>YUV-MATRIX and RGB-SWITCH
COMB INTERFACE
TO LSP
NOTCH ON
Y/CVBS-COMB
C/CVBS-COMB
FSUB-COMB-ON/OFF
4.43MHz
SCL-S
SDA-S
SYS1
EHTINFO (ONLY 1FH)
VDNEG (ONLY 1FH)
VDPOSIVD(ONLY 1FH)
HFB
HDRIVE/HD(ONLY 1FH)
SC1FH/SC2FH
* 6301 ONLY FOR 50Hz
p15 short to p9
TDA9161 = 1FH
TDA9143 = 2FH
7352 TDA91_61_41
7358 TDA4665V3
TDA9141
TDA9161
LATAM
Y-DEC
U-DEC
V-DEC
SC1FH