

# SCHEMATIC DIAGRAM-3

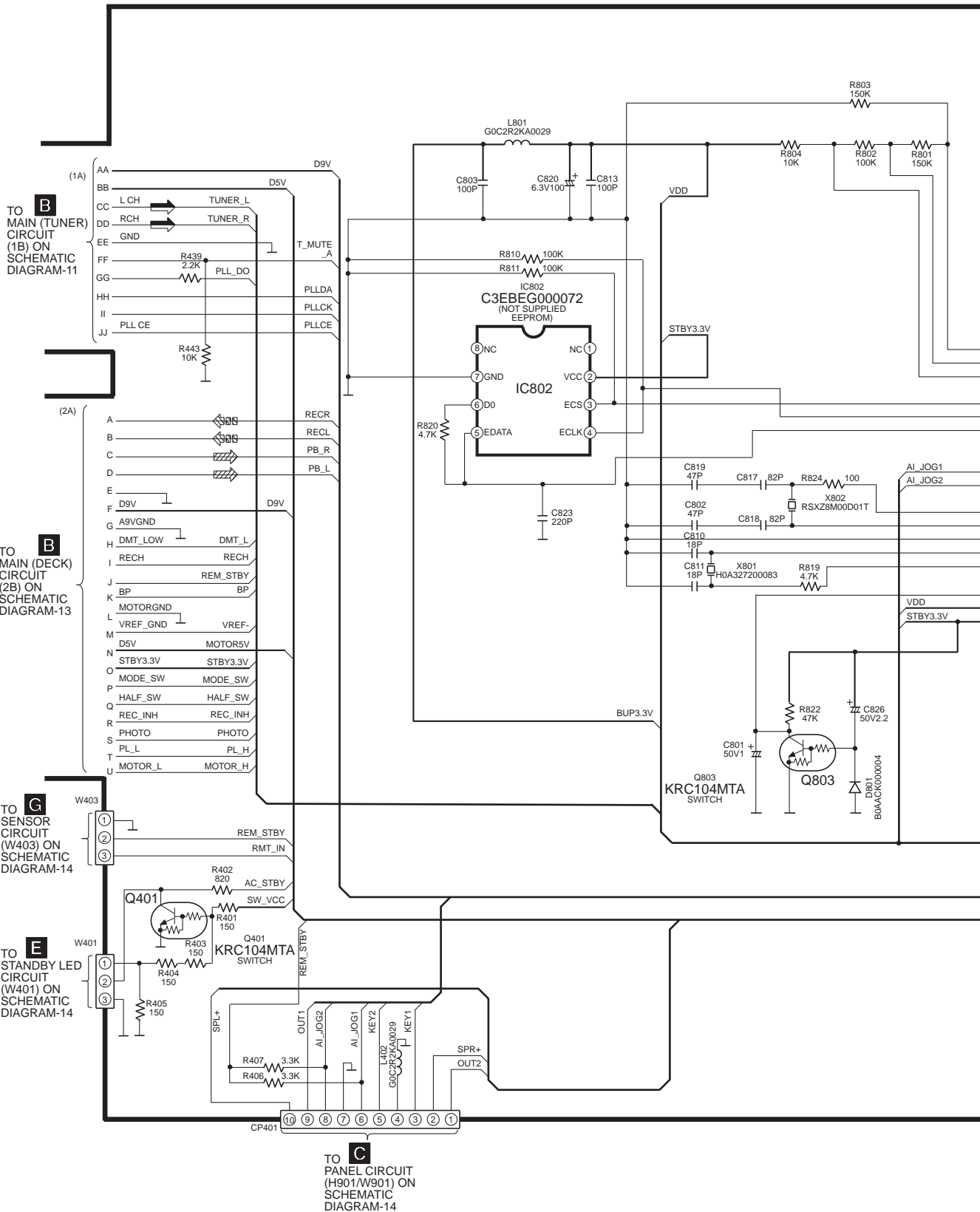
**B** MAIN CIRCUIT

— : +B SIGNAL LINE

⇒ : FM/AM SIGNAL LINE

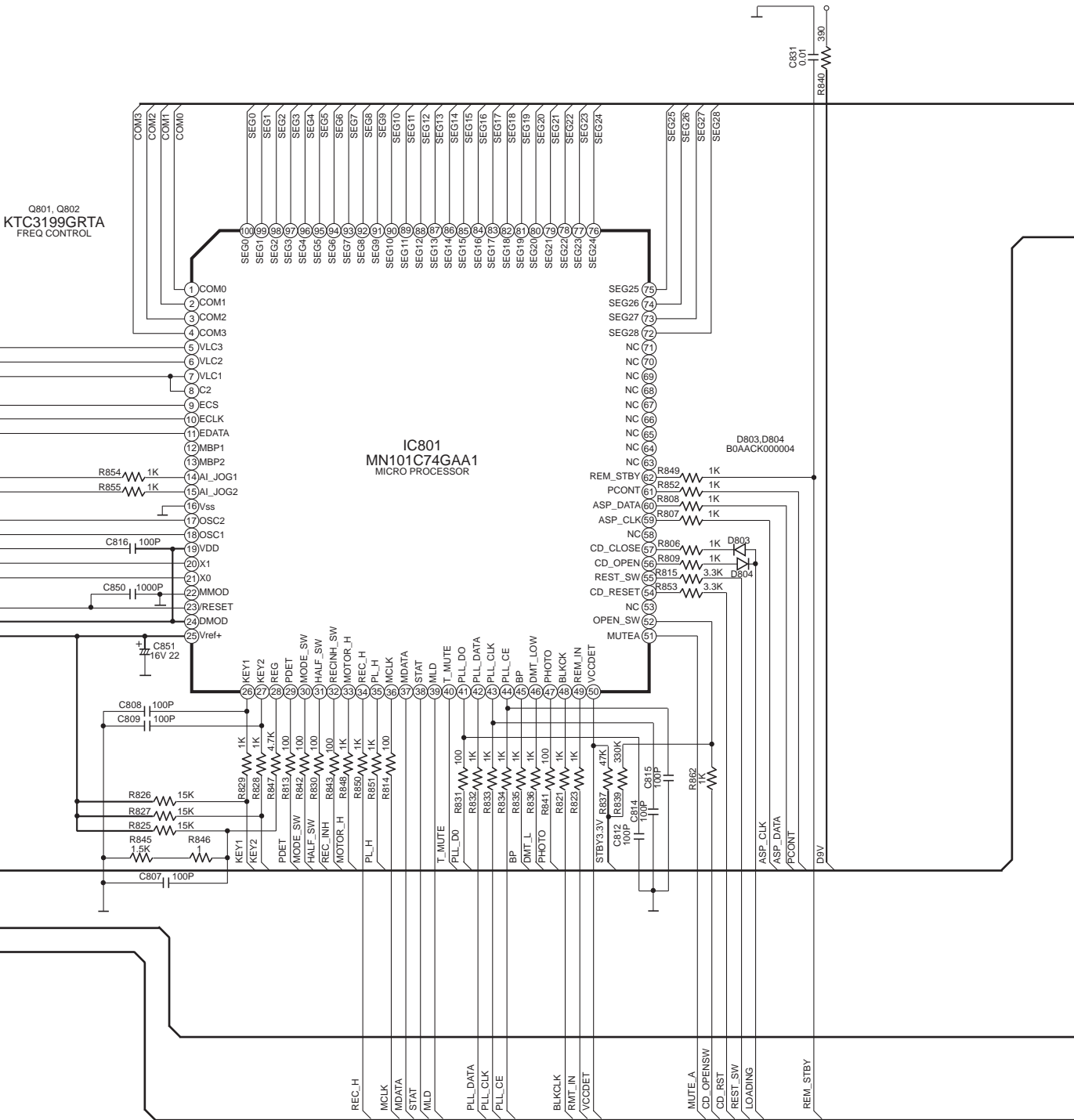
▨ : PLAYBACK SIGNAL LINE

⊘ : RECORD SIGNAL LINE




# SCHEMATIC DIAGRAM-4



**B** MAIN CIRCUIT — : +B SIGNAL LINE





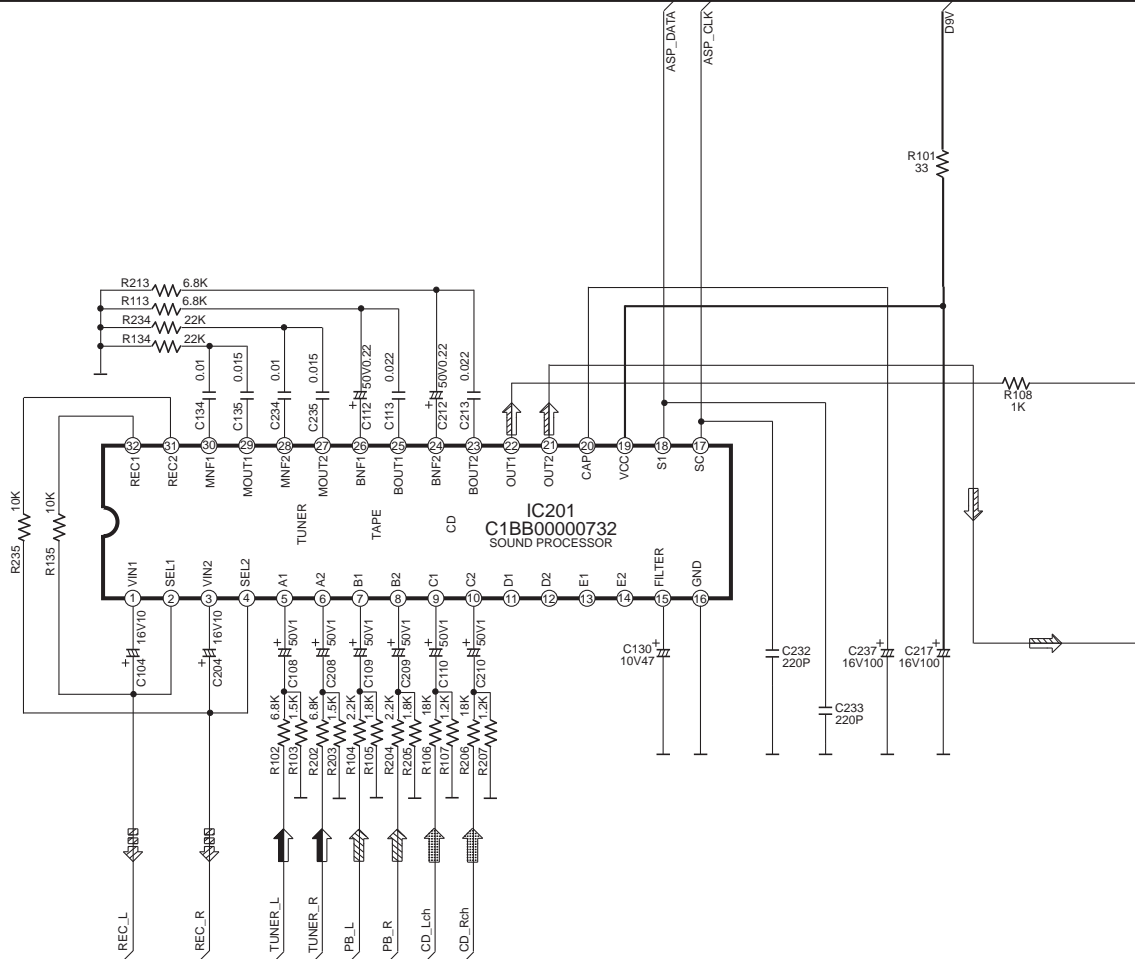
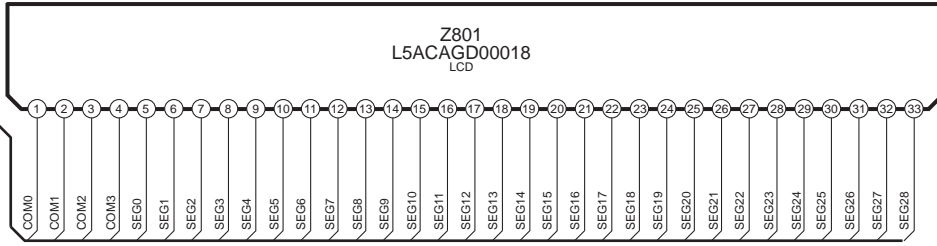
# SCHEMATIC DIAGRAM-5

## B MAIN CIRCUIT

— : +B SIGNAL LINE  
 : MAIN SIGNAL LINE

 : CD SIGNAL LINE  
 : FM/AM SIGNAL LINE


 : PLAYBACK SIGNAL LINE  
 : RECORD SIGNAL LINE

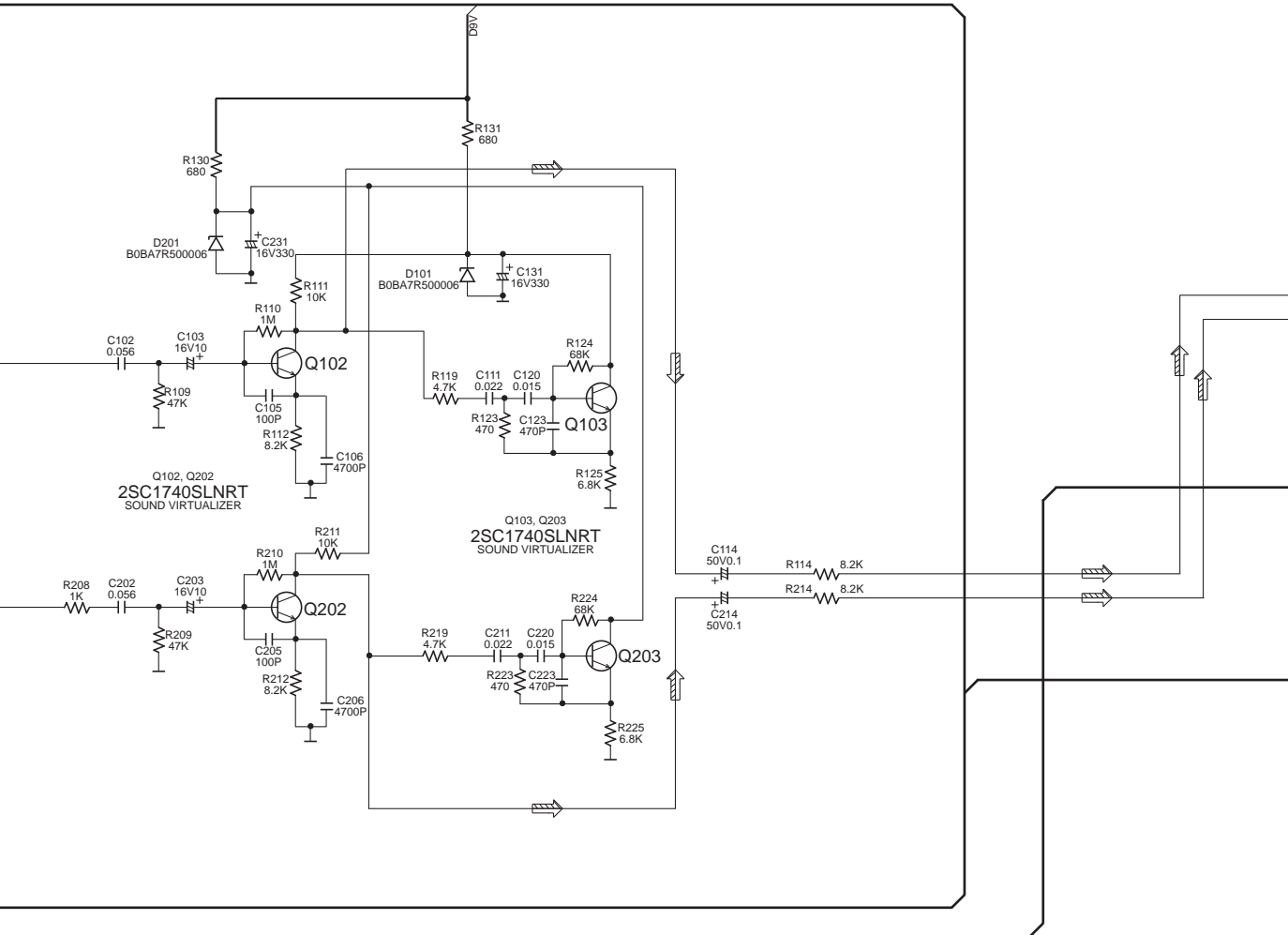


# SCHEMATIC DIAGRAM-6

**B**

MAIN CIRCUIT

— : +B SIGNAL LINE  
 : MAIN SIGNAL LINE

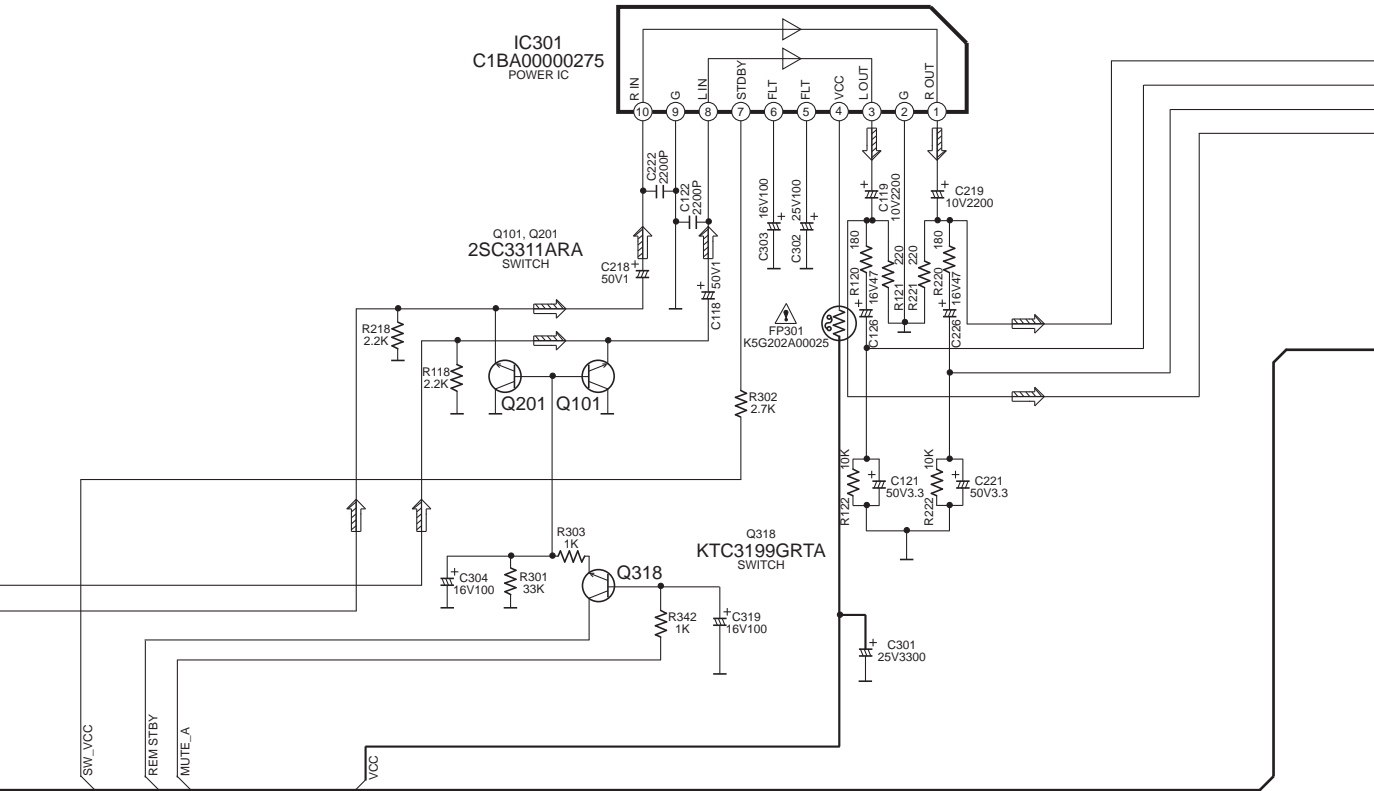


# SCHEMATIC DIAGRAM-7

**B**

MAIN CIRCUIT

— : +B SIGNAL LINE  
 ⇨ : MAIN SIGNAL LINE



# SCHEMATIC DIAGRAM-8

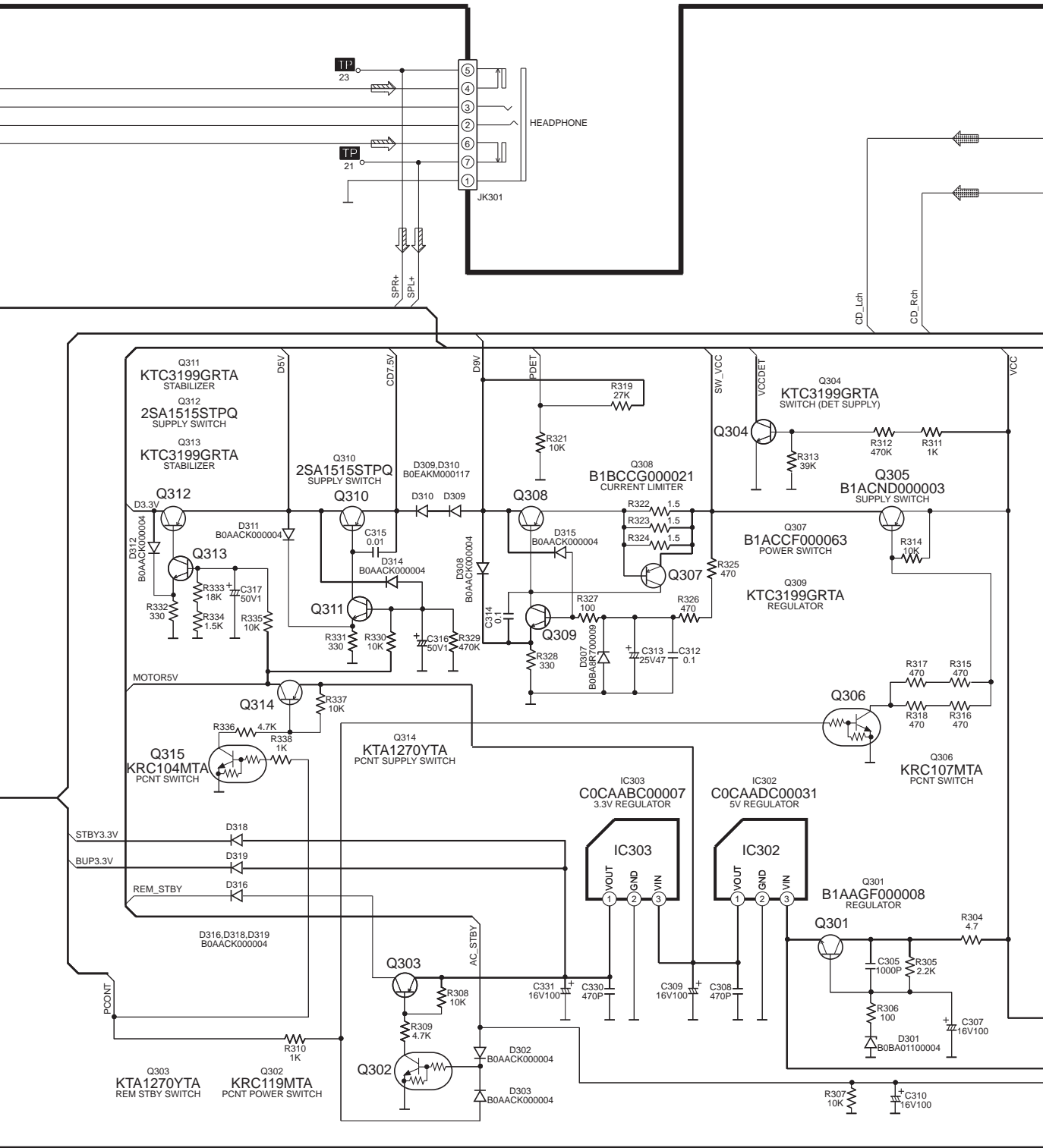
**B**

## MAIN CIRCUIT


— : +B SIGNAL LINE

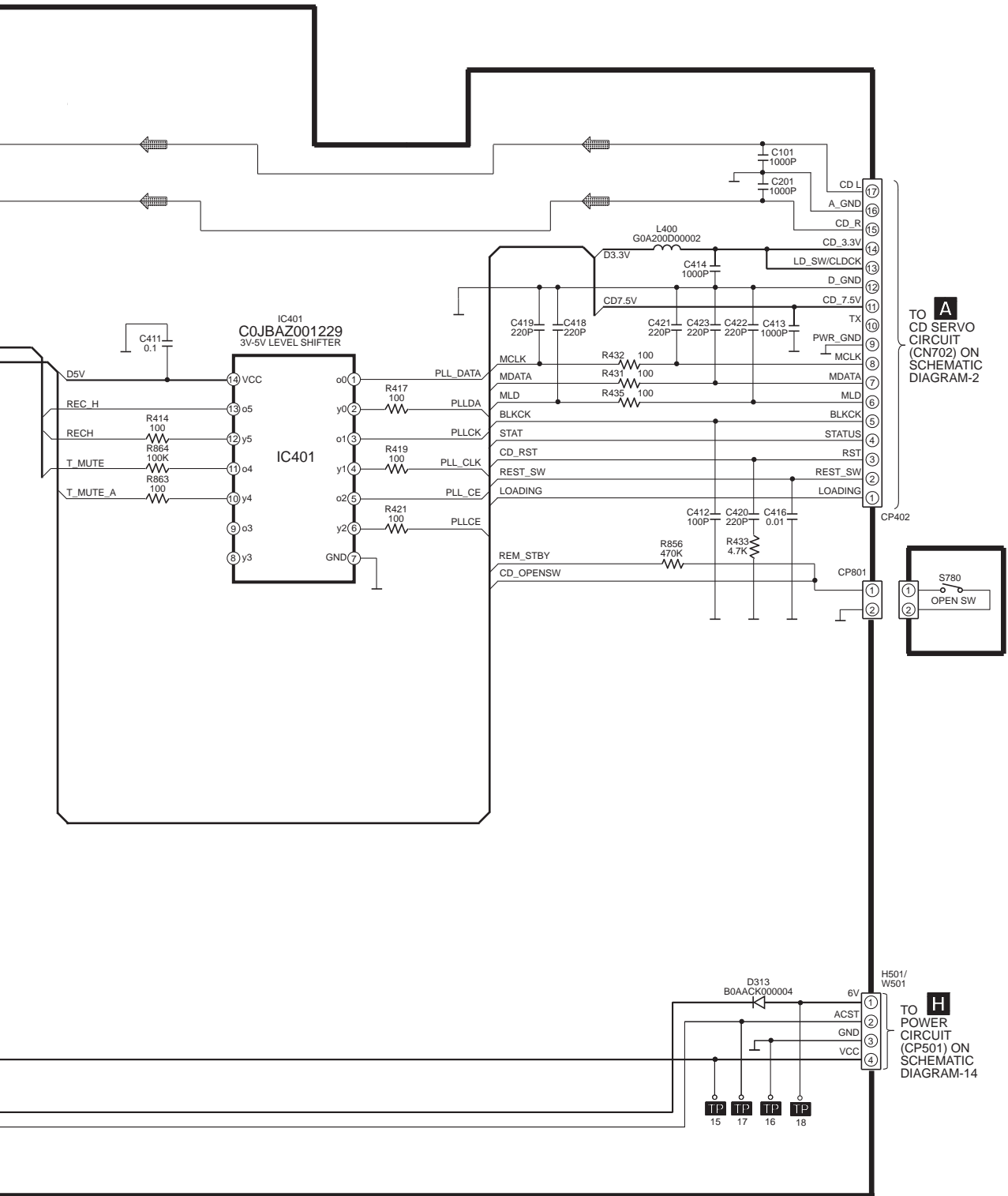
⇨ : MAIN SIGNAL LINE

⇨ : CD SIGNAL LINE

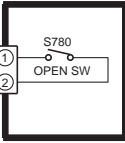


# SCHEMATIC DIAGRAM-9

**B** MAIN CIRCUIT — : +B SIGNAL LINE     : CD SIGNAL LINE



**A** TO CD SERVO CIRCUIT (CN702) ON SCHEMATIC DIAGRAM-2



**H** TO POWER CIRCUIT (CP501) ON SCHEMATIC DIAGRAM-14

